



<u>SIP</u>

Size: 1.38in x 0.65in x 0.43in (35mm x 16.45mm x 11mm)

Size: 1.38in x 0.71in x 0.43in (35mm x 18mm x 11mm)

SIP with 90° Bend

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**FEATURES** 

DESCRIPTION

- Ultra Wide Input Voltage Range of 85~305VAC/70~430VDC
- Low Power Consumption, Green Power
- High Efficiency & High Power Density
- Flexible Design of Peripheral Circuit Reduces Layout Problems
- RoHS Compliant
- Over Current and Short Circuit
  Protection
- Industrial Grade
- IEC60950, EN60950, UL60950, UL, CE, and CB Approvals

This PSLS03 series of AC/DC converters offers up to 3 watts of output power in either a SIP model or SIP model with a 90° bend. This series consists of single output models with an ultra-wide input voltage range of 85-305VAC. Each model in this series has low power consumption, high efficiency and high power density, as well as over current and short circuit protection. This series has IEC60950, EN60950, UL60950, UL, CE, and CB approvals.

MODEL SELECTION TABLE							
Model Number <sup>(1)</sup>	Input Voltage Range	Nominal Output Voltage	Output Current	Ripple & Noise	Output Power	Maximum Capacitive Load	Efficiency
PSLS03-15B03S(-F)		3.3V	600mA	70mV	1.98W	820uF	65%
PSLS03-15B05S(-F)		5V	600mA	70mV	3W	680uF	70%
PSLS03-15B09S(-F)	85-305VAC	9V	333mA	50mV	3W	470uF	73%
PSLS03-15B12S(-F)	(70-430VDC)	12V	250mA	50mV	3W	470uF	74%
PSLS03-15B15S(-F)		15V	200mA	50mV	3W	330uF	75%
PSLS03-15B24S(-F)		24V	125mA	50mV	3W	100uF	77%

## SPECIFICATIONS All specifications are based

All specifications are based on 25°C, Nominal Input Voltage (115V and 230V), <75% Humidity and Rated Output Load unless otherwise noted. We reserve the right to change specifications based on technological advances.

SPECIFICATION	TEST	Min	Тур	Max	Unit		
INPUT SPECIFICATIONS							
Input Voltago Bango	AC Input	85		305	VAC		
Input voltage Range	DC Input	70		430	VDC		
Input Frequency			47		63	Hz	
Input Current	@115VAC			0.12			
input Current	@277VAC				0.06	A	
Invision Current	@115VAC			13		_	
Infusti Current	@277VAC		23		A		
Recommended External Input Fuse			1	A, slow fusir	ng, necessa	iry	
OUTPUT SPECIFICATIONS							
Output Voltage				See	Table		
Voltago Accuracy <sup>(2)</sup>	3.3V Model			±6	9/		
	5-24V Models			±5	70		
Line Regulation	Full Lood	3.3V Model		±2.5		%	
	Full Load	5-24V Models		±1.5			
Load Regulation	on 10%-100% Load					%	
Output Power See Table							
Output Current				See Table			
Min. Load			10			%	
Maximum Capacitive Load See Table							
Ripple & Noise <sup>(3)</sup>	Noise <sup>(3)</sup> 20MHz bandwidth (peak to peak value)			80	150	mV	
Stand-By Power Consumption	Power Consumption 230VAC Input			0.15	0.25	W	
Temperature Coefficient				±0.15		%/°C	
PROTECTION							
Short Circuit Protection		C	Continuous, Self-Recovery				
/er Current Protection Self-Recovery			110		500	%lo	
ENVIRONMENTAL SPECIFICATIONS							
Operating Temperature			-40		+85	°C	
Storage Temperature					+105	°C	
Storage Humidity					85	%RH	
Power Derating	-40 ~ -20°C (85-110VAC)	2.0			%/°C		
	+70~85°C	2.67			707 C		
MTBF MIL-HDBK-217F@25°C			300,000			Hours	



#### **SPECIFICATIONS**

All specifications are based on 25°C, Nominal Input Voltage (115V and 230V), <75% Humidity and Rated Output Load unless otherwise noted.

Rev E

we reserve the right to change specifications based on technological advances.								
SPECIFICATION	T	EST CONDITIONS	Min	Тур	Max	Unit		
GENERAL SPECIFICATIONS								
Efficiency			See Table					
Isolation Voltage	Input-Output, 1 minute Te	est Time	3000			VAC		
Switching Frequency					65	kHz		
PHYSICAL SPECIFICATIONS								
Weight				0.21c	oz (6g)			
	SIP Model		1.38in x 0.65in x 0.43in (35mm x 16.45mm x 11mm)					
	SIP Model with 90° Bend		1.38in x 0.71in x 0.43in (35mm x 18mm x 11mm)					
Cooling			Free Convection					
SAFETY CHARACTERISTICS								
Safety Standards & Certifications IEC60950, EN60950, UL60950 <sup>(11)</sup>								
Safety Class	Class II							
	CE	CISPR32/EN55032 <sup>(4)</sup> CISPR32/EN55032 <sup>(5)</sup>				Class A Class B		
EMI	RE	CISPR32/EN55032 <sup>(4)</sup> CISPR32/EN55032 <sup>(5)</sup>				Class A Class B		
ESD	IEC/EN61000-4-2	±4kV			Perf	. Criteria B		
RS	IEC/EN61000-4-3	10V/m <sup>(5)</sup>			Perf	. Criteria A		
FFT	IEC/EN61000-4-4	±2kV <sup>(4)</sup>			Perf	. Criteria B		
EFI	IEC/EN61000-4-4	±4kV <sup>(5)</sup>			Perf	. Criteria B		
Surgo	IEC/EN61000-4-5	Line to line ±1kV <sup>(4)</sup>			Perf	. Criteria B		
Suige	IEC/EN61000-4-5	Line to line ±1kV/line to ground ±2kV <sup>(5)</sup>			Perf	. Criteria B		
CS	IEC/EN61000-4-6	10Vr.m.s <sup>(5)</sup>			Perf	. Criteria A		
Voltage Dips, Short Interruptions and Voltage Variations Immunity	IEC/EN61000-4-11	0%, 70% <sup>(5)</sup>			Perf	. Criteria B		

#### NOTES

- 1. Add -F to model name to indicate 90° corner model.
- 2. When 3.3V/5V/9V/12V models are working in -20-40°C temperature range output filter capacitor C2 needs 270µF/16V solid-state capacitor.
- 3. Ripple & Noise are measured by "parallel cable" method.
- 4. See Fig. 1 for typical application circuit.
- 5. See Fig. 2 for recommended circuit.
- 6. External electrolytic capacitors are required to use modules.
- 7. This part is open frame, at least 6.4mm safety distance between the primary and secondary external components of the module is needed to meet safety requirement.
- 8. In order to increase the conversion efficiency of the product with light load in the design, the product will have audio noise when it is operating, but it will not affect the product's reliability and performance.
- 9. Module requires dispensing fixed after assembly.
- 10. Product customization available.
- 11. This product is Listed to applicable standards and requirements by UL.

Due to advances in technology, specifications subject to change without notice.



DERATING CURVES -



1. Input voltage should be derated based on temperature derating when it is 85-110VAC/277~305VAC/70~130VDC/400~430VDC 2. This product is suitable for use in natureal air cooling environments, if in a closed environment, please contact factory.

EFFICIENCY GRAPHS





#### Rev E

#### MECHANICAL DRAWINGS





## DESIGN REFERENCE



Model	FUSE (Necessary)	C1 (Necessary)	L2	NTC	C2 (Necessary)	L1 (Necessary)	C3 (Necessary)	C4	CY0	TVS
PSLS03-15B03S(-F) PSLS03-15B05S(-F)		10µF/400V			270µF16V		120µF/25V	-		SMBJ7.0A
PSLS03-15B09S(-F) PSLS03-15B12S(-F)	1A/300V	(-20 to +85°C) 22µF/450V	4.7mH	13D-5	Capacitor) 4.7	4.7µH	68µF/35V	0.1µF/ 50V	1nF/400 VAC	SMBJ12A
PSLS03-15B15S(-F) PSLS03-15B24S(-F)		(-40 to +85°C)			470µF/35V 220µF/35V		47µF/35V			SMBJ20A SMBJ30A

## Note:

C1: AC Input, C1 is input filer capacitor (required)

DC Input is a filtering capacitor in EMC filter (required)

R1: Limit current resistance, the value of R1 is  $12\Omega$ , 2W; if the capacitance value of C1>22µF, you cannot take.

C2 and C3 are output flier capacitors (required), C2, C3 and L1 form a pi-type filter circuit, they are recommended to be high frequency and low impedance electrolytic capacitors. Capacitance and rated ripple current of capacitors refer to the data sheets provided by factory. Capacitor voltage reduced to at least 80%. C4 is a ceramic capacitor, which is used to filter high frequency noise. Current of L1 and L2 refer to the data sheets provided by factory. Current rating should be 80%. TVS is a recommended component to protect post-circuit (if converter fails). External input NTC model is recommended to use 13D-5. External input MOV model is recommended to us S14K350.



## COMPANY INFORMATION

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