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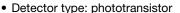
## Vishay Semiconductors

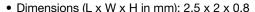
## **Reflective Optical Sensor With Transistor Output**

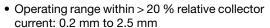


### **FEATURES**

Package type: SMD







• Emitter wavelength: 940 nm

• Moisture sensitivity level (MSL): 4

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>



# (e4)

RoHS

COMPLIANT HALOGEN FREE

### **DESCRIPTION**

The VCNT2020 is a reflective sensor in a miniature SMD package. It has a compact construction where the emitting light source and the detector are arranged in the same plane. The operating infrared wavelength is 940 nm. The detector consists of a silicon phototransistor. The sensor analog output signal (photo current) is triggered by detection of reflected infrared light from a close by object.

The sensor has a built in daylight blocking filter, which greatly suppresses disturbing ambient light and therefore increases signal to noise ratio.

#### **APPLICATIONS**

- Position sensor
- · Optical switch
- Optical encoder (e.g. disc and tape drives for DVD and / or camera applications)
- Object detection (e.g. paper presence in printer and copy machines)

PRODUCT SUMMARY						
PART NUMBER	TARGET MATERIAL	DISTANCE RANGE FOR RELATIVE I <sub>OUT</sub> > 0.5 mA WITH I <sub>Fmax.</sub> (mm)	TYPICAL CTR <sup>(1)</sup> (%)	DISTANCE OF PEAK SENSITIVITY (mm)	DAYLIGHT BLOCKING FILTER INTEGRATED	
VCNT2020	Kodak Gray Card, gray side (18 %)	0 to 2	1.3	0.5	Yes	
VCNT2020	Kodak Gray Card, white side (90 %)	0 to 7	12	0.5	Yes	

#### Note

(1) CTR: current transfer ratio, Iout/Iin

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	VOLUME (1)	REMARKS		
VCNT2020	Tape and reel	MOQ: 3000 pcs	Drypack, MSL 4		

#### Note

(1) MOQ: minimum order quantity



<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT (EMITTER)							
Reverse voltage		$V_{R}$	5	V			
Forward current		I <sub>F</sub>	100	mA			
Forward surge current	t <sub>p</sub> ≤ 100 μs	I <sub>FSM</sub>	500	mA			
OUTPUT (DETECTOR)							
Collector emitter breakdown voltage		V <sub>(BR)CEO</sub>	20	V			
Emitter collector voltage		V <sub>ECO</sub>	7	V			
Collector current		I <sub>C</sub>	20	mA			
SENSOR							
Total power dissipation	T <sub>amb</sub> ≤ 25 °C	P <sub>tot</sub>	170	mW			
Ambient temperature range		T <sub>amb</sub>	-25 to +85	°C			
Storage temperature range		T <sub>stg</sub>	-25 to +85	°C			
Soldering temperature	In accordance with Fig. 11	T <sub>sd</sub>	260	°C			

### **ABSOLUTE MAXIMUM RATINGS**

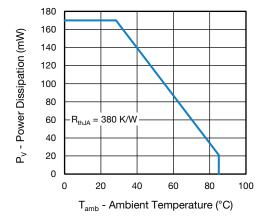


Fig. 1 - Power Dissipation vs. Ambient Temperature

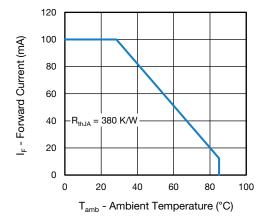


Fig. 2 - Forward Current vs. Ambient Temperature

<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT (EMITTER)							
Forward voltage	I <sub>F</sub> = 20 mA	V <sub>F</sub>	-	1.25	1.4	V	
i oi ward voitage	I <sub>F</sub> = 100 mA		-	1.5	1.7		
Temperature coefficient of V <sub>F</sub>	$I_F = 20 \text{ mA}$	TKV <sub>F</sub>	=	-1.0	-	mV/K	
Peak wavelength	$I_F = 100 \text{ mA}$	$\lambda_{P}$	=	940	-	nm	
Reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	=	-	10	μΑ	
OUTPUT (DETECTOR)							
Collector emitter breakdown voltage	$I_C = 0.1 \text{ mA, E} = 0$	V <sub>(BR)CEO</sub>	20	-	-	V	
Emitter collector voltage	I <sub>E</sub> = 100 μA, E = 0	V <sub>ECO</sub>	7	-	-	V	
Collector emitter dark current	$V_{CE} = 5 V, E = 0$	I <sub>CEO</sub>	-	1	100	nA	
SENSOR							
Collector current	$V_{CE} = 5 \text{ V}, I_F = 20 \text{ mA},$ d = 1 mm (flat mirror)	Ic	0.8	1.8	2.7	mA	
Collector current	$V_{CE} = 5 \text{ V}, I_F = 20 \text{ mA}, d = 1 \text{ mm}$ (Kodak gray card, 18 %)	Ic	-	0.25	-	mA	
Current transfer ratio	I <sub>C</sub> /I <sub>F</sub> , V <sub>CE</sub> = 5 V, d = 1 mm (Kodak gray card, 18 %)	CTR	-	1.25	-	%	
Rise time	$I_C$ = 0.8 mA, $V_{CE}$ = 5 V, $R_L$ = 100 $\Omega$	t <sub>r</sub>	-	10	70	μs	
Fall time	$I_C$ = 0.8 mA, $V_{CE}$ = 5 V, $R_L$ = 100 $\Omega$	t <sub>f</sub>	=	15	70	μs	

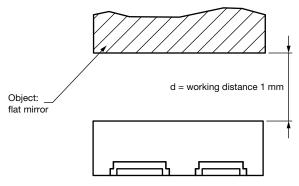


Fig. 3 - Test Circuit

## **BASIC CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

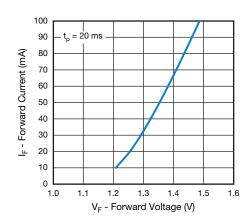


Fig. 4 - Forward Current vs. Forward Voltage

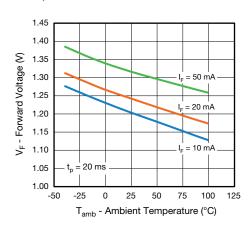


Fig. 5 - Forward Voltage vs. Ambient Temperature



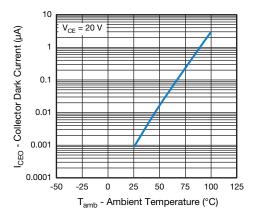


Fig. 6 - Collector Dark Current vs. Ambient Temperature

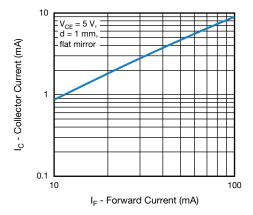


Fig. 7 - Collector Current vs. Forward Current

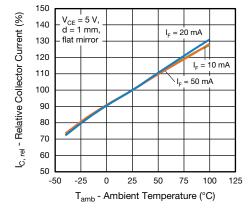


Fig. 8 - Relative Collector Current vs. Ambient Temperature

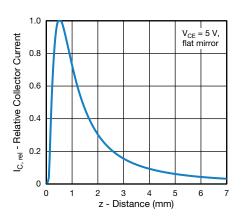


Fig. 9 - Relative Collector Current vs. Distance

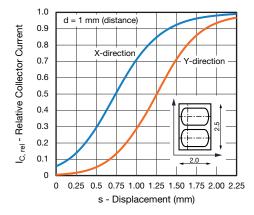


Fig. 10 - Relative Collector Current vs. Displacement

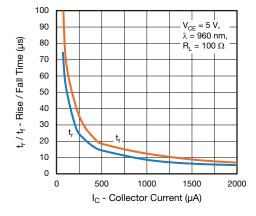


Fig. 11 - Rise / Fall Time vs. Collector Current



### **FLOOR LIFE**

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

Moisture sensitivity: level 4

Floor life: 72 h

Conditions: T<sub>amb</sub> < 30 °C, RH < 60 %

#### **DRYING**

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or recommended conditions:

192 h at 40 °C (+ 5 °C), RH < 5 %

or

96 h at 60 °C (+ 5 °C), RH < 5 %

#### **REFLOW SOLDER PROFILE**

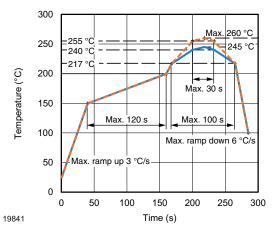
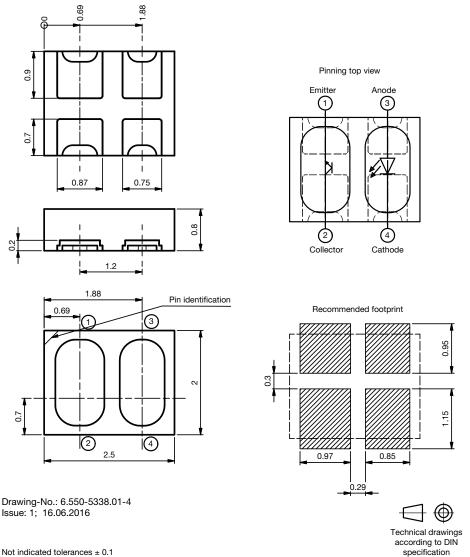


Fig. 12 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

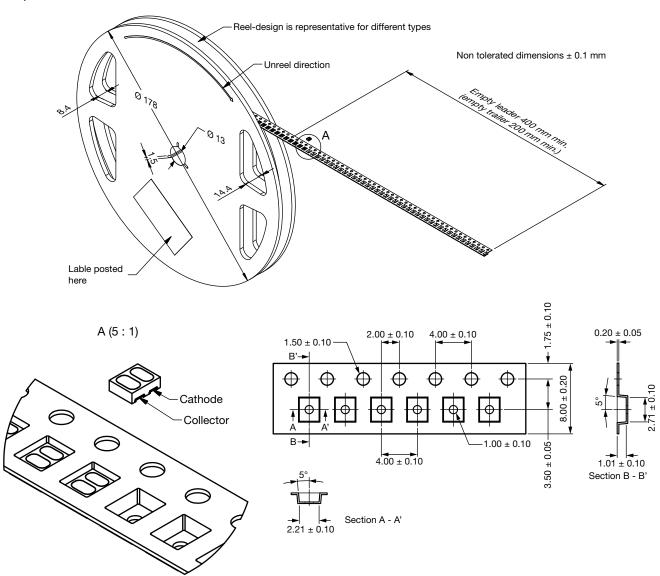
## **PACKAGE DIMENSIONS** in millimeters





## TAPE AND REEL DIMENSIONS in millimeters

3000 pcs/reel





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Vishay

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