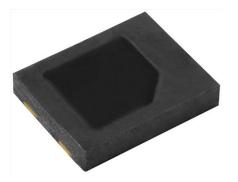
# VEMD5110X01

## **Vishay Semiconductors**



# Silicon PIN Photodiode



### DESCRIPTION

VEMD5110X01 is a high speed and high sensitive PIN photodiode. It is a low profile surface mount device (SMD) including the chip with a 7.5 mm<sup>2</sup> sensitive area and a daylight blocking filter matched with IR emitters operating at wavelength 870 nm or 950 nm.

### **FEATURES**

- Package type: surface-mount
- · Package form: top view
- Dimensions (L x W x H in mm): 5 x 4 x 0.9
- Radiant sensitive area (in mm<sup>2</sup>): 7.5
- AEC-Q101 qualified
- · High sensitivity
- Daylight blocking filter matched with 870 nm to 950 nm emitters
- Fast response times
- Angle of half sensitivity:  $\phi = \pm 65^{\circ}$
- Floor life: 72 h, MSL 4, according to J-STD-020
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

- · High speed detector for infrared radiation
- · Infrared remote control and free air data transmission systems, e.g. road cash systems
- Photodiode for smoke detectors
- · Photodiode for rain sensors

PRODUCT SUMMARY				
COMPONENT	I <sub>ra</sub> (μΑ)	φ (°)	λ <sub>0.5</sub> (nm)	
VEMD5110X01	48	± 65	790 to 1050	

#### Note

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VEMD5110X01	Tape and reel	MOQ: 1000 pcs, 1000 pcs/reel	Top view		
VEMD5110X01-GS15	Tape and reel	MOQ: 5000 pcs, 5000 pcs/reel	Top view		

#### Note

· MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	20	V	
Power dissipation	T <sub>amb</sub> ≤ 25 °C	Pv	215	mW	
Junction temperature		Тj	110	°C	
Operating temperature range		T <sub>amb</sub>	-40 to +110	°C	
Storage temperature range		T <sub>stg</sub>	-40 to +110	°C	
Soldering temperature	According to reflow sloder profile Fig. 8	T <sub>sd</sub>	260	°C	
Thermal resistance junction to ambient		R <sub>thJA</sub>	350	K/W	
ESD safety HBM	± 2000 V, 1.5 kΩ, 100 pF, 3 pulses	ESD <sub>HBM</sub>	≥2	kV	



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<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>	-	1	1.3	V
Breakdown voltage	I <sub>R</sub> = 100 μA, E = 0	V <sub>(BR)</sub>	20	-	-	V
Reverse dark current	$V_{R} = 10 V, E = 0$	I <sub>ro</sub>	-	2	30	nA
Diode capacitance	$V_{R} = 0 V, f = 1 MHz, E = 0$	CD	-	70	-	pF
	$V_{R} = 3 V, f = 1 MHz, E = 0$	CD	-	25	40	pF
Open circuit voltage	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	Vo	-	350	-	mV
Temperature coefficient of Vo	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	TK <sub>Vo</sub>	-	-2.6	-	mV/K
Short circuit current	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	l <sub>k</sub>	-	45	-	μA
Temperature coefficient of $I_k$	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	TK <sub>lk</sub>	-	0.1	-	%/K
Reverse light current	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm},$ $V_R = 5 \text{ V}$	I <sub>ra</sub>	40	48	-	μA
Angle of half sensitivity		φ	-	± 65	-	o
Wavelength of peak sensitivity		λρ	-	940	-	nm
Range of spectral bandwidth		λ <sub>0.5</sub>	-	790 to 1050	-	nm
Noise equivalent power	$V_R = 10 \text{ V}, \lambda = 950 \text{ nm}$	NEP	-	4 x 10 <sup>-14</sup>	-	W/√Hz
Rise time	$V_R = 10 \text{ V}, \text{ R}_L = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$	t <sub>r</sub>	-	100	-	ns
Fall time	$V_R = 10 \text{ V}, \text{ R}_L = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$	t <sub>f</sub>	-	100	-	ns

### BASIC CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

Basic characteristics graphs to be extended to 110 °C ambient temperatures where applicable.

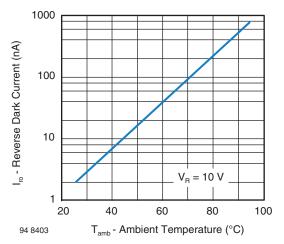


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

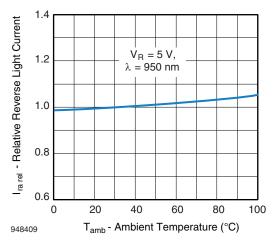
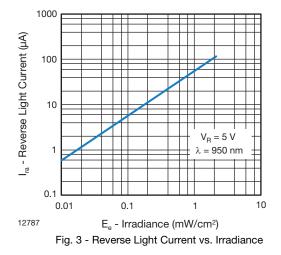


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature



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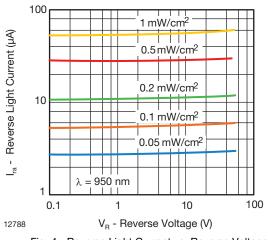


Fig. 4 - Reverse Light Current vs. Reverse Voltage

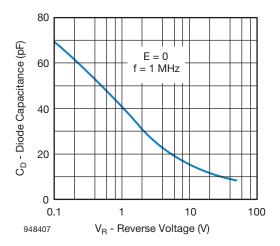


Fig. 5 - Diode Capacitance vs. Reverse Voltage

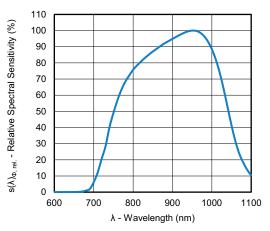


Fig. 6 - Relative Spectral Sensitivity vs. Wavelength

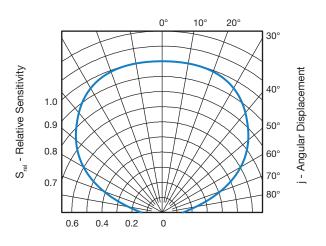


Fig. 7 - Relative Sensitivity vs. Angular Displacement

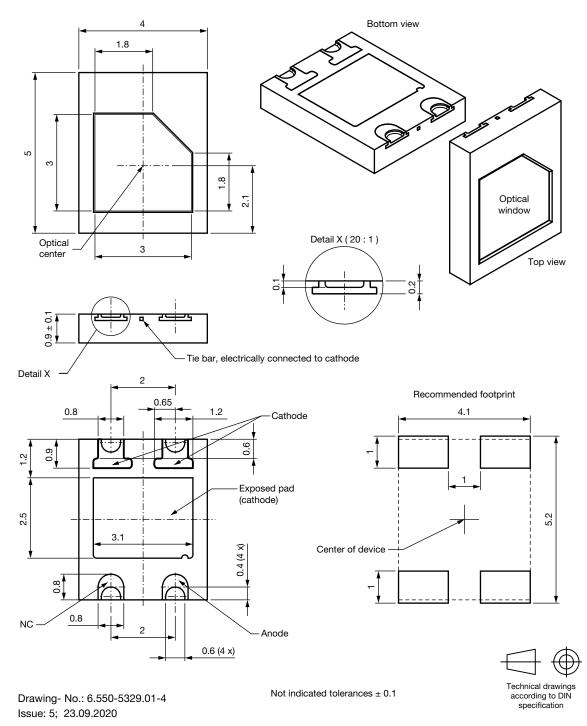
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## VEMD5110X01



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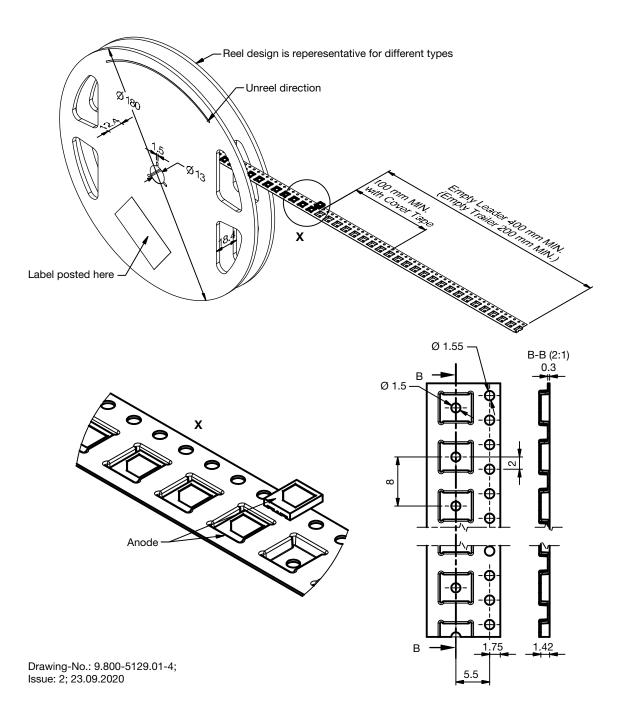
### **PACKAGE DIMENSIONS** in millimeters





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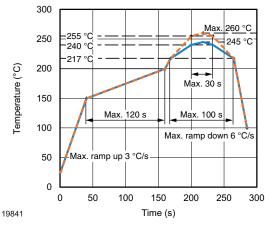
### TAPE AND REEL DIMENSIONS in millimeters



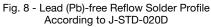
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### SOLDER PROFILE



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### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

### **FLOOR LIFE**

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

Moisture sensitivity: Level 4

Floor life: 72 h

Conditions:  $T_{amb} < 30\ ^\circ C,\ RH < 60\ \%$ 

#### DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or recommended conditions:

192 h at 40 °C (+ 5 °C), RH < 5 % or 96 h at 60 °C (+ 5 °C), RH < 5 %



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