# **TEMD7100ITX01**

### **Vishay Semiconductors**



## Silicon PIN Photodiode



#### DESCRIPTION

TEMD7100ITX01 is a high speed and high sensitive PIN photodiode. It is a miniature surface mount device (SMD) including the chip with a 0.23 mm<sup>2</sup> sensitive area and a daylight blocking filter matched with IR emitters operating at wavelength of 830 nm to 950 nm.

### **FEATURES**

- Package type: surface mount
- Package form: 0805
- Dimensions (L x W x H in mm): 2 x 1.25 x 0.85
- Radiant sensitive area (in mm<sup>2</sup>): 0.23
- High radiant sensitivity
- Enhanced operating temperature range:  $T_{OP} = -40 \ ^{\circ}C \ to \ +110 \ ^{\circ}C$
- Daylight blocking filter matched with 830 nm to 950 nm emitters
- Fast response times
- Angle of half sensitivity:  $\varphi = \pm 60^{\circ}$
- Floor life: 72 h, MSL 4, according to J-STD-020
- · Lead (Pb)-free reflow soldering
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

- · High speed photo detector
- · Infrared remote control
- Infrared data transmission
- Photo interrupters
- Shaft encoders

PRODUCT SUMMARY				
COMPONENT	I <sub>ra</sub> (μΑ)	φ (deg)	λ <sub>0.5</sub> (nm)	
TEMD7100ITX01	3	± 60	750 to 1050	

#### Note

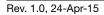
• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
TEMD7100ITX01	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	0805		

#### Note

MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V <sub>R</sub>	60	V
Power dissipation	T <sub>amb</sub> ≤ 25 °C	Pv	215	mW
Junction temperature		Тj	110	°C
Operating temperature range		T <sub>amb</sub>	-40 to +110	°C
Storage temperature range		T <sub>stg</sub>	-40 to +110	°C
Soldering temperature	Acc. reflow solder profile fig. 8	T <sub>sd</sub>	260	°C
Thermal resistance junction / ambient	Acc. J-STD-051	R <sub>thJA</sub>	270	K/W



Document Number: 84317



RoHS

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<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>		1		V
Breakdown voltage	I <sub>R</sub> = 100 μA, E = 0	V <sub>(BR)</sub>	60			V
Reverse dark current	V <sub>R</sub> = 10 V, E = 0	I <sub>ro</sub>		1	3	nA
Diode capacitance	V <sub>R</sub> = 0 V, f = 1 MHz, E = 0	CD		4		pF
	V <sub>R</sub> = 5 V, f = 1 MHz, E = 0	CD		1.3		pF
Open circuit voltage	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	Vo		350		mV
Temperature coefficient of Vo	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	TK <sub>Vo</sub>		-2.6		mV/K
Short circuit current	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	l <sub>k</sub>		3		μA
Temperature coefficient of $I_k$	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	TK <sub>lk</sub>		0.1		%/K
Reverse light current	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$ , $V_R = 5 \text{ V}$	I <sub>ra</sub>	2.4	3	3.6	μA
Angle of half sensitivity		φ		± 60		deg
Wavelength of peak sensitivity		λρ		950		nm
Range of spectral bandwidth		λ <sub>0.5</sub>		750 to 1050		nm
Rise time	$V_R$ = 10 V, $R_L$ = 1 k $\Omega$ , $\lambda$ = 820 nm	t <sub>r</sub>		100		ns
Fall time	$V_{R}$ = 10 V, $R_{L}$ = 1 k $\Omega$ , $\lambda$ = 820 nm	t <sub>f</sub>		100		ns

**BASIC CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

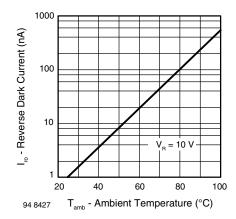


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

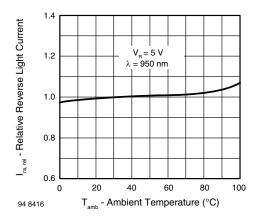


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature

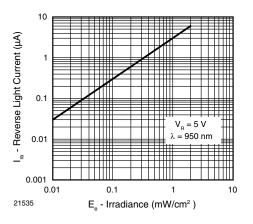


Fig. 3 - Reverse Light Current vs. Irradiance

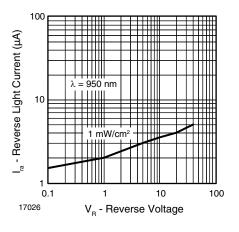


Fig. 4 - Reverse Light Current vs. Reverse Voltage

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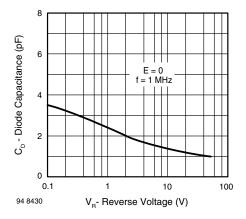


Fig. 5 - Diode Capacitance vs. Reverse Voltage

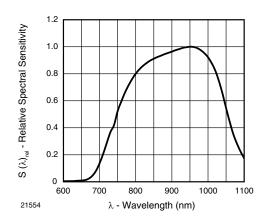


Fig. 6 - Relative Spectral Sensitivity vs. Wavelength

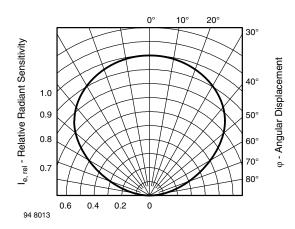


Fig. 7 - Relative Radiant Sensitivity vs. Angular Displacement

### REFLOW SOLDER PROFILE

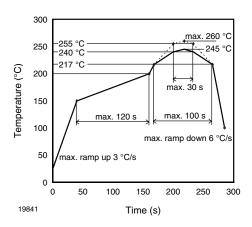


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

#### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

### **FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 72 h

Conditions:  $T_{amb}$  < 30 °C, RH < 60 %

Moisture sensitivity level 4, according to J-STD-020.

#### DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.

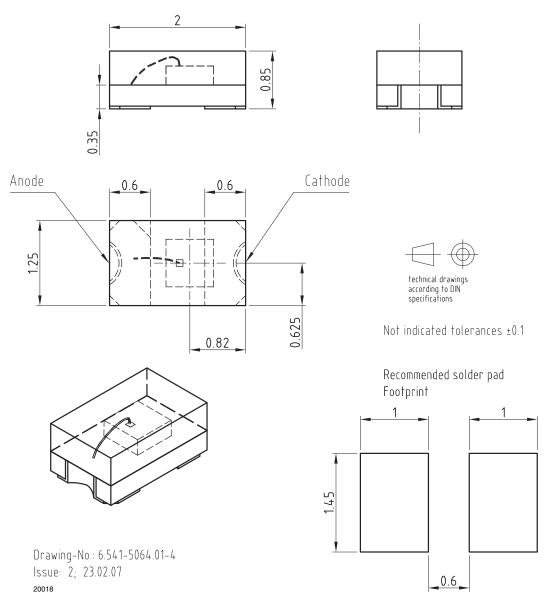
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#### **PACKAGE DIMENSIONS** in millimeters



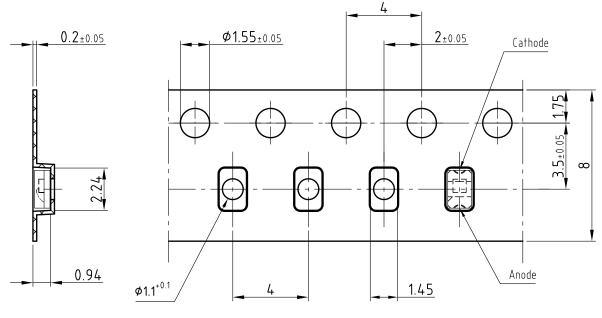
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#### **BLISTER TAPE DIMENSIONS** in millimeters



Reel off direction

rechnical drawings

technical drawings according to DIN specifications

Drawing-No.: 9.700-5311.01-4 Issue: 1; 23.02.07 21501

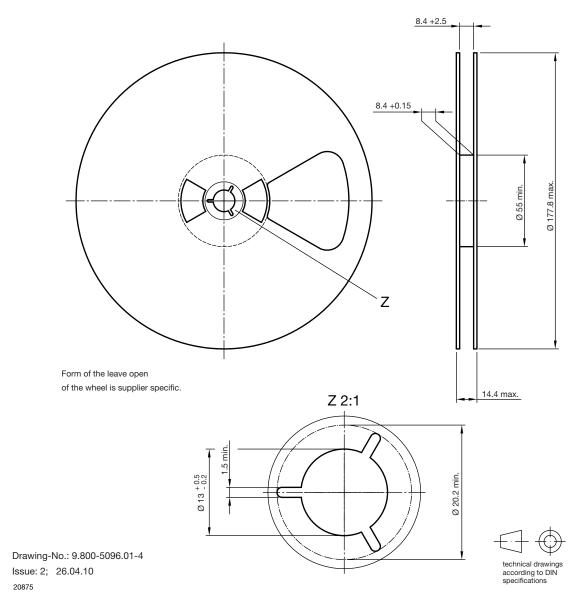
Not indicated tolerances ±0.1





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#### **REEL DIMENSIONS** in millimeters





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