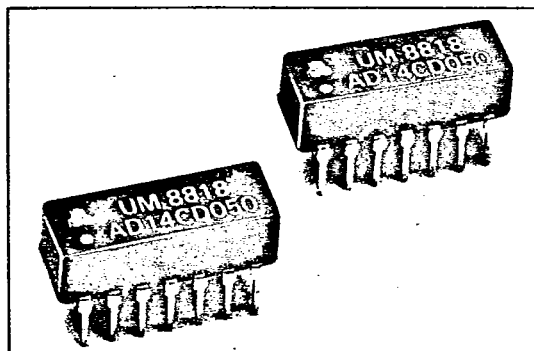


AD14CD SERIES: 14-PIN DIP 10-TAP AUTO INSERTABLE**FEATURES:**

- Compatible with TTL Circuits
- 10 Equally-Spaced Delay Taps
- Operating Temperature Range: 0°C to +70°C
- Custom Designs (Delays or Pin Layouts) Available upon Request

ELECTRICAL CHARACTERISTICS

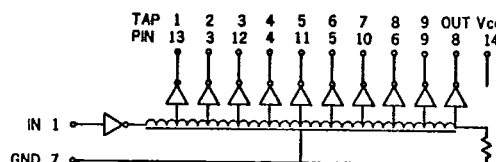
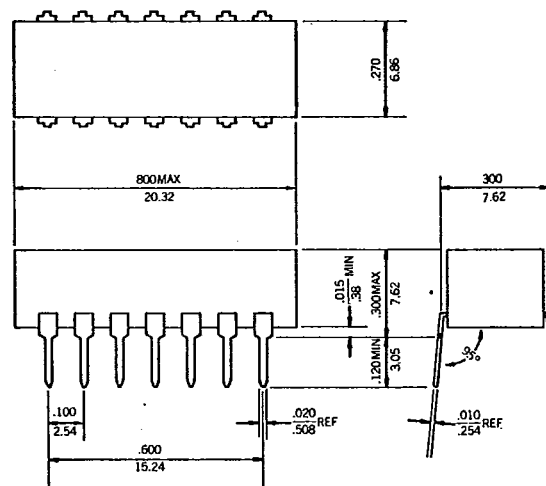
Supply Voltage V_{cc} : 5.0 ± 0.25 VDC
 Logic 1 Input Voltage: 2.0V min.
 Input Current: 50 μ A max.
 Logic 0 Input Voltage: 0.8V max.
 Input Current: -2.0mA max.
 Logic 1 Output Voltage: 2.4V min.
 Logic 0 Output Voltage: 0.5V max.
 All measurements made at $V_{cc}=5.0$ V, 25°C

FAN OUT CAPABILITIES

Logic 0 Output: 10 TTL loads/tap max.
 : 20 TTL loads/unit max.
 Logic 1 Output: 20 TTL loads/unit max.

INPUT TEST CONDITIONS

Pulse Voltage: 3.2 V
 Rise Time: 3.0 ns
 Supply Current: 120 mA typical
 Pulse Width: min. 100% of total delay
 Duty Cycle: 33% or less

CIRCUIT AND PIN CONNECTIONS:**PACKAGE DIMENSIONS:**

INCHES XXX $\pm$ 010
 MILLIMETERS .XX $\pm$.25

ELECTRICAL SPECIFICATIONS:

PART NO.	TOTAL DELAY(1) ns $\pm$ 5%	TAP DELAY(1) ns $\pm$ 10%	RISE TIME(2) ns max.
AD14CD050	50	5 $\pm$ 2	4
AD14CD100	100	10 $\pm$ 2	4
AD14CD150	150	15 $\pm$ 2	4
AD14CD200	200	20 $\pm$ 3	4
AD14CD250	250	25 $\pm$ 3	4
AD14CD300	300	30	5
AD14CD350	350	35	5
AD14CD400	400	40	5
AD14CD450	450	45	5
AD14CD500	500	50	5

(1) Delays measured at 1.5V level on leading edge only with no loads on taps.

(2) Rise Time measured from 0.75V to 2.4V with no loads.