TOSHIBA Phototransistor Silicon NPN Epitaxial Planar

TPS611(F)

Photoelectric Counter Various Kinds Of Readers Position Detection

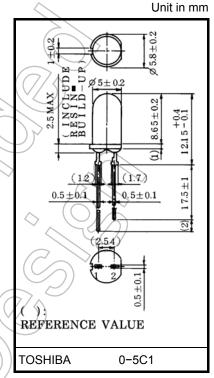
- φ5mm epoxy resin package(black)
- High sensitivity: $I_L = 120 \mu A(typ.)$
- Half value angle: $\theta 1/2 = \pm 8^{\circ} (typ.)$
- Protected from external light by black mold packaging.

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Collector-emitter voltage	V_{CEO}	30	W
Emitter-Collector voltage	V _{ECO}	5 (>
Collector current	IC	50	mA
Collector power dissipation	PC	150	mW
Collector power dissipation derating(Ta>25°C)	ΔP _C /°C	-2	mW/°C
Operating temperature range	T _{opr}	-20~75	°C//
Storage temperature range	T _{stg}	-30~100	°C/

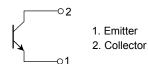
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Weight: 0.3 g (typ.)

Pin Connection



Opto-electrical Characteristics (Ta = 25°C)

Characteri	stics	Symbol	Test Condition		Min	Тур.	Max	Unit
Dark current		ID (ICEO)	I _F = 24V, E=0		_	0.005	0.1	μΑ
Light current		F	V_{CE} = 3V, E=0.1mW/cm ²	(Note)	30	120		μΑ
Collector–emitter saturation voltage		V _{CE} (sat)	I _C = 15μA, E=0.1mW/cm ²	(Note)	_	0.25	0.4	V
Switching time	Rise time	t _r	$V_{CC} = 5V$, $I_C = 2mA$ $R_L = 100\Omega$		_	6		2
	Fall time	t _f			_	6		μs
Peak sensitivity waveler	ngth	λ _P	_		_	900		nm
Half value angle		$\theta \frac{1}{2}$	_		_	±8	_	o

Note: Color temperature = 2870K, standard tungsten lamp

Precaution

Please be careful of the followings.

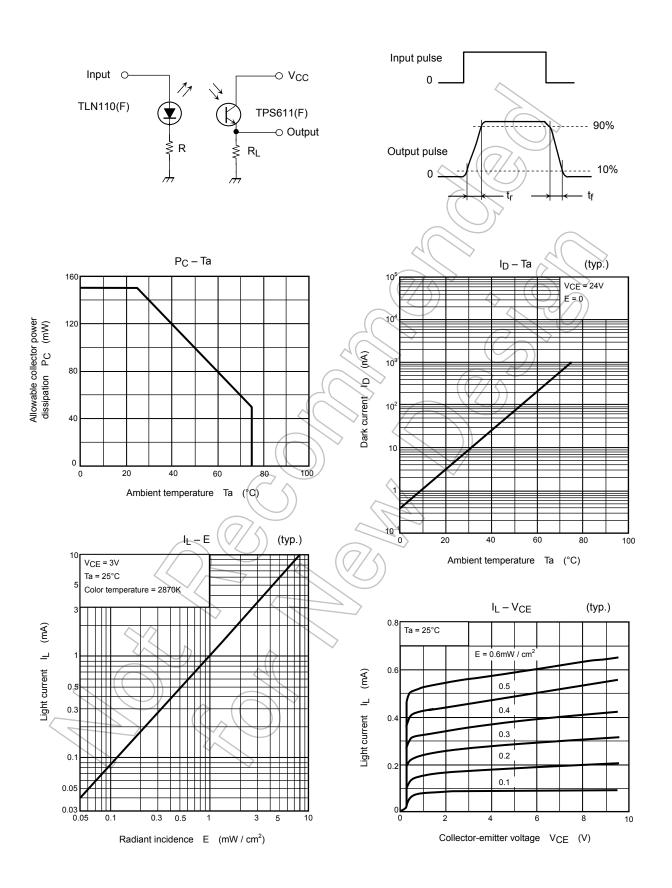
1. Soldering temperature: 260°C max. Soldering time: 5s max. (Soldering portion of lead: The top portion from the lead stopper.)

2. When the leads is formed, the lead shall be formed at the top portion of the stopper without leaving forming stress to the body of the device. Soldering shall be performed after lead forming.

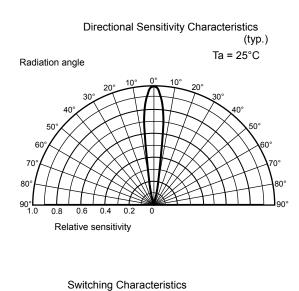


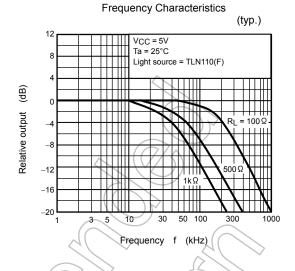
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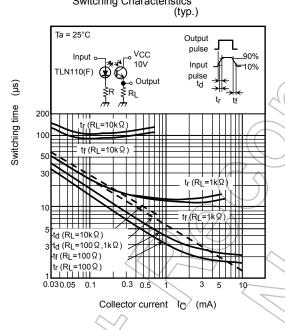
Fig.1 Switching time test circuit

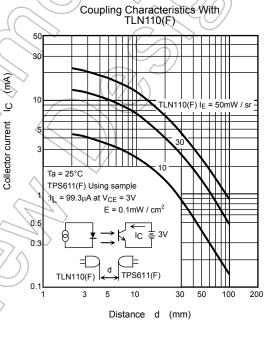


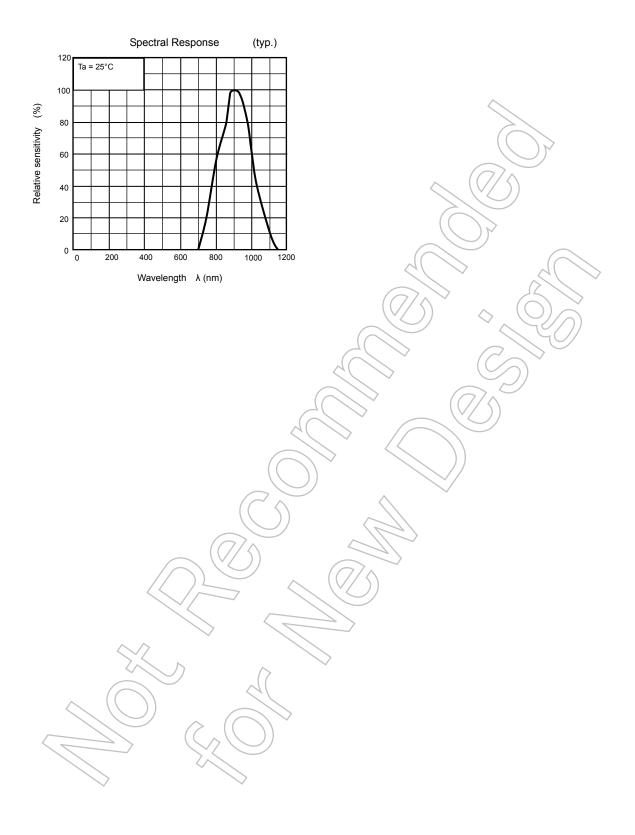
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