



Dual Output A-Series, BWR Models



High-Reliability, 1" x 2" 15-17 Watt, DC/DC Converters

Features

- Output voltages: ±5, ±12 or ±15 Volts
- Input voltage ranges: 10-18V. 18-36V or 36-75V
- Small packages, 1" x 2" x 0.48"
- Industry-standard pinouts
- Low cost; Highly reliable
- Proven SMT-on-pcb construction
- Qual tested; HALT tested; EMC tested
- IEC/EN/UL60950 certified
- CE mark available (75V-input models)
- Fully isolated, 1500Vdc guaranteed
- Efficiencies to 86%
- -40 to +100°C operating temperature
- Thermal protection
- On/Off control

For your mid-range power requirements, it's hard to beat the combination of small packaging, low cost, proven reliability and outstanding electrical performance offered by the 15-17W, dual-output models of C&D's A-Series DC/DC converters. These highly efficient, rugged converters combine straightforward circuit topologies, the newest components, proven SMT-on-pcb construction methods, and highly repeatable automatic-assembly techniques. Their superior durability is substantiated by a rigorous in-house qualification program that includes HALT (Highly Accelerated Life Testing).

The input voltage ranges of the BWR 15-17 Bipolar Series (10-18V for "D12A" models, 18-36V for "D24A" models and 36-75V for "D48A" models) make them excellent candidates for telecommunication system line drivers, or distributed power architectures. Their ± 5 , ± 12 or ± 15 Volt outputs cover virtually all standard applications.

These popular power converters are fully isolated (1500Vdc guaranteed) and display excellent line and load regulation ($\pm 0.5\%$ max. for line and load). They are completely I/O protected (input overvoltage shutdown and reverse-polarity protection, output current limiting and overvoltage protection) and contain input (pi type) and output filtering to reduce noise.

These extremely reliable, cost-effective power converters are housed in standard 1" x 2" x 0.48" UL94V-0 rated plastic packages. They offer industry-standard pinouts and are ideally suited for high-volume computer, telecom/datacom, instrumentation and ATE applications.

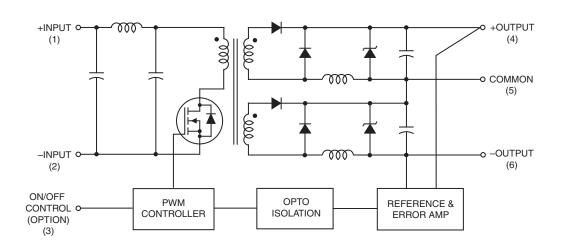


Figure 1. Simplified Schematic

Typical topology is shown

Performance Specifications and Ordering Guide

		Output					Input					Davidson.
	Vout	louт (mA)	R/N (mVp-p) ②		Regulation (Max.)		V _{IN} Nom.	Range	lin ④	Efficiency		Package (Case,
Root Model ®	(Volts)		Тур.	Max.	Line	Load 3	(Volts)	(Volts)	(mA/A)	Min.	Тур.	Pinout)
BWR-5/1500-D12A	±5	±1500	75	100	±0.5%	±0.5%	12	10-18	240/1.5	79%	81%	C14A, P43
BWR-5/1500-D24A	±5	±1500	75	100	±0.5%	±0.5%	24	18-36	112/0.75	81%	83%	C14A, P43
BWR-5/1500-D48A	±5	±1500	75	100	±0.5%	±0.5%	48	36-75	59/0.38	81%	83%	C14A, P43
BWR-12/725-D12A	±12	±725	75	100	±0.5%	±0.5%	12	10-18	265/1.7	82%	83.5%	C14A, P43
BWR-12/725-D24A	±12	±725	75	100	±0.5%	±0.5%	24	18-36	127/0.85	83%	85%	C14A, P43
BWR-12/725-D48A	±12	±725	75	100	±0.5%	±0.5%	48	36-75	62/0.4	84%	86%	C14A, P43
BWR-15/575-D12A	±15	±575	75	100	±0.5%	±0.5%	12	10-18	266/1.7	82%	84%	C14A, P43
BWR-15/575-D24A	±15	±575	75	100	±0.5%	±0.5%	24	18-36	125/0.84	84%	86%	C14A, P43
BWR-15/575-D48A	±15	±575	75	100	±0.5%	±0.5%	48	36-75	65/0.41	85%	87%	C14A, P43

- ① Typical at TA = +25°C under nominal line voltage and full-load conditions unless otherwise noted.
- ② Ripple/Noise (R/N) measured over a 20MHz bandwidth.
- Balanced loads, 10% to 100% load step.
- Nominal line voltage, 10% load/100% load conditions.
- ⑤ These are incomplete part numbers, use the part number structure when ordering.

PART NUMBER STRUCTURE B WR-12/725-D48 A C-C **Output Configuration:** RoHS-6 Compliant* **B** = Bipolar Wide Range Input Add C or N suffix as desired. See below. **Nominal Output Voltages:** A-Series ±5, ±12 or ±15 Volts High Reliability Input Voltage Range: **Maximum Output Current D12** = 10-18 Volts (12V nominal) in mA from each output **D24** = 18-36 Volts (24V nominal) **D48** = 36-75 Volts (48V nominal)

Part Number Suffixes

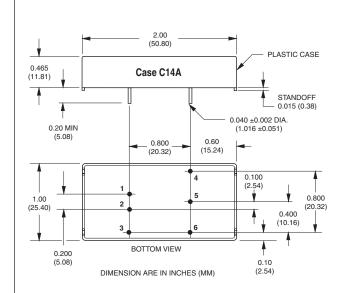
BWR 15-17 Watt DC/DC's are designed so an On/Off Control function with either positive polarity ("C" suffix) or negative polarity ("N" suffix) can be added to the pin 3 position. Models ordered without On/Off control (without C or N suffix) will not have pin 3 installed.

No Suffix Pin 3 not installed

C Positive On/Off control function (pin 3)N Negative On/Off control function (pin 3)

* Contact C&D Technologies for availability.

MECHANICAL SPECIFICATIONS



I/O Connections		
Pin	Function P43	
1	+Input	
2	-Input	
3	On/Off Control*	
4	+Output	
5	Output Return	
6	-Output	

^{*} Pin is optional

Performance/Functional Specifications

Typical @ TA = +25°C under nominal line voltage and full-load conditions, unless noted. ①

lı	nput
Input Voltage Range:	
D12A Models	10-18 Volts (12V nominal)
D24A Models	18-36 Volts (24V nominal)
D48A Models	36-75 Volts (48V nominal)
Overvoltage Shutdown:	
D12A Models	19-21 Volts
D24A Models	37-40 Volts
D48A Models	77-81 Volts
Start-Up Threshold: ③	
D12A Models	9.4-10 Volts
D24A Models	16.5-18 Volts
D48A Models	34-36 Volts
Undervoltage Shutdown: ③	7.0 5 1/- 1/-
D12A Models D24A Models	7-8.5 Volts 15.5-17.5 Volts
D48A Models	32.5-34.5 Volts
Input Current	SELO OTLO VOILO
Normal Operating Conditions	See Ordering Guide
Standby Mode (Off, OV, UV)	5mA
Input Reflected Ripple Current ⑦	10 mAp-p
Input Filter Type	Pi
Reverse-Polarity Protection	••
•	Brief duration, 5A maximum.
On/Off Control: ④ ⑤ C Models	On = open or 13V- +VIN, IIN = 1mA max.
Civiodeis	•
	Off - 0-0 8V IIN - 1mA may
N Models	Off = 0-0.8V, $IIN = 1mA max$. On = 0-0.5V, $IIN = 3mA max$.
N Models	*
	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max.
0	On = 0-0.5V, lin = 3mA max. Off = open or 3.5- +Vin, lin = 1mA max.
Οι Vουτ Accuracy (balanced half load)	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max.
Ο Voυτ Accuracy (balanced half load) Minimum Load Requirement ②	On = 0-0.5V, lin = 3mA max. Off = open or 3.5- +Vin, lin = 1mA max. utput ±2.0%, maximum 10%
Oυτ Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide
Vouπ Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide
Vour Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide See Ordering Guide
Vout Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency Isolation Voltage	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide See Ordering Guide See Ordering Guide 1500Vdc, minimum
Vour Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide See Ordering Guide
Vout Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency Isolation Voltage	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide See Ordering Guide See Ordering Guide 1500Vdc, minimum
Vour Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency Isolation Voltage Isolation Capacitance	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide See Ordering Guide 1500Vdc, minimum 550pF
Vout Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency Isolation Voltage Isolation Capacitance Isolation Resistance Current Limit Inception (@ 98% Vout) ±5V Models	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide See Ordering Guide 1500Vdc, minimum 550pF 10MΩ
Vour Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency Isolation Voltage Isolation Capacitance Isolation Resistance Current Limit Inception (@ 98% Vout) ±5V Models ±12V Models	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide See Ordering Guide 1500Vdc, minimum 550pF 10MΩ 1.9-2.5A 1-1.5A
Vout Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency Isolation Voltage Isolation Capacitance Isolation Resistance Current Limit Inception (@ 98% Vout) ±5V Models ±12V Models ±15V Models	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide See Ordering Guide 1500Vdc, minimum 550pF 10MΩ
Vout Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency Isolation Voltage Isolation Capacitance Isolation Resistance Current Limit Inception (@ 98% Vout) ±5V Models ±12V Models ±15V Models Short-Circuit Current	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide 1500Vdc, minimum 550pF 10MΩ 1.9-2.5A 1-1.5A 0.85-1.2A
Vout Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency Isolation Voltage Isolation Capacitance Isolation Resistance Current Limit Inception (@ 98% Vout) ±5V Models ±12V Models ±15V Models Short-Circuit Current ±5V Models	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide See Ordering Guide 1500Vdc, minimum 550pF 10MΩ 1.9-2.5A 1-1.5A 0.85-1.2A
Vout Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency Isolation Voltage Isolation Capacitance Isolation Resistance Current Limit Inception (@ 98% Vout) ±5V Models ±12V Models ±15V Models Short-Circuit Current ±5V Models ±12V Models ±12V Models	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide 1500Vdc, minimum 550pF 10MΩ 1.9-2.5A 1-1.5A 0.85-1.2A 800mA maximum 700mA maximum
Vour Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency Isolation Voltage Isolation Capacitance Isolation Resistance Current Limit Inception (@ 98% Vour) ±5V Models ±12V Models ±15V Models \$\frac{1}{2}\$\$ Short-Circuit Current \$\frac{1}{2}\$\$ Wodels	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide 1500Vdc, minimum 550pF 10MΩ 1.9-2.5A 1-1.5A 0.85-1.2A 800mA maximum 700mA maximum 700mA maximum
Vour Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency Isolation Voltage Isolation Capacitance Isolation Resistance Current Limit Inception (@ 98% Vour) ±5V Models ±12V Models ±15V Models ±15V Models ±12V Models ±12V Models ±15V Models ±15V Models ±15V Models 15V Models 15V Models 15V Models	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide 1500Vdc, minimum 550pF 10MΩ 1.9-2.5A 1-1.5A 0.85-1.2A 800mA maximum 700mA maximum 700mA maximum Output voltage comparator
Vout Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency Isolation Voltage Isolation Capacitance Isolation Resistance Current Limit Inception (@ 98% Vout) ±5V Models ±12V Models ±15V Models ±15V Models ±12V Models ±15V Models ±15V Models ±15V Models ±15V Models ±5V Models ±5V Models 0vervoltage protection ±5V Models	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide 1500Vdc, minimum 550pF 10MΩ 1.9-2.5A 1-1.5A 0.85-1.2A 800mA maximum 700mA maximum 700mA maximum 700mA maximum Output voltage comparator 5.45-7.15 Volts
Vour Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency Isolation Voltage Isolation Capacitance Isolation Resistance Current Limit Inception (@ 98% Vour) ±5V Models ±12V Models ±15V Models ±15V Models ±12V Models ±12V Models ±15V Models ±15V Models ±15V Models 15V Models 15V Models 15V Models	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide 1500Vdc, minimum 550pF 10MΩ 1.9-2.5A 1-1.5A 0.85-1.2A 800mA maximum 700mA maximum 700mA maximum Output voltage comparator
Vour Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency Isolation Voltage Isolation Capacitance Isolation Resistance Current Limit Inception (@ 98% Vout) ±5V Models ±12V Models ±15V Models	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide 1500Vdc, minimum 550pF 10MΩ 1.9-2.5A 1-1.5A 0.85-1.2A 800mA maximum 700mA maximum 700mA maximum 700mA maximum Output voltage comparator 5.45-7.15 Volts 13-15.8 Volts 16.2-19.8 Volts
Vout Accuracy (balanced half load) Minimum Load Requirement ② Ripple/Noise (20MHz BW) ① ⑥ Line/Load Regulation Efficiency Isolation Voltage Isolation Capacitance Isolation Resistance Current Limit Inception (@ 98% Vout) ±5V Models ±12V Models ±15V Models	On = 0-0.5V, IIN = 3mA max. Off = open or 3.5- +VIN, IIN = 1mA max. utput ±2.0%, maximum 10% See Ordering Guide See Ordering Guide 1500Vdc, minimum 550pF 10MΩ 1.9-2.5A 1-1.5A 0.85-1.2A 800mA maximum 700mA maximum 700mA maximum 700mA maximum Output voltage comparator 5.45-7.15 Volts 13-15.8 Volts

Dynamic Ch	aracteristics
Transient Response:	
(50-100% load step to 2% Vout)	250µsec maximum
Start-Up Time:	
VIN to VOUT	35msec
On/Off to Vout	30msec
Switching Frequency	300kHz (±30kHz)
Enviro	nmental
MTBF ®	Bellcore, ground fixed, fullpower
	25°C ambient, 1 million hours
Operating Temperature (ambient):	
±5V Models	48-58°C
±12V Models	50-60°C
±15V Models	55-65°C
Thermal Shutdown	115°C
Storage Temperature	-40 to +120°C
Flammability	UL 94V-0
Phy	sical
Dimensions	1" x 2" x 0.48" (25.4 x 50.8 x 12.19mm)
Case Material	Diallyl Phthalate
Pin Material	Gold-plate over copper alloy
Weight	1.19 ounces (34 grams)
Primary to Secondary Insulation Level	Operational

- ① All models are specified with no external I/O capacitors.
- ② See Technical Notes/Graphs for details.
- ③ Applying a voltage to the On/Off Control (pin 3) when no input power is applied to the converter can cause permanent damage to the converter.
- ④ Output noise may be further reduced with the addition of additional external output capacitors. See Technical Notes.
- The On/Off Control is designed to be driven with open-coolector logic or the application of appropriate voltage levels. Voltages may be referenced to the –Input (pin 2).
- Demonstrated MTBF available on request.
- ① Input Ripple Current is tested/specified over a 5-20MHz bandwidth with an external 33µF input capacitor and a simulated source impedance of 220µF and 12µH. See I/O Filtering, Input Ripple Current and Output Noise for details.

Input Voltage:	
Continuous:	
D12A Models	23 Volts
D24A Models	42 Volts
D48A Models	81 Volts
Transient (100msec):	
D12A Models	50 Volts
D24A Models	50 Volts
D48A Models	100 Volts
On/Off Control (pin 3) Max. Voltages Referenced to –Input (pin 2)	Wes
"C" Suffix	+VIN
"N" Suffix	+7 Volts
Input Reverse-Polarity Protection	Current must be <5 Amps. Brief duration only. Fusing recommended.
Output Current	Current limited. Devices can withstand sustained output short circuits without damage.
Case Temperature	120°C
Storage Temperature	-40 to +120°C
Lead Temperature (soldering, 10 sec.)	+300°C

Performance/Functional Specifications Table is not implied.

TECHNICAL NOTES

Input Fusing

Certain applications and/or safety agencies may require the installation of fuses at the inputs of power conversion components. Fuses should also be used if the possibility of sustained, non-current-limited, input-voltage polartiy reversal exists. For C&D BWR 15-17 Watt DC/DC Converters, you should use slow-blow type fuses with values no greater than the following:

Model	Fuse Value
All D12A Models	4 Amp
BWR-5/1500-D24A	2 Amp
BWR-12/725-D24A, BWR-15/575-D24A	2.5 Amp
All D48A Models	1 Amp

Start-Up Time

The VIN to VOUT start-up time is the interval of time where the input voltage crosses the turn-on threshold point, and the fully loaded output voltage enters and remains within its specified accuracy band. Actual measured times will vary with external output capacitance and load. The BWR 15-17W Series implements a soft start circuit that limits the duty cycle of the PWM controller at power up, thereby limiting the Input Inrush current.

The On/Off Control to Vout start-up time assumes the converter has its nominal input voltage applied but is turned off via the On/Off Control pin. The specification defines the interval between the time at which the converter is turned on and the fully loaded output voltage enters and remains within its specified accuracy band. Similar to the VIN to Vout start-up, the On/Off Control to Vout start-up time is also governed by the internal soft start circuitry and external load capacitance.

Input Overvoltage/Undervoltage Shutdown and Start-Up Threshold

Under normal start-up conditions, devices will not begin to regulate until the ramping-up input voltage exceeds the Start-Up Threshold Voltage (35V for D48 models). Once operating, devices will not turn off until the input voltage drops below the Undervoltage Shutdown limit (33.5V for D48 models). Subsequent re-start will not occur until the input is brought back up to the Start-Up Threshold. This built in hysteresis prevents any unstable on/off situations from occurring at a single input voltage.

Input voltages exceeding the input overvoltage shutdown specification listed in the Performance/Functional Specifications will cause the device to shutdown. A built-in hysteresis of 0.6 to 1.6 Volts for all models will not allow the converter to restart until the input voltage is sufficiently reduced.

Input Source Impedance

The converters must be driven from a low ac-impedance input source. The DC/DC's performance and stability can be compromised by the use of highly inductive source impedances. The input circuit shown in Figure 2 is a practical solution that can be used to minimize the effects of inductance in the input traces. For optimum performance, components should be mounted close to the DC/DC converter. If the application has a high source impedance, low VIN models can benefit of increased external input capacitance.

I/O Filtering, Input Ripple Current, and Noise Reduction

All BWR 15-17W DC/DC Converters achieve their rated ripple and noise specifications without the use of external input/output capacitors. In critical applications, input/output ripple and noise may be further reduced by installing additional external I/O caps.

External input capacitors (CIN in Figure 2) serve primarily as energy-storage elements, minimizing line voltage variations caused by transient IR drops in conductors from backplane to the DC/DC. Input caps should be selected for bulk capacitance (at appropriate frequencies), low ESR, and high rms-ripple-current ratings. The switching nature of DC/DC converters requires that dc voltage sources have low ac impedance as highly inductive source impedance can affect system stability. In Figure 2, CBUS and LBUS simulate a typical dc voltage bus. Your specific system configuration may necessitate additional considerations.

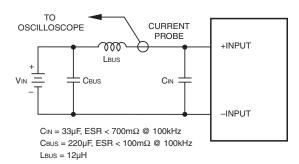


Figure 2. Measuring Input Ripple Current

In critical applications, output ripple/noise (also referred to as periodic and random deviations or PARD) may be reduced below specified limits using filtering techniques, the simplest of which is the installation of additional external output capacitors. These output caps function as true filter elements and should be selected for bulk capacitance, low ESR and appropriate frequency response. All external capacitors should have appropriate voltage ratings and be located as close to the converter as possible. Temperature variations for all relevant parameters should also be taken carefully into consideration.

The most effective combination of external I/O capacitors will be a function of line voltage and source impedance, as well as particular load and layout conditions. Our Applications Engineers can recommend potential solutions and discuss the possibility of our modifying a given device's internal filtering to meet your specific requirements. Contact our Applications Engineering Group for additional details.

Floating Outputs

Since these are isolated DC/DC converters, their outputs are "floating," with respect to the input. As such, it is possible to use +Output, -Output or Output Return as the system ground thereby allowing the flexibility to generate a variety of output voltage combinations.

Regulation for BWR 15-17W bipolar converters is monitored between –Output and +Output (as opposed to Output to Return).

Minimum Loading Requirements

BWR 15-17W converters employ a classical diode-rectification design topology and require a minimum 10% loading to achieve their listed regulation specifications and a stable operating condition.

Load Regulation

Regulation for the BWR 15-17W bipolar converters is monitored between –Output and +Output (as opposed to Output to Return). As such regulation will assure that voltage between –Output and +Output pins remains within the Vout accuracy listed in the Performance/Functional Specifications table.

If loading from +/- Outputs to Output Return is symmetrical, the voltage at Output pins with respect to Output Return will also be symmetrical. An unbalance in loading will consequently result in a degraded Vout regulation accuracy from +/- Outputs to Output Return (-Output to +Output regulation will still be within specification) with a load step from minimum to maximum load and with the other output at full load, the maximum deviation is 2.5% Vout nominal.

BWR-15/575-D48A Unbalanced Output Load Regulation

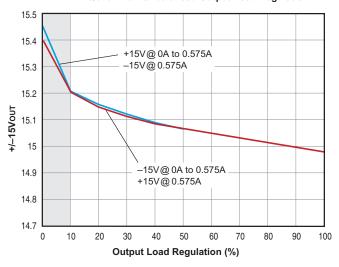


Figure 4. Output Voltage Accuracy vs. Imbalanced Loading

Current Limiting

When output current increases to approximately 15% to 50% above the rated output current, the DC/DC converter will go into a current limiting mode. In this condition the output voltage will decrease proportionately with increases in output current, thereby maintaining a somewhat constant power dissipation. This is commonly referred to as power limiting. Current limit inception is defined as the point where the full-power output voltage falls below the specified tolerance. See Performance/Functional Specifications. If the load current being drawn from the converter is significant enough, the unit will go into a short circuit condition. See "Short Circuit Condition."

Short Circuit Condition

When a converter is in current limit mode the output voltages will drop as the output current demand increases. If the output voltage drops too low, the magnetically coupled voltage used to develop primary side voltages will also drop, thereby shutting down the PWM controller.

Following a time-out period, the PWM will restart, causing the output voltages to begin ramping to their appropriate values. If the short-circuit condition persists, another shutdown cycle will be initiated. This on/off cycling is referred to as "hiccup" mode. The hiccup cycling reduces the average output current, thereby preventing internal temperatures from rising to excessive levels. The BWR 15-17W Series is capable of enduring an indefinite short circuit output condition.

Thermal Shutdown

These BWR converters are equipped with Thermal Shutdown Circuitry. If environmental conditions cause the internal temperature of the DC/DC converter rises above the designed operating temperature, a precision temperature sensor will power down the unit. When the internal temperature

Start-Up Time

The VIN to VOUT start-up time is the interval of time where the input voltage crosses the turn-on threshold point, and the fully loaded output voltage enters and remains within its specified accuracy band. Actual measured times will vary with external output capacitance and load. The BWR 15-17W Series implements a soft start circuit that limits the duty cycle of the PWM controller at power up, thereby limiting the Input Inrush current.

The On/Off Control to V_{OUT} start-up time assumes the converter has its nominal input voltage applied but is turned off via the On/Off Control pin. The specification defines the interval between the time at which the converter is turned on and the fully loaded output voltage enters and remains within its specified accuracy band. Similar to the V_{IN} to V_{OUT} start-up, the On/Off Control to V_{OUT} start-up time is also governed by the internal soft start circuitry

On/Off Control

The input-side, remote On/Off Control function (pin 3) can be ordered to operate with either polarity. Positive-polarity devices ("C" suffix) are enabled when pin 3 is left open (or is pulled high, +13V to V_{IN} applied with respect to –Input, pin 2, see Figure 2). Positive-polarity devices are disabled when pin 3 is pulled low (0-0.8V with respect to –Input). Negative-polarity devices are off when pin 3 is left open (or pulled high, 3.5V to V_{IN}), and on when pin 3 is pulled low (0-0.5V). See Figure 5.

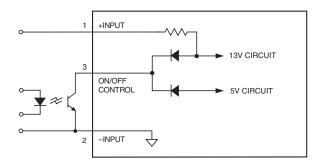


Figure 4. Driving the Positive Polarity On/Off Control Pin

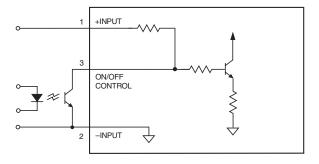
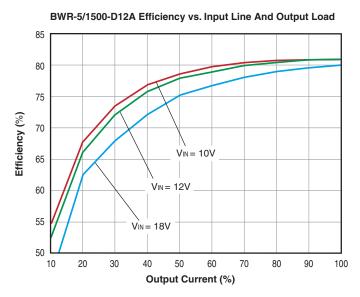
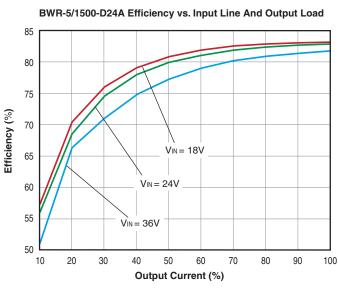


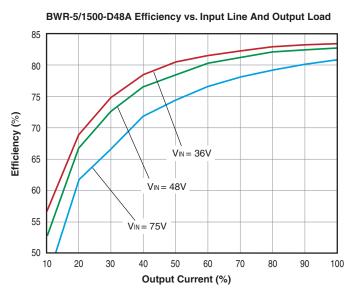
Figure 5. Driving the Negative Polarity On/Off Control Pin

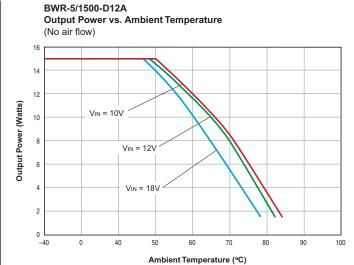
Dynamic control of the remote on/off function is best accomplished with a mechanical relay or an open-collector/open-drain drive circuit (optically isolated if appropriate). The drive circuit should be able to sink appropriate current (see Performance Specs) when activated and withstand appropriate voltage when deactivated.

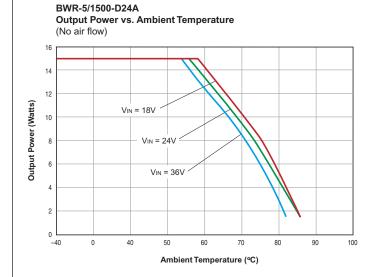
Applying an external voltage to pin 3 when no input power is applied to the converter can cause permanent damage to the converter.

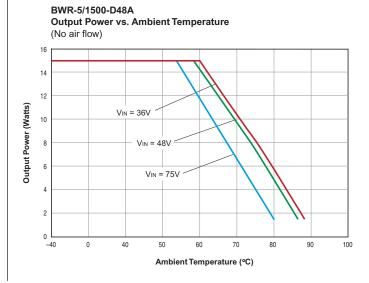


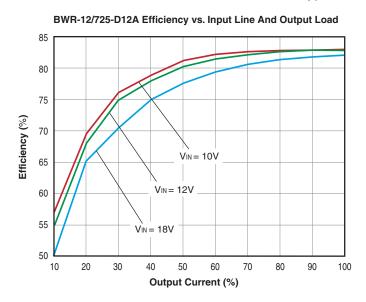


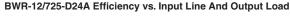


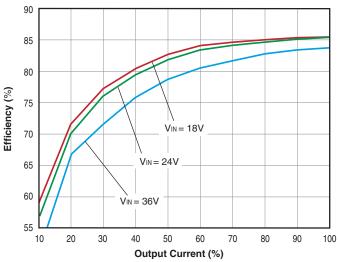




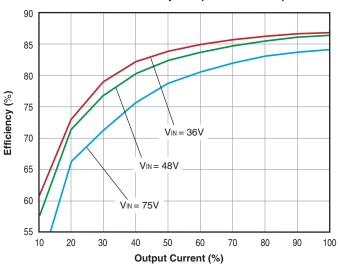




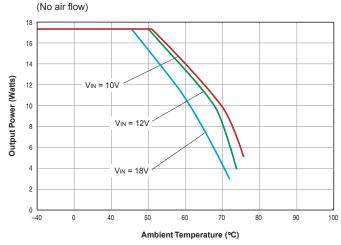




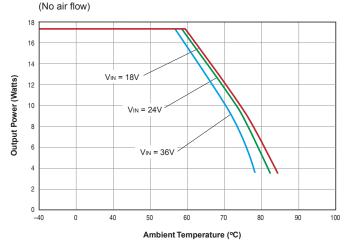
BWR-12/725-D48A Efficiency vs. Input Line And Output Load



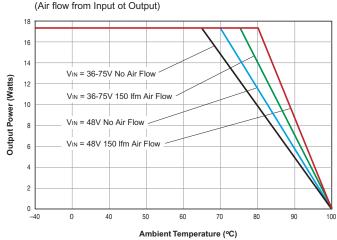
BWR-12/725-D12A
Output Power vs. Ambient Temperature

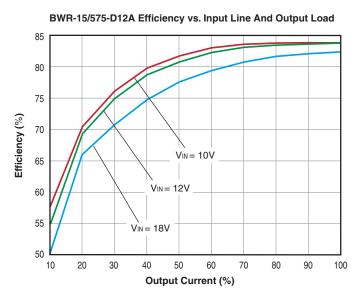


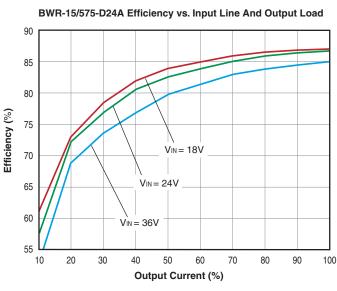
BWR-12/725-D24A
Output Power vs. Ambient Temperature

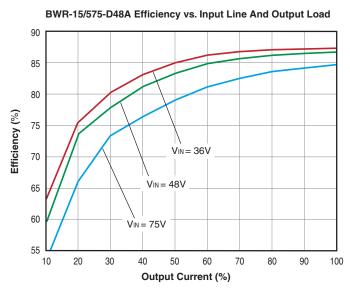


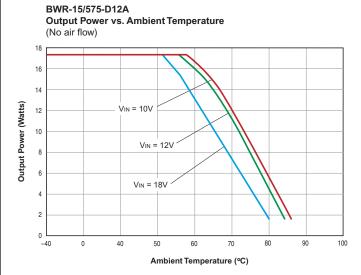
BWR-12/725-D48A
Output Power vs. Ambient Temperature

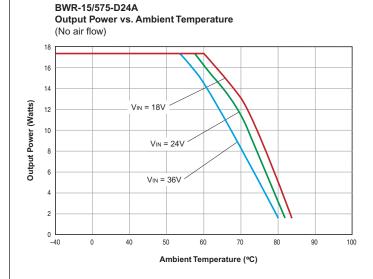


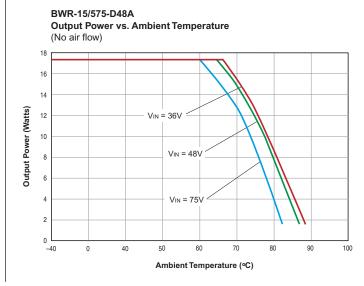


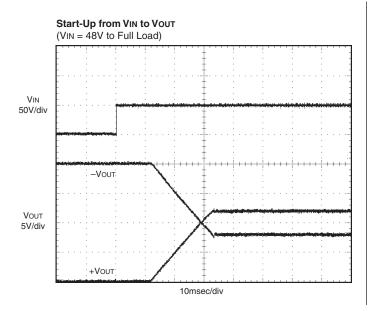


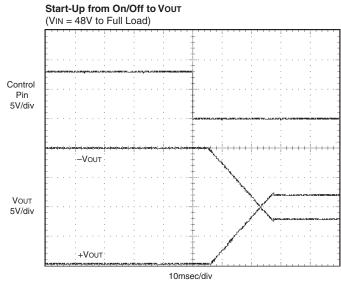












Adaptations

Several different additional converter configurations are available. Generally, these are modifications of an existing standard product. In some cases, they are designated with an additional 5-digit suffix on the end of the root parent standard model number. The actual details of the modification are contained in a Specification Control Drawing maintained by C&D and tracked using this same 5-digit special number suffix. These adapted products are normally built in the same production facilities and to the same quality standards as catalog products. Usually, they share the same components.

Once a modified product has been configured and supplied to a customer, it may be available as a "standard" product to other customers, assuming there is no proprietary status or other restriction. There may be scheduling and minimum order requirements for such products. Contact directly if you are interested in your own set of adaptations or modifications.



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