Wide input voltage , non-isolated & regulated single output



Patent Protection RoHS

FEATURES

- Efficiency up to 91%
- Low ripple & noise
- Without heatsink
- Short circuit protection and overheat protection
- Pin-out compatible with LM78XX series
- Operating temperature range: -40°C to +85°C
- Subminiature SIP package, meeting requirements of UL94-V0
- Low cost
- Industry standard pin-out

K78Lxx-1000R2 series are high efficiency switching regulators and ideal substitutes of LM78XX series three-terminal linear regulators. The product is featured with high efficiency, low loss, low radiation and no heat sink requirement. They are widely used in industrial control, instrumentation, and electric power applications.

Selection Guide					
	Input Voltage (VDC)	Out	Output Efficiency (%/Typ.)		Max.
Part No.	Nominal (Range)	Output Voltage (VDC)	Output Current (mA)	(Min. Vin)/ (Max. Vin)	Capacitive Load(µF)
K78L05-1000R2	12 (6.5-18)	5.0	1000	91/88	1000

Input Specifications					
Item	Operating Conditions	Min.	Тур.	Max.	Unit
No-load Power Consumption	Input voltage range	-		0.27	W
Reverse Polarity Input	erse Polarity Input Forbidden				
Input Filter		Capacitor filter			

Output Specifications					
Item	Operating Conditions	Operating Conditions Min. Typ			
Output Voltage Accuracy	100% load, input voltage range		±2	±3	
Line Regulation	Input voltage range		±0.4	±0.75	%
Load Regulation	10%-100% load		±0.5	±1.0	
Ripple & Noise*	20MHz bandwidth (refer to Fig. 2)		20	35	mVp-p
Temperature Drift Coefficient	-40°C to +85°C			±0.025	%/℃
Over temperature Protection	IC built-in			160	$^{\circ}$
Output short circuit protection			Continuous	, self-recovery	,
Transient response deviation	Name is additional to COV to and about the same as		55	250	mV
Transient recovery time	Nominal input, 25% load step change		0.5	3	ms
Thermal impedance			85	_	°C/W
Note: * Ripple and noise tested with "paral	llel cable" method, please see DC-DC Converter Applicati	ion Notes for spe	cific operation	n methods.	'

General Specifications					
Item	Operating Condition	Min.	Тур.	Max.	Unit
Operating Temperature	Derating if the temperature ≥55 °C (see Fig. 1)	-40		85	
Storage Temperature		-55		125	
Max. Operating Temperature for casing	Within the operating temperature curve	-		100	°C
Pin Welding Resistance Temperature	Welding spot is 1.5mm away from the casing, 10 seconds			300	
Storage Humidity	Non-condensing			95	%RH
Switching Frequency	100% load, input voltage range	350	400	450	KHz
MTBF	MIL-HDBK-217F@25℃	1000			K hours

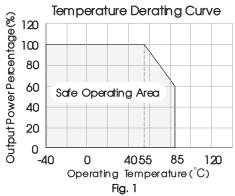
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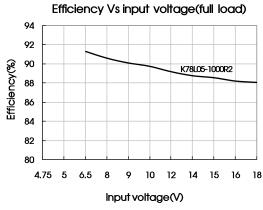
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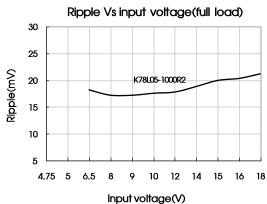
Physical Specifications				
Casing Material	lack flame-retardant and heat-resistant plastic (UL94-V0)			
Package Dimensions	1.60*7.55*10.16 mm			
Weight	2.00g (Typ.)			
Cooling Method	ree air convection			

EMC	Specifications			
EMI	Conducted Disturbance	CISPR22/EN55022	CLASS B (see Fig. 4-2) for recommended circ	uit)
EIVII	Radiated Emission	CISPR22/EN55022	CLASS B (see Fig. 4-2) for recommended circ	uit)
	Electrostatic Discharge	IEC/EN 61000-4-2	Contact ±4KV	perf. Criteria B
	Radiation Immunity	IEC/EN 61000-4-3	10V/m	perf. Criteria A
EN 4C	EFT	IEC/EN 61000-4-4	±1KV (see Fig. 4-① for recommended circuit)	perf. Criteria B
EMS	Surge Immunity	IEC/EN 61000-4-5	±1KV (see Fig. 4-① for recommended circuit)	perf. Criteria B
	Conducted Disturbance Immunity	IEC/EN 61000-4-6	3Vr.m.s	perf. Criteria A
	Voltage dip, drop and short interruption	IEC/EN 61000-4-29	0%-70%	perf. Criteria B

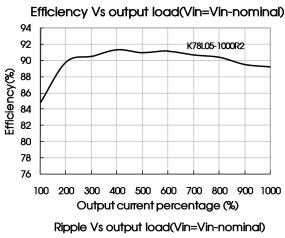
Product Characteristic Curve

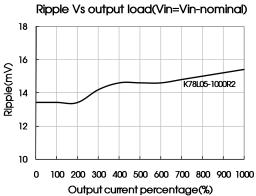






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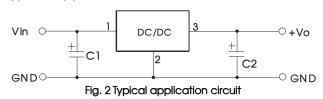




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Design Reference

1. Typical application circuit

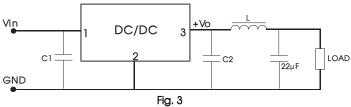


Part No.	C1 (ceramic	C2 (ceramic		
T GIT TO	capacitor)	capacitor)		
K78L05-1000R2	10µF/25V	10µF/10V		

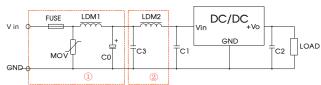
Notes:

- ① C1 and C2 are required and should be connected close to the pin terminal of the module.
- ② Capacitance of C1 and C2 refers to the table, which may be increased appropriately based on actual requirement, and a tantalum capacitor or a low ESR electrolytic capacitor may also be used.
- ③ No parallel connection and plug and play.

To reduce the output ripple furtherly, it is suggested to connect a "LC" filter at the output terminal, and recommended value of L is 10µH-47µH.



2. EMC solution-recommended circuit





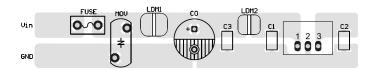


Fig.5 Recommended EMC circuit-PCB layout

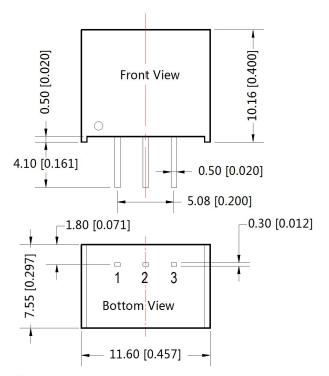
FUSE	MOV	LDM1	C0	C1/C2	C3	LDM2
Selected based on the actual input current from the customer	S10K35	82µH	680µF /50V	Refer to Fig.2	4.7µF /50V	12µH

Note: Part ① in the Fig. 4 is for EMS test, part ② is for EMI filtering; parts ① and ② can be added based on actual requirement.

3. For more information please find the application notes on www.mornsun-power.com

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Dimensions and Recommended Layout



THIRD ANGLE PROJECTION

Note : Grid 2.54*2.54mm

Ø1.00 [Ø0.039]

Pin-Out				
Pin Positive Output				
1	Vin			
2	GND			
3	+Vo			

Note:

Unit:mm[inch]

Pin section tolerances: ±0.10[±0.004] General tolerances: ±0.25[±0.010]

Notes:

- 1. Packing Information please refer to 'Product Packing Information'. Packing bag number: 58200003;
- 2. The max. capacitive load should be tested within the input voltage range and under full load conditions;
- Unless otherwise specified, data in this datasheet should be tested under the conditions of Ta=25°C, humidity<75% when inputting nominal voltage and outputting rated load;
- 4. All index testing methods in this datasheet are based on our Company's corporate standards;
- 5. The performance indexes of the product models listed in this manual are as above, but some indexes of non-standard model products will exceed the above-mentioned requirements, and please directly contact with our technician for specific information;
- 6. We can provide product customization service;
- 7. Specifications of this product are subject to changes without prior notice.

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