

PU040HXXXAQ_CLKS

General Built-in

DWG NO.: MSSD-XXXX



■ Features

- · Input voltage: 90-305VAC
- · Built-in active PFC function: 0.99 Typ.
- · Low THD: 10% Typ.
- · High efficiency: 88% Typ.
- · IP66 design for indoor installations
- · High surge immunity
- Constant current/ 0-10V/ clock(CLK)/ PWM dimming
- · Compliance to worldwide safety regulations for lighting
- · Suitable for dry/damp locations

IP66 (CRU US Class 2

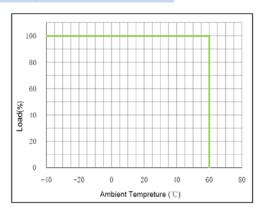
	Model											
(PL	J040HXXXAQ_CLKS)	035	045	053	070	105	140	175	210	245	280	315
,	Efficiency (120Vac)(Typ.) _{Note:1}	89.0%	88.5%	88.0%	87.5%	87.0%	86.5%	85.0%	84.0%	83.5%	82.5%	81.5%
	Efficiency (230Vac)(Typ.) _{Note.1}	90.0%	89.5%	89.0%	88.5%	88.0%	87.5%	86.0%	85.0%	84.5%	84.0%	83.0%
	Voltage Range (V) _{Note,2}	90 ~ 305Vac, OR 127~ 430Vdc										
	Voltage Rated (V) _{Note,2}	100Vac-277Vac										
	Frequency Range (Hz)	47~63										
	, , ,	0.99 (Typ.), with 85%~100% load, at 120Vac										
lanc.	Power Factor	0.97 (Typ.), with 85%~100% load, at 230Vac										
Input		0.9 (Min.), with 85%~100% load, at 277Vac										
	TUD	10% (Typ.), at 220Vac input, with 80%~100% load conditions										
	THD	15% (Typ.), at 110/277Vac input, with 80%–100% load conditions										
	AC Current (Max.)	0.6A at 100VAC input, 0.3A at 230VAC										
	Inrush Current (Max.)	15A at 230Vac input 25°C Cold Start (time wide=500uS, measured at 50% Ipeak,Not applicable for the inrush current to Noise Filter for less than 0.2ms)										
	Leakage Current (Max.)	0.5mA at 277Vac/60Hz										
	Voltage Range (V)	114	89	75	54	37	29	23	19	16	14	12
	Rated Current (mA)	350	450	530	700	1050	1400	1750	2100	2450	2800	3150
	Rated Power (W)	40	40	40	38	39	40.6	40	40	39	39	39
Output	Ripple Current((PK-AV)/AV) with LED default mode full load(Typ.)	<25%	<25%	<25%	<25%	<25%	<25%	<25%	<25%	<25%	<25%	<25%
Output	Current Tolerance	5%										
	Line Regulation	5%										
	Load Regulation	5%										
	Current ADJ. Range											
	Turn on Delay Time	<1.2s, at 120Vac; <1s, at 230Vac										
	Over Voltage (V)	150	118	84	60	42.4	35	35	25	23	22	18
Protection		Protection type: Limit the output voltage, recovers automatically after fault condition is removed										
	Short Circuit	Hiccup mode, recovers automatically after fault condition is removed.										
	Operating Temp.		-40~+60°C(Refer to 'Derating Curve')									
	Tc	90°C max										
	Operating Humidity	20~95%RH										
Environment	Storage Temp., Humidity	-40~+85℃ , 10-95%RH										
	Temp. Coefficient	0.03%/°C (0~50°C)										
	Vibration	10~500Hz, 5G 12min/cycle, period for 72min each along X、Y、Z axes										
Safety & EMC	Safety Standard		UL8	750, UL1012,	UL1310, CSA	A-C22.2 NO. 1	07.1, CSA-C2	2.2 NO. 223-I	И91, EN61347	7-1, EN61347-	-2-13	
	Withstand Voltage					I/	P-O/P:3.75KV	ac				
	Isolation Resistance					I/P-O/P:100M	Ohms/500Vd	c/25°C/70%RI	+			
	EMC Emission				EN55015/FC	C Part 15 Clas	ss B, EN6100	0-3-2 Class C	EN61000-3-3	3		
	EMC Immunity	EN61000-4-2,3,4,5,6,8,11, EN61547 (Surge: L-N 2kV)										
	MTBF	300,000 Hours, measured at full load, 25 ℃ ambient temperature										
Others	Lifetime				50,000 Ho	ours at Tc 75°C	(Refer to Life	Time VS. To	ase (Ref.)")			
	Dimension	95 x 70 x 32 (mm) (LxWxH)										
	Weight (Typ.)	0.3 kg										

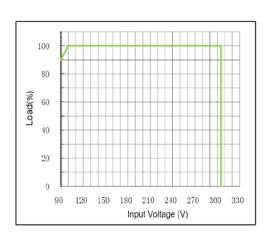
Note.1: Measured at full load and steady-state temperature in 25°C ambient; Note. 2: Derating may be needed under low input voltage, Please refer to 'Derating Curve'; Note. 3: All parameters NOT specially mentioned are measured at 230VAC input, rated load and 25°C ambient temperature;

Tel: +86 (0)21 52634688 Website: www.moons.com.cn

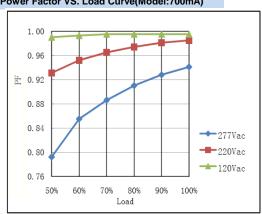
DWG NO. : MSSD-XXXX A

Derating Curve

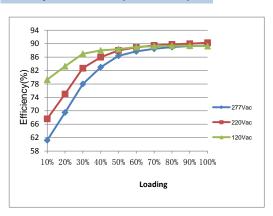




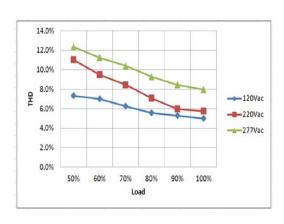
Power Factor VS. Load Curve(Model:700mA)



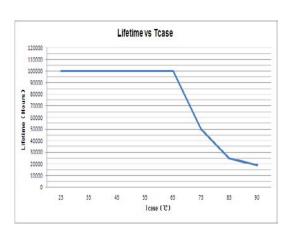
Efficiency VS. Load Curve(Model:700mA)



THD Curve(Model:700mA)



Life Time VS. Tcase (Ref.)





PU040HXXXAQ_CLKS

General Built-in

DWG NO.: MSSD-XXXX A2

■ Instruction

1.Field Programmable Topology



The programmable driver can be programmed by using special PC software and the programmer module.

2.Dimming Interface Description

Pin description

Pin	Name	Value	Description
1	Vaux 12V	10.8V-13.2V	Passive dimmers power supply
2	Dim+/Program	0-10V	Dimming/Programming input
3	Dim-	0V	DC Ground

CLKS DIMMING PROGRAMMING INTERFACE Vaux 12V / YE(黄色)

Dim+ Program / PU(紫色)

Dim- / GR(灰色)

3. Dimming Software Function Instruction

Adjustable Output Current(AOC)



Users can set the rated current between 10%*Max Current and 100%*Max Current

■ Adjustable Startup Time(AST)



Set driver's "Start Fade up Time". It means how much time the driver costs to achieve the "Module Current" that the user set. The valid value is 0s, 1s, 2s, 5s, 10s, 20s, 40s.

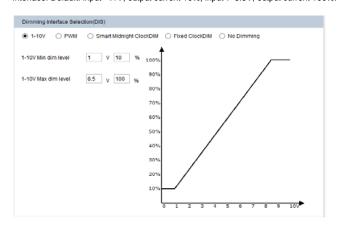
■ Fade Time(FT)



Set driver's "Fade up Time". This function is available in the Smart Midnight ClockDIM and Fixed ClockDIM mode; It means how much time the driver costs to achieve another dimming level from previous dimming level. The valid value is 0s, 1s, 2s, 5s, 10s, 20s, 40s

■ 1-10V

Allow users to set the max and min output current and corresponding output voltage to clarify the 1-10V dimming curve. Input a 0~10V signal from 2nd pin of the dimming interface. Default: input ≤1V, output current 10%; input ≥8.5V, output current 10%.



■ DWM

Input a PWM signal from the 2nd pin(Dim+/Program) of the dimming interface to change the output current. User can set "Positive Logic" or "Negative Logic" of the PWM signal. PWM duty circle: 1%~99%(it has both positive and negative logics), frequency: 500Hz~5kHz, 3V~10V is high,-0.3V~0.8V is low.



subject to change without notice

Page 3 of 5



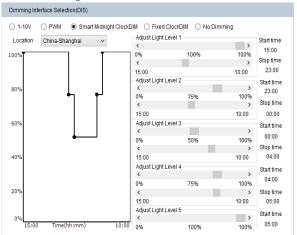
PU040HXXXAQ CLKS

General Built-in

DWG NO.: MSSD-XXXX

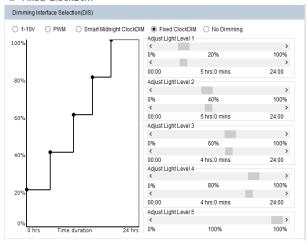
■ Instruction

■ Smart Midnight ClockDIM



Smart Midnight ClockDIM allows dimming to predefined light levels based on the nightly operating time. With flexibility in setting time and light levels, the user can configure the driver for specific locations and application needs. Using Integrated Dynadimmer, it is possible to set up to 5 dim levels and time intervals. The driver does not have a real time clock. Instead it runs a virtual clock, determined by the length of nightly operating hours. After 3 ON-OFF cycles, the driver will calculate the virtual clock time. A valid ON-time is defined as a period during which the driver operates continuously for ≥4 hours to ≤24 hours. For example, if the requirement in summer is: 23:00-00:00: 75%, 00:00-04:00: 50%, 04:00-05:00: 75% (other time 100% or Off). The driver should be powered on for 7h, so it can calculate the virtual clock time as 22:00. Then we can set the dimming plan: 22:00~23:00: 100%, 23:00-00:00: 75%, 00:00-04:00: 50%, 04:00-05:00: 75%. From summer to winter, the valid ON-time changes day by day. The driver should be powered on for 17h in winter, and it also can calculate the virtual clock time as 17:00. Then the dimming plan is 17:00~23:00: 100%, 23:00-00:00: 75%, 00:00-04:00: 50%, 04:00-05:00: 75%, 05:00~10:00: 100%. From the above, if we set the dimming plan as shown in the picture, after repeating the driver ONtime for 3 consecutive days, the dimming plan takes effect from the 4th day onwards. Each day the driver powered on, it has a different start time according to the virtual clock time. So the driver can satisfy different requirements for different seasons.

■ Fixed ClockDIM



Allow users to separate 24hrs into 5 sections and corresponding output current.

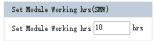
■ No Dimming

100



The driver will be in constant output mode.

■ Set MODULE Working hrs(SMW)

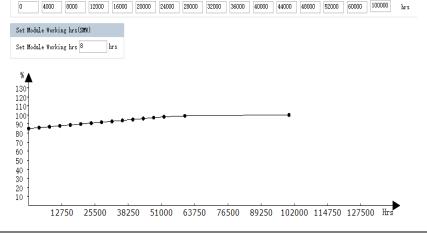


User can check how much time the driver works through this function.

■ Constant Light Output(CLO) Constant Light Output(CLO)

O Disable CLO 💿 Enable CLO

86 87 88



90 91 92 93 94 95

Traditional light sources suffer from depreciation in light output over time. This applies to LED light sources as well. The CLO feature enables LED solutions to deliver constant lumen output through the life of the light engine. Based on the type of LEDs used, heat sinking and driver current, it is possible to estimate the depreciation of light output for specific LEDs and this information can be entered into the driver. The driver counts the number of light source working hours and will increase output current based on this input to enable CLO.

When the CLO feature is enabled, the driver nominal output current will be defined by the CLO percentage as shown by the equation below:

Driver target nominal output current = CLO percentage * AOC. For example, in the CLO profile shown in Figure, between 52,000-60,000 working hours, the CLO percentage is set at 98%. Assuming the nominal AOC is set to 500mA, the driver output current with CLO enabled will be $0.98 \times 500 = 600$

The CLO percentage can be set to a value between 85%-100%, in increments of 1%. The LED module working hours can be set at any value between (0-100,000 hours).

subject to change without notice

97



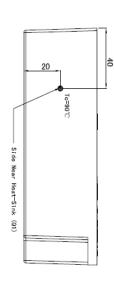
PU040HXXXAQ_CLKS

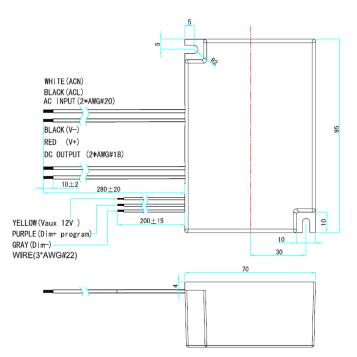
General Built-in

DWG NO.: MSSD-XXXX A2

■ Mechanical Specification

Dimensions (Unit: mm)







RoHS Compliance:

Our products comply with the European Directive 2002/95/EC, calling for the elimination of lead and other hazardous substances from electronic products.