



M4365 SERIES

DC/DC POWER SUPPLY



PRODUCT HIGHLIGHTS

- **VITA 62 COMPLIANT** •
- **3U VPX FORM FACTOR** •
- **DC/DC CONVERTER** •
- **6 OUTPUTS** •
- 600W (700W PEAK) •
- **Current Share for VS# Outputs** •
- **Input Options:** •
 - o MIL-STD-704
 - MIL-STD-1275
- System Management • Protocol - VITA 46.11
- Cyber secure •

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Electrical Specifications

DC Input18 to 48 VDCMax Non-Operating 100VOptions:1) MIL-STD-704 (A-F)Normal and AbnormalSteady State2) MIL-STD-704(A-F)transients Up to 50V, 80V3) MIL-STD-1275 Surge4) Def Stan 61-5 170V Loaddump.	DC Output VS1: VS2: VS3: 12V_Aux: -12V_Aux: 3.3V_Aux: Peak power op VS1: VS3: Current Sharin	12V 5V	up to 30A up to 20A up to 30A up to 1A up to 1A up to 5A up to 40A up to 35A	Isolation Over 20 MΩ at test voltage: 200V between Input and Output 200V between Input and Case 100V between Output and Case
<u>Line/Load regulation</u> See Table 2 on page 7	Optional for V <u>Efficiency</u> Up to 88% 85.5 % @ Full (See Para. 4)	51, VS2, V	VS3	<u>EMC</u> Complies with MIL-STD-461F (5μH LISN): CE101, CE102, CS101, CS114, CS115, CS116
<u>Ripple and Noise</u> Typically, less than 50mV _{P-P} (max.1% _P). Measured across a 0.1μF capacitor and 10μF capacitor on load at Input Voltage of 18V-36V, all Temperature Range.	<u>Load Transien</u> <u>and Undersho</u> Output dynam than 5% at loa Output return than 1mSec	o <u>ot</u> nic respor nd Step of	nse of less 30%-60%.	System Management VITA 46.11 Tier 2 IPMC Data Available: • Output voltages and currents • Input voltage • Card Temperature • Card Status







Salt Fog:

Shock

Method 509.5

Method 516.6

(all directions)

40g, 11msec saw-tooth

Environmental 1

Design to Meet MIL-STD-810G

Temperature

Operating: -55 °C to +85 °C at unit edge Storage: -55 °C to +125 °C Designed to meet 600 thermal cycles durability test

<u>Fungus</u>

Does not support fungus growth, in accordance with the guidelines of MIL- STD-454, Requirement 4.

Vibration

Vibration: Figure 514.6E-1. General minimum integrity exposure. (1 hour per axis.)

Reliability: 305,000 Hours, calculated IAW MIL-HDBK-217F Notice 2 at +65°C, GF.

Note 1: *Environmental Stress Screening (ESS)* Including random vibration and thermal cycles is also available. Please consult factory for details.

Altitude

Humidity

Method 500.5, Procedure I & II

Storage/Air Transport: 40 kft

Operation/Air carriage: 70 kft

Method 507.5, Up to 95% RH

Protections⁺

<u>Input</u>

Input Reverse Polarity: Protection for unlimited time

Inrush Current Limiter Peak value of 5 x I_{IN} for initial inrush currents lasting more than 50 µSec.

Under Voltage Unit shuts down when input voltage drops below 17± 0.5VDC. Automatic restart when input voltage returns to nominal range.

Over Voltage Lock-Out Unit shuts down when input steady state voltage rise above 55 ± 2VDC. Automatic restart when input voltage returns to nominal range.

<u>Output</u>

Passive over voltage protection on Aux outputs

Zener selected at $25\% \pm 5\%$ above nominal voltage, is placed across the output for passive voltage limit.

Active over voltage protection on VS# outputs

20% ± 5% above nominal voltage. Automatic recovery when output voltage drops below threshold.

Overload / Short-Circuit Protection Continuous Hiccup protection (110-130%) for VS#. Aux Typical: 3.3Vaux / 8A 12Vaux. 1.5A-2A -12Vaux. 2.5A-3A

General

Over Temperature Protection Automatic shutdown at temperature of 95 ± 5 °C (at unit edge) Automatic recovery when temperature drops below 90 ± 5 °C.

Note 1: Thresholds and protections can be modified / removed (please consult factory)







Functions and Signals - According to VITA 62

Signal Name	Туре	Description
FAIL*	Output	Indicates to other modules in the system that a failure has occurred in one of the outputs. Please refer to Figure 2 ¹
SYSRESET*	Output	Indicates to other modules in the system that all outputs are within their working level. Please refer to Figure 2 ¹
INHIBIT*	Input	Controls power supply outputs. This signal in conjunction with ENABLE controls the outputs. Please refer to Table 1 and Figure 1 ¹
ENABLE*	Input	Controls power supply outputs. This signal in conjunction with INHIBIT controls the outputs. Please refer to Table 1 and Figure 1 ¹
GA0*, GA1*, GA2*	Input	Used for geographical addressing. GA2 is the most significant bit and GA0 is the least significant bit. ¹
SCL, SDA	Bidirectional	I2C bus Clock and Data respectively. Through this bus the voltage and temperature readouts can be shared. ¹
REF_CLK	Input	The REF_CLK signal is used to allow the power supply frequency to sync with the system frequency. ¹
VOUT SENSE	Input	The SENSE is used to achieve accurate load regulations at load terminals (this is done by connecting the pins directly to the load's terminals).
Alert Bit	Output	Indicates to other modules in the system about Input Voltage loss. Please refer to Figure 2 ¹
Active Current Share	Bidirectional	Support Active current share between Outputs. See Current Share para. ¹²³
PO#_SHARE	Bidirectional	Support Passive current share between Outputs ¹

Notes:

- 1. Signal referenced to SIGNAL RTN
- 2. When not used leave open
- 3. For Passive of Non-Current Share configuration, this pin is intentionally Disconnected

Table 1 – Inhibit and Enable Functionality







INHIBIT*	Low	Low	High	High
ENABLE*	Low	High	Low	High
VS1 ,VS2,VS3, ±12VAux	OFF	OFF	ON	OFF
3.3V_AUX	ON	OFF	ON	OFF

Figure 1 – Inhibit and Enable Input stage

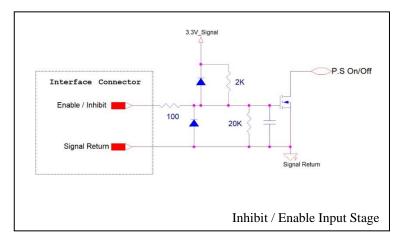
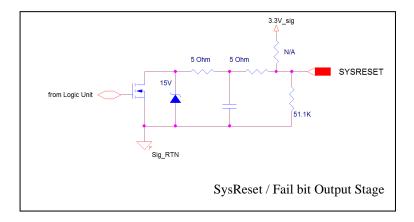


Figure 2 – SysReset and Fail Bit output stage

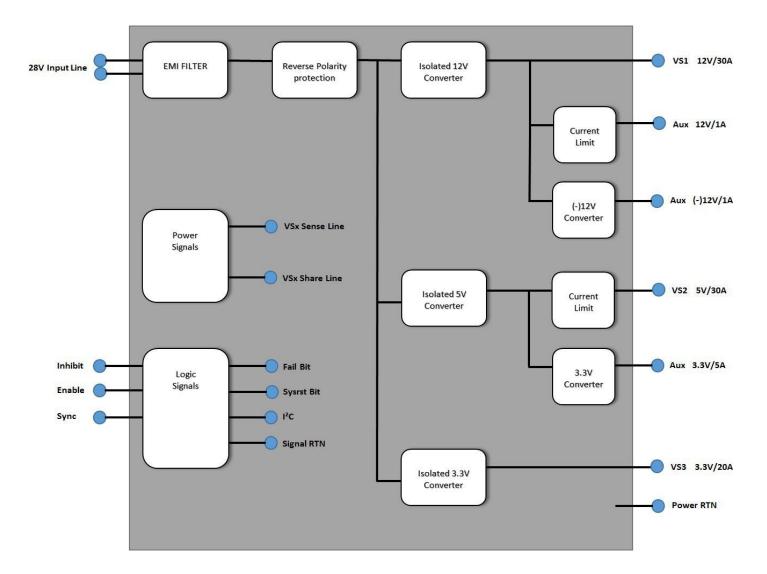












Detailed Information







1. M4365 Input Voltage Operation Range.

The M4365 steady state operation voltage is 18V to 48V, continuously work up to 50V Input line. Unit can be modified to work up to 100V or down to 16V transient and Surge.

Max Non operation Voltage 100V.

1.1 Low Line Turn-on and Turn-off Limits

To avoid Turn-on and Turn-off cycling glitch the unit have about 2V Hysteresis. The Turn-on threshold is under 20V and turn- off below 18V. Those limits can be adjusted, contact Factory for more information.

2. Outputs Voltage Regulation

The M4365 contains accurate internal sense lines to keep output voltage at less than 3% regulation for all Line/ Load and temperature range (see Table 2).

Output	12V/25A	5V/30A	3.3V/20A	3.3VAux/5A	12VAux/1A	(-)12VAux/1A	Notes
Voltage Range	11.85 - 12.15	4.9 - 5.1	3.28 - 3.42	3.25 - 3.45	11.7 - 12.2	(-)11.85 – (-)12.15	
Voltage Range	11.80 - 12.20	4.9 - 5.2	3.20 - 3.40	3.25 - 3.45	11.7 - 12.2	(-)11.7 – (-)12.2	Current share configuration

Table 2: Outputs voltage regulation. VIN 18V – 48V, Temperature -55 °C – 85 °C single and parallel configuration.

2.1. Sense Lines

Sense Lines are provided for VS1, VS2 and VS3 output to compensate line voltage drop. Sense Lines proper connection is shown in Figure 3.

Each VSx output has its own Sense Lines, additional common

Sense RTN Line is provided for all VSx Outputs (VITA 62

Standard).

Contact Factory for Sense configuration different than the VITA 62 standard

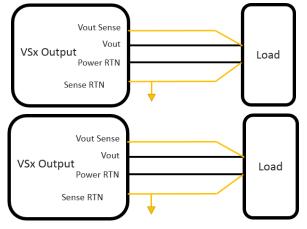


Figure 3: M4365 Sense line connection







3. Output Power

The M4365 can deliver up to 616W steady State at all temperature and input range. Unit can support a peak power of 700W, please contact factory for more details and limitations.

Max Total Power Output	12V/30A	5V/30A	3.3V/20A	3.3VAux/5A	12VAux/1A	(-)12VAux/1A
616W	30A	30A	20A	5A	1A	1A
700W	40A	35A	20A	5A	1A	1A

3.1. Mil-STD 1275E Surges (optional)

The Unit can support up to 500W during 12V surge. Operating the unit at higher power may results in a poor output transient response during the surge.

Total Power Output	12V/25A ¹	5V/30A	3.3V/20A	3.3VAux/5A	12VAux/1A	(-)12VAux/1A
550W	22A	30A	20A	5A	1A	1A

Table 4: Max current per output during 12V surge

Note 1: Max total current of 12V rail (12Vvs1, 12Vaux & (-) 12Vaux) should not exceed 22A in order to stay in spec regulation

3.2 Typical Efficiency (Typical results at room temperature)

Input Voltage	Output Power	Efficiency
	170W	88%
28V	200W	87.6%
	600W	86.1%

4. Current Share (optional)





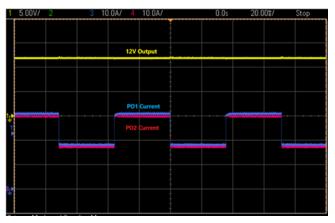


Current Share FOR vs# Outputs between two or more units is optional (Contact Factory)

4.1 Active current sharing (A.C.S)

Current sharing done in a closed loop. All paralleled outputs are compared and feedback is used to balance their load current. The result is a more stable, less sensitive output voltage without voltage drop. Typical Load Balance of about 2-3A for all Load range is expected.

Typical ACS Dynamic Load of Two 12V Paralleled Outputs (P01 & PO2 refer to two different units)



Notes 1. When Not used, A.C.S pins can be left open.

4.2 Passive current sharing (P.C.S)

Current sharing is done in open loop, output voltage drops as a function of output load. Load Balance of about 5-10% is expected.

For a required output to current share please connect the following Pins between the two units

• PO#_Sense & PO#_Sense_RTN

(for best performance, Pins from paralleled units should be connected to a single point and as close as possible to the load point)

- Unit 1 VS#_SHARE to Unit 2 VS#_SHARE
 - A7 to A7 B7 to B7
 - C7 to C7
- Unit 1 VS#_AC SHARE to Unit 2 VS#_AC SHARE (not required for P.C.S) A1 to A1
 - B1 to B1
 - D1 to D1

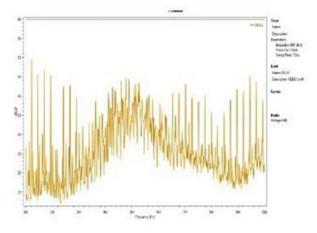






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M4365 SERIES VPX DC/DC POWER SUPPLY



5. Typical EMI Tests











6. Communication Protocol

Unit communication protocol can be configured as VITA 46.11 Tier 2 IPMC or Advanced I2C compatible protocol.

For more details on protocols refer to para. 6.1 and 6.2

Electrical Parameters

Vcc: 3.3VDC Pull-up: 10K Ohm Input Capacitance 330Pf

Slave Device Addressing

- 256 address spaces _
- Baud rate: 200kHz maximum -
- 7 Bit Protocol
- Support Slot Addressing per VITA62 _

	MSB							LSB
Slot Number	A6	A5	A4	A3	A2	A1/*GA1	A0/*GA0	R/W
Slot0	0	1	0	0	0	0	0	
Slot1	0	1	0	0	0	0	1	
Slot2	0	1	0	0	0	1	0	
Slot3	0	1	0	0	0	1	1	

* Slot location is determined by GAx per VITA 62.









6.1. Advanced I2C Protocol

Read Command – 21Hex, deliver 64Bytes of Data. (More commands are available by request) The communication starts when the master sends a start followed by the unit slave address,

command, checksum and a stop. A second start followed by the slave address and a read will be followed by a 64 Bites response.

S	Slave Address	R/W	Α	Command	A	Check sum	A	Ρ
	A6:A0	0	0	21 Hex	0	DF Hex	0	

S	Slave Address	R/W	Α	DATA	Α	DATA	Α	DATA	A		DATA	A	Check sum	N/A	Ρ
	A6:A0	1	0	D7:D0	0	D7:D0	0	D7:D0	0	•••	D7:D0	0	D7:D0	1	

Command – 21Hex read all 64 Bytes S -Start P- Stop

Master Transmit Unit Transmit

6.2. 46.11 Tier 2

Please see 46.11 User Manual for detailed information of operation. Sensors included are seen in the table below.

Record ID	Sensor ID	Sensor Type	Name
0000	00	F0h	FRU State Sensor
0001	01	F1h	System IPMB Link Sensor
0002	02	F2h	FRU Health Sensor
0003	03	02h	FRU Voltage Sensor
0004	04	F3h	FRU Temperature Sensor
0005	05	F4h	Payload Test Results Sensor
0006	06	F5h	Payload Test Status Sensor
0100	07	02h	VS1 Voltage
0101	08	02h	VS2 Voltage
0102	09	02h	VS3 Voltage
0103	0A	02h	3.3V Aux Voltage
0104	OB	02h	+12V Aux Voltage
0105	0C	02h	-12V Aux Voltage
0106	0D	03h	VS1 Current
0107	OE	03h	VS2 Current
0108	OF	03h	VS3 Current
0109	10	03h	3.3V Aux Current
010A	11	03h	+12V Aux Current
010B	12	03h	-12V Aux Current
010C	13	01h	Analog Temperature
9999	N/A	N/A	Device Management







Pin Assignment

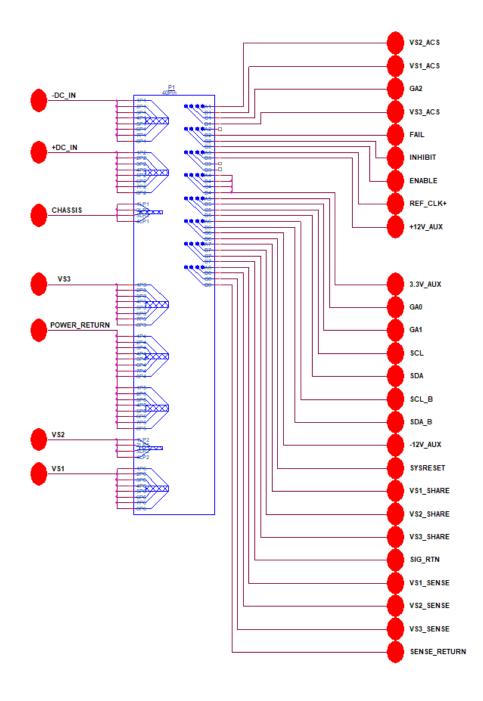


2400. ILD. 200. 2000. ILD. 10000		0100010 1	6150210-7		TAN NUMBER	DAGT NUMBED
	A	в	n	D	U M V M J	DOWC
		1	1		P	
ľ		13	1		P2 LP	POWER
ľ		1		LPI	20	
	05	R5	Υ5	25	-	
ľ	05	R5	75	25	2	
	05	R5	Y5 Y5 Y5 Y5 Y5	25 25 Z5	ω	
ľ	30	R5	5	25	4	SIGNAL
	05	R5	Υ.5	15	cn	NAL
	05	85	Υ5	25	m	
	05	85	Y5 Y5	25 25	Ľ	
	0	R5	Υ5	25	00	
		200	11		Ρ3	
		35	1		P4	-
		1	1		P5	POWE
		1	Ŧ		P3 P4 P5 LP2 P6	2
			-		P6	









ILPOWER

SOURCE

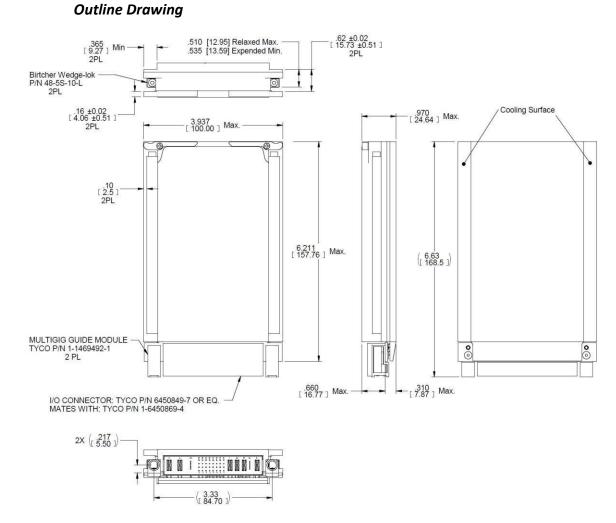
Pin Number	Pin Name
P1	-DC_IN
P2	+DC_IN
LP1	CHASSIS
P3	VS3
P4	POWER_RETURN
P5	POWER_RETURN
LP2	VS2
P6	VS1
A8	VS1_SENSE
B8	VS2_SENSE
C8	VS3_SENSE
D8	SENSE_RETURN
A7	VS1_SHARE
B7	VS2_SHARE
C7	VS3_SHARE
D7	SIG_RTN
A6	SCL_B
B6	SDA_B
C6	-12V_AUX
D6	SYSRESET*
A5	GA0*
B5	GA1*
C5	SCL
D5	SDA
A4	+3.3V_AUX
B4	+3.3V_AUX
C4	+3.3V_AUX
D4	+3.3V_AUX
A3	Sync_IN
B3	+12V_AUX
C3	N.C
D3	N.C
A2	N.C
B2	FAIL*
C2	INHIBIT*
D2	ENABLE*
A1	VS2_ACS
B1	VS1_ACS
C1	GA2
D1	VS3_ACS











Notes

- Dimensions are in Inches[mm] 1.
- Tolerance is: 2. $.XX\pm0.02~\text{IN}$ $.XXX\pm0.008~\text{IN}$
- 3. Weight: Approx. 796 g (28.08 oz)
- 3D model available 4.

Note: Specifications are subject to change without prior notice by the manufacturer.

