PKM 4402NG PIDP IBC 5:1 Ratio, Input 38-55 V, Output 63A / 480W

Key Features

- Industry standard Quarter-brick 57.9 x 36.8 x 10.6 mm (2.28 x 1.45 x 0.42 in.)
- Double output pin for better thermal and high • current/power handling
- High efficiency, typ. 97 % at 9.6 Vout 50% load ٠
- 1500 Vdc input to output isolation
- · Meets isolation requirements equivalent to basic insulation according to IEC/EN/UL 60950
- Baseplate option •
- More than 1.4 million hours MTBF

General Characteristics

- n+1 parallelable
- Input under voltage protection
- Input over voltage shutdown
- Over temperature protection
- · Output short-circuit protection
- Remote control
- · Highly automated manufacturing ensures quality
- ISO 9001/14001 certified supplier



Safety Approvals



Design for Environment

Meets requirements in hightemperature lead-free soldering processes.

Contents

General Information Safety Specification Absolute Maximum Ratings		3
Product Program	Ordering No.	
9.6 V, 63 A / 480W Electrical Specification	PKM 4402NG PIDP	5
EMC Specification		
Operating Information		
Thermal considerations		14
Connections		15
Mechanical Information		16
Soldering Information		18
Delivery Information		18
Product Qualification Specification		19

Technical Specification EN/LZT 146 405 R1B June 2009

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P

RoHS compatible

PKM 4402NG PIDP IBC 5:1 Ratio, Input 38-55 V, Output 63A / 480W

General Information

Ordering Information

See Contents for individual product ordering numbers.

Option	Suffix	Ordering No.
Positive Remote Control Logic	P	PKM 4402NG PIP
Dual pin	DP	PKM 4402NG PIDP
Lead length 3.69 mm (0.145 in)	LA	PKM 4402NG PILA
Lead length 4.57 mm (0.180 in)	LB	PKM 4402NG PILB
Lead length 2.80 mm (0.110 in)	LC	PKM 4402NG PILC
Baseplate	HS	PKM 4402NG PIHS
Case to ground pin (¹)	G	PKM 4402NGPIHSG

Note: (1)Case to ground pin only available with baseplate If several options needed below sequence is to be used LOGIC OPTION BASEPLATE CASE GROUND PIN LENGTH Example: PKM4402NGPIPHSGDPLA

Reliability

The Mean Time Between Failure (MTBF) is calculated at full output power and an operating ambient temperature (T_A) of +40°C, which is a typical condition in Information and Communication Technology (ICT) equipment. Different methods could be used to calculate the predicted MTBF and failure rate which may give different results. Ericsson Power Modules currently uses Telcordia SR332.

Predicted MTBF for the series is:

1.4 million hours according to Telcordia SR332, issue 1, Black box technique.

Telcordia SR332 is a commonly used standard method intended for reliability calculations in ICT equipment. The parts count procedure used in this method was originally modelled on the methods from MIL-HDBK-217F, Reliability Predictions of Electronic Equipment. It assumes that no reliability data is available on the actual units and devices for which the predictions are to be made, i.e. all predictions are based on generic reliability parameters.

Compatibility with RoHS requirements

The products are compatible with the relevant clauses and requirements of the RoHS directive 2002/95/EC and have a maximum concentration value of 0.1% by weight in homogeneous materials for lead, mercury, hexavalent chromium, PBB and PBDE and of 0.01% by weight in homogeneous materials for cadmium.

Exemptions in the RoHS directive utilized in Ericsson Power Modules products include:

- Lead in high melting temperature type solder (used to solder the die in semiconductor packages)
- Lead in glass of electronics components and in electronic ceramic parts (e.g. fill material in chip resistors)
- Lead as an alloying element in copper alloy containing up to 4% lead by weight (used in connection pins made of Brass)

Technical Specification

EN/LZT 146 405 R1B June 2009

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Quality Statement

The products are designed and manufactured in an industrial environment where quality systems and methods like ISO 9000, 6σ (sigma), and SPC are intensively in use to boost the continuous improvements strategy. Infant mortality or early failures in the products are screened out and they are subjected to an ATE-based final test. Conservative design rules, design reviews and product qualifications, plus the high competence of an engaged work force, contribute to the high quality of our products.

Warranty

Warranty period and conditions are defined in Ericsson Power Modules General Terms and Conditions of Sale.

Limitation of Liability

Ericsson Power Modules does not make any other warranties, expressed or implied including any warranty of merchantability or fitness for a particular purpose (including, but not limited to, use in life support applications, where malfunctions of product can cause injury to a person's health or life).

PKM 4402NG PIDP IBC 5:1 Ratio, Input 38-55 V, Output 63A / 480W

Safety Specification

General information

Ericsson Power Modules DC/DC converters and DC/DC regulators are designed in accordance with safety standards IEC/EN/UL60950, *Safety of Information Technology Equipment.*

IEC/EN/UL60950 contains requirements to prevent injury or damage due to the following hazards:

- Electrical shock
- Energy hazards
- Fire
- Mechanical and heat hazards
- Radiation hazards
- Chemical hazards

On-board DC-DC converters are defined as component power supplies. As components they cannot fully comply with the provisions of any Safety requirements without "Conditions of Acceptability". It is the responsibility of the installer to ensure that the final product housing these components complies with the requirements of all applicable Safety standards and Directives for the final product.

Component power supplies for general use should comply with the requirements in IEC60950, EN60950 and UL60950 *"Safety of information technology equipment"*.

There are other more product related standards, e.g. IEEE802.3af "Ethernet LAN/MAN Data terminal equipment power", and ETS300132-2 "Power supply interface at the input to telecommunications equipment; part 2: DC", but all of these standards are based on IEC/EN/UL60950 with regards to safety.

Ericsson Power Modules DC/DC converters and DC/DC regulators are UL60950 recognized and certified in accordance with EN60950.

The flammability rating for the board meets requirements for V-0 class material according to IEC 60695-11-10.

The products should be installed in the end-use equipment, in accordance with the requirements of the ultimate application. Normally the output of the DC/DC converter is considered as SELV (Safety Extra Low Voltage) and the input source must be isolated by minimum Double or Reinforced Insulation from the primary circuit (AC mains) in accordance with IEC/EN/UL60950.

Isolated DC/DC converters

It is recommended that a slow blow fuse with a rating twice the maximum input current per selected product be used at the input of each DC/DC converter. If an input filter is used in the circuit the fuse should be placed in front of the input filter.

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In the rare event of a component problem in the input filter or in the DC/DC converter that imposes a short circuit on the input source, this fuse will provide the following functions:

- Isolate the faulty DC/DC converter from the input power source so as not to affect the operation of other parts of the system.
- Protect the distribution wiring from excessive current and power loss thus preventing hazardous overheating.

The galvanic isolation is verified in an electric strength test. The test voltage (V_{iso}) between input and output is 1500 Vdc or 2250 Vdc for 60 seconds (refer to product specification).

Leakage current is less than 1 µA at nominal input voltage.

24 V DC systems

The input voltage to the DC/DC converter is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

48 and 60 V DC systems

If the input voltage to the DC/DC converter is 75 Vdc or less, then the output remains SELV (Safety Extra Low Voltage) under normal and abnormal operating conditions.

Single fault testing in the input power supply circuit should be performed with the DC/DC converter connected to demonstrate that the input voltage does not exceed 75 Vdc.

If the input power source circuit is a DC power system, the source may be treated as a TNV2 circuit and testing has demonstrated compliance with SELV limits and isolation requirements equivalent to Basic Insulation in accordance with IEC/EN/UL60950.

Non-isolated DC/DC regulators

The input voltage to the DC/DC regulator is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

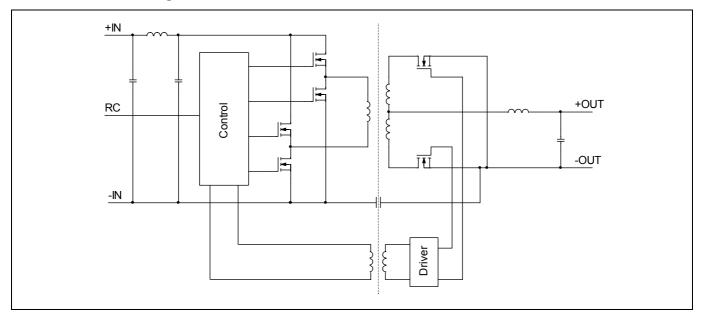
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Absolute Maximum Ratings

Chara	Characteristics		min	typ	max	Unit
T_{ref}	T _{ref} Operating Temperature (see Thermal Consideration section)		-40		+125	°C
Ts	Storage temperature		-55		+125	°C
VI	Input voltage		-0.5		+57	V
V_{iso}	Isolation voltage (input to output test voltage)				1500	Vdc
V _{tr}	Input voltage transient (t _p 500 ms)				60	V
V _{RC}	Remote Control pin voltage	{Positive logic option}	-0.5		15	V
▼ RC	(see Operating Information section)	{Negative logic option}	-0.5		15	V

Stress in excess of Absolute Maximum Ratings may cause permanent damage. Absolute Maximum Ratings, sometimes referred to as no destruction limits, are normally tested with one parameter at a time exceeding the limits of Output data or Electrical Characteristics. If exposed to stress above these limits, function and performance may degrade in an unspecified manner.

Fundamental Circuit Diagram



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9.6V/480W Electrical Specification

PKM 4402NG

5

 T_{ref} = -30 to +100°C, V_I = 38 to 55 V, P_O = 0 to 480 W unless otherwise specified under Conditions. Typical values given at: T_{ref} = +25°C, V_I = 48 V, max P_O, unless otherwise specified under Conditions

Chara	acteristics	Conditions	min	typ	max	Unit
Vı	Input voltage range	See Note 1	38		55	V
V _{loff}	Turn-off input voltage	Decreasing input voltage	29.0	30.1	31.7	V
Vlon	Turn-on input voltage	Increasing input voltage	30.8	32.4	33.9	V
Cı	Internal input capacitance			11		μF
		V ₁ = 55 V	0		480	W
Po	Output power	V ₁ = 48 V	0		480	W
		V ₁ = 38 V, see Note 2	0		460	W
		50 % of max P _o		97.0		
~	Efficiency	max Po		96.7		%
η	Efficiency	50 % of max P_0 , V_1 = 53 V		96.9		70
		max P _o , V _i = 53 V		97.0		
Pd	Power Dissipation	max Po		16.4	27.4	W
Pli	Input idling power	P ₀ = 0 W, V ₁ = 48 V		4	8	W
P _{RC}	Input standby power	V_1 = 48 V (turned off with RC)		100		mW
s	Switching frequency		100	125	150	kHz

V _{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V _I = 48 V, P _O = 0 W	9.50	9.53	9.65	V
	Output voltage tolerance band		7.08		11.10	V
Vo	Idling voltage	$P_0 = 0 W$	7.50		11.10	V
	Load regulation	from min P_0 to max P_0		0.31	0.46	V
V _{tr}	Load transient voltage deviation	V ₁ = 48 V, Load step 25-75-25 % of max P ₀ , di/dt = 5 A/us,		±0.7		V
t _{tr}	Load transient recovery time	see Note 3		0.1		mS
tr	Ramp-up time (from 10−90 % of V _{Oi})	max Po		4.5		ms
ts	Start-up time (from V _I connection to 90% of V _{OI})			6		ms
t _f	Shutdown fall time	max P _o		0.1		ms
ч	(from V_1 off to 10% of V_0)	P _o = 48 W		0.5		ms
	RC start-up time	max Po		6.1		ms
t _{RC}	RC shutdown fall time	max Po		0.2		ms
	(from RC off to 10% of $V_{\rm O})$	P _o = 48 W		0.5		ms
		V ₁ = 55 V, see Note 4	0		45	Α
lo	Output current	V ₁ = 48 V, see Note 4	0		52	Α
		V ₁ = 38 V	0		63	А
l _{lim}	Current limit threshold	T _{ref} < max T _{ref}	70	85	100	А
I _{sc}	Short circuit current	T _{ref} = 25°C, see Note 5		88	108	Α
V _{Oac}	Output ripple & noise	See ripple & noise section, max Po		90	250	mV _{p-p}
OVP	Input over voltage protection	$T_{ref} = +25^{\circ}C$	56.5	58		V

Note 1: The module withstands a input voltage of 57 V for 500 ms

Note 2: the maximum output current is limited to 63 \mbox{A}

Note 3: Output filter 10 x 47uF tantalum capacitors, estimated ESR<5 mOhm

Note 4: the maximum output power is limited to 480 W

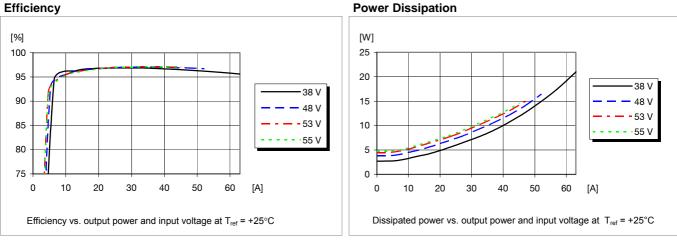
Note 5: RMS current in hick-up mode, measured over 3 $m\Omega$

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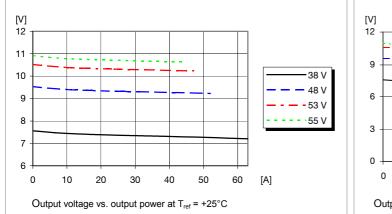
	Technical Specification	6
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Typical Characteristics

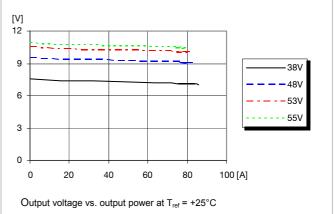
Efficiency



Output Characteristics



Current Limit Characteristics



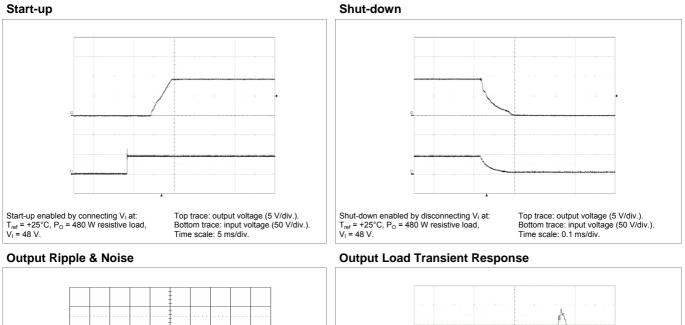
PKM 4402NG

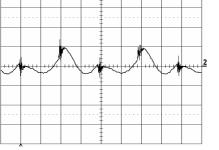
Technical Specification

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Typical Characteristics

PKM 4402NG





Output voltage ripple (50mV/div.) at: $T_{ref} = +25^{\circ}C$, $P_{O} = 480$ W resistive load, $V_{I} = 48$ V Time scale: 2 µs/div. See the filter in the Output ripple and noise section (EMC Specification).

Output voltage response to load current stepchange (120-360-120 W) at: T_{ref} =+25°C, V₁ = 48 V.

Top trace: output voltage (0.5 V/div.). Bottom trace: load current (20 A/div.). Time scale: 0.1 ms/div.

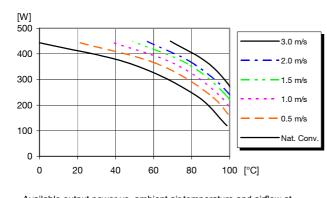
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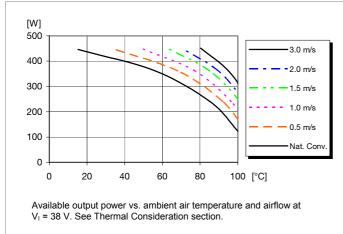
Typical Characteristics at V₁ = 38 V

Output Power Derating, open frame

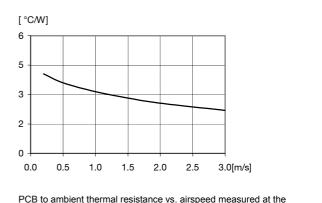


Available output power vs. ambient air temperature and airflow at V_I = 38 V. See Thermal Consideration section.

Output Power Derating, base plate option

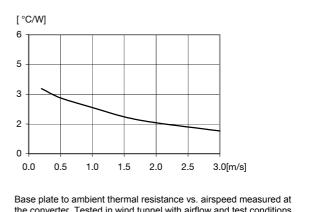


Thermal Resistance, open frame



PCB to ambient thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Thermal Resistance, base plate option



the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

8

PKM 4402NG

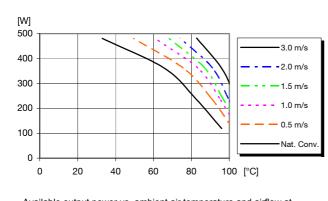
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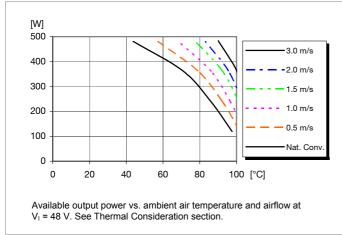
Typical Characteristics at V₁ = 48 V

Output Power Derating, open frame

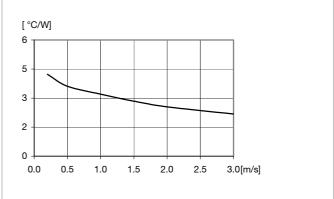


Available output power vs. ambient air temperature and airflow at V_I = 48 V. See Thermal Consideration section.

Output Power Derating, base plate option

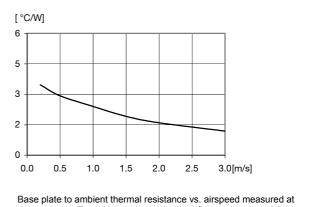


Thermal Resistance, open frame



PCB to ambient thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Thermal Resistance, base plate option



the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

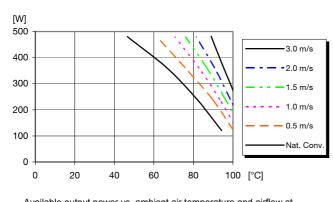
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PKM 4402NG

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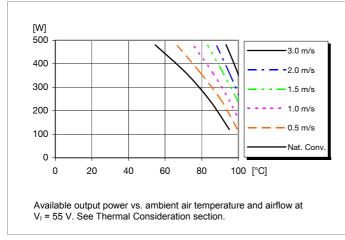
Typical Characteristics at V₁ = 55 V

Output Power Derating, open frame

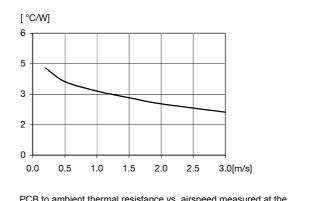


Available output power vs. ambient air temperature and airflow at V_I = 55 V. See Thermal Consideration section.

Output Power Derating, base plate option

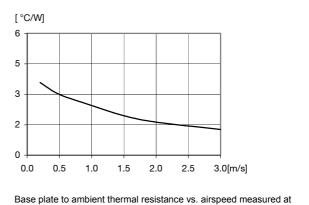


Thermal Resistance, open frame



PCB to ambient thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Thermal Resistance, base plate option



the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

PKM 4402NG

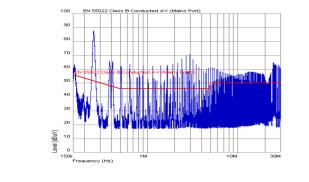
PKM 4402NG PIDP

IBC 5:1 Ratio, Input 38-55 V, Output 63A / 480W

EMC Specification

Conducted EMI measured according to EN55022, CISPR 22 and FCC part 15J. See document "Design Note 009" on the homepage for detailed information. The fundamental switching frequency is 125 kHz.

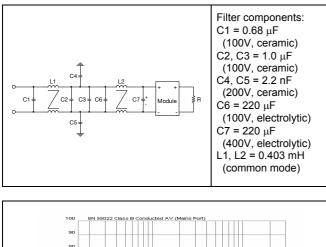
Conducted EMI Input terminal value (typ)

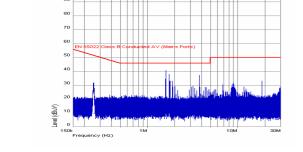


EMI without filter

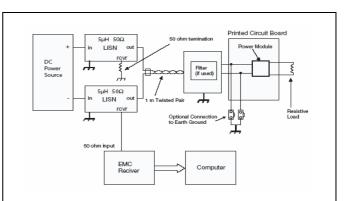
External filter (class B)

Required external input filter in order to meet class B in EN 55022, CISPR 22 and FCC part 15J.





EMI with filter



Test set-up

Layout recommendation

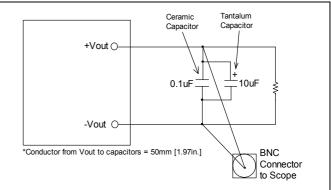
The radiated EMI performance of the DC/DC converter will depend on the users PCB layout and ground layer design. It is also important to consider the stand-off of the DC/DC converter.

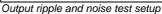
If a ground layer is used, it should be connected to the output of the DC/DC converter and the equipment ground or chassis.

A ground layer will increase the stray capacitance in the PCB and improve the high frequency EMC performance.

Output ripple and noise

Output ripple and noise measured according to figure below. See document "Design Note 022" on the homepage for detailed information.





EN/LZT 146 405 R1B June 2009

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IBC 5:1 Ratio, Input 38-55 V, Output 63A / 480W

Operating information

PKM 4402NG PIDP

Input Voltage

The input voltage range of the DC/DC converters is especially adapted to meet the requirements of non-battery backup -48 V systems.

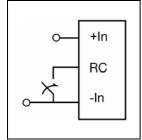
At input voltages exceeding 48 V, the power loss will be higher than at normal input voltage and T_{ref} must be limited to absolute max +125°C.

Turn-off Input Voltage

The DC/DC converters monitor the input voltage and will turn on and turn off at predetermined levels.

The minimum hysteresis between on and off input voltage is 1.0 V.

Remote Control (RC)



The products are fitted with a remote control function referenced to the primary negative input connection (- In), with positive logic option available. The RC function allows the product to be turned on/off by an external device like a semiconductor or mechanical switch. The RC pin has an internal pull up resistor to + In.

The maximum required sink current is less than 1 mA. When the RC pin is left open, the voltage generated on the RC pin is 10 V. The second option is "positive logic" remote control, which can be ordered by adding the suffix "P" to the end of the part number. The DC/DC converter will turn on when the input voltage is applied with the RC pin open. Turn off is achieved by connecting the RC pin to the - In. To ensure safe turn off the voltage difference between RC pin and the - In pin shall be less than 0.8 V. The DC/DC converter will restart automatically when this connection is opened. Design note 21 explains more in detail about the RC pin.

Input and Output Impedance

The impedance of both the input source and the load will interact with the impedance of the DC/DC converter. It is important that the input source has low characteristic impedance. Minimum recommended external input capacitance is 100 µF. The performance in some applications can be enhanced by addition of external capacitance as described under External Decoupling Capacitors.

External Decoupling Capacitors

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When powering loads with significant dynamic current requirements, the voltage regulation at the point of load can be improved by addition of decoupling capacitors at the load. The most effective technique is to locate low ESR ceramic and electrolytic capacitors as close to the load as possible, using several parallel capacitors to lower the effective ESR. The ceramic capacitors will handle high-frequency dynamic load changes while the electrolytic capacitors are used to handle low frequency dynamic load changes. Ceramic capacitors will also reduce any high frequency noise at the load. It is equally important to use low resistance and low inductance PCB layouts and cabling.

The absolute maximum value of output capacitance is 6000 uF.

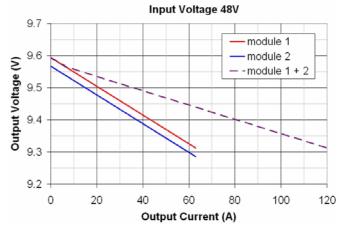
For further information please contact your local Ericsson Power Modules representative.

Parallel Operation

With the same input voltage and an output contact/trace resistance of 1 m Ω or higher, the converters may be paralleled for redundancy if the total current is equal to or less than $n \times 0.95 \times I_0$ max.

For best result, trace resistance and module cooling must be symmetrical. At this condition, the modules current share within 5% at the maximum load.

No external components are required for parallel operation or load sharing.



Over Temperature Protection (OTP)

The dc/dc converters are protected from thermal overload by an internal over temperature shutdown circuit. When T_{ref} as defined in thermal consideration section exceeds 125°C the DC/DC converter will shut down. The DC/DC converter will make continuous attempts to start up (non-latching mode) and resume normal operation automatically when the temperature has dropped below the temperature threshold.

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Over Voltage Protection (OVP)

The DC/DC converters have over voltage protection that will shut down the DC/DC converter in over voltage conditions. The DC/DC converter will make continuous attempts to start up (non-latching mode) and resume normal operation automatically after removal of the over voltage condition. The output voltage depends on the input voltage. The internal OVP circuit detects the input voltage and is activated at an input voltage threshold between the maximum and absolute maximum level.

Over Current Protection (OCP)

The DC/DC converters include current limiting circuitry for protection at continuous overload.

The DC/DC converters will go into hiccup mode for output currents in excess of max output current (max I_o). The output will go through repeated cycles of shut-down and restart with a duty cycle of 7 ms on and 21 ms off. During the 7 ms on and a load of 3 m Ω the output current will reach approximately 190 A. The DC/DC converter will resume normal operation after removal of the overload. The load distribution should be designed for the maximum output short circuit current specified.

PKM 4402NG PIDP	EN/LZT 146 405 R1B June 2009
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Thermal Consideration

General

The DC/DC converters are designed to operate in different thermal environments and sufficient cooling must be provided to ensure reliable operation.

Cooling is achieved mainly by conduction, from the pins to the host board, and convection, which is dependant on the airflow across the DC/DC converter. Increased airflow enhances the cooling of the DC/DC converter.

The Output Current Derating graph found in the Output section for each model provides the available output current vs. ambient air temperature and air velocity at V_{in} = 48 V.

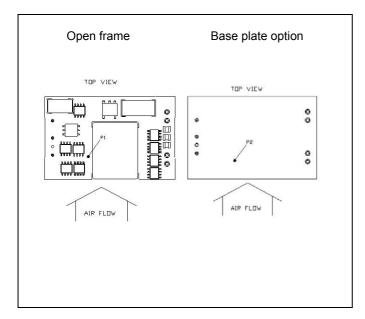
The DC/DC converter is tested on a 254 x 254 mm, 35 μ m (1 oz), 16-layer test board mounted vertically in a wind tunnel with a cross-section of 305 x 305 mm.

Proper cooling of the DC/DC converter can be verified by measuring the temperature at position P1. The temperature should not exceed the max value provided in the table below.

Note that the max value is the absolute maximum rating (non destruction) and that the electrical Output data is guaranteed up to T_{ref} +100°C.

See document "Design Note 019" on the homepage for detailed information.

Position	Device	Designation	max value
P1	PCB	T _{ref}	+125° C
P2	Baseplate	T _{baseplate}	+100° C



Definition of reference temperature (T_{ref})

The reference temperature is used to monitor the temperature limits of the product. Temperatures above maximum T_{ref} are not allowed and may cause degradation or permanent damage to the product. T_{ref} is also used to define the temperature range for normal operating conditions.

 T_{ref} is defined by the design and used to guarantee safety margins, proper operation and high reliability of the module.

	•	
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IBC 5:1 Ratio, Input 38-55 V, Output 63A / 480W	© Ericsson AB	

Connections

01	70 60
0 ²	
	5
	40

Pin	Designation	Function
1	+In	Positive input
2	RC	Remote Control To turn on and turn off the output
3	-In	Negative input
4	+Out	Positive output
5	-Out	Negative output
6	+Out	Positive output
7	-Out	Negative output

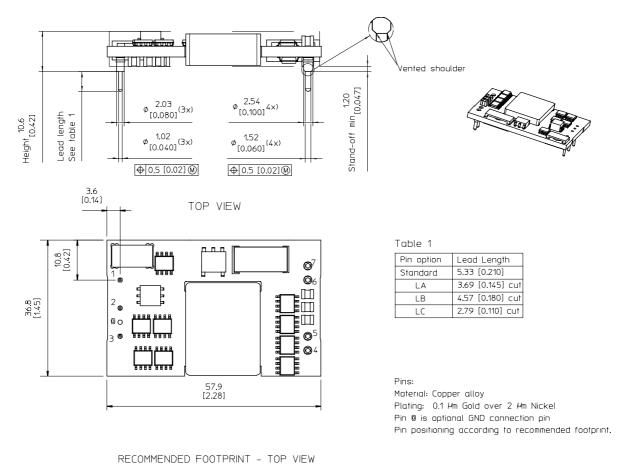
PKM 4402NG PIDP

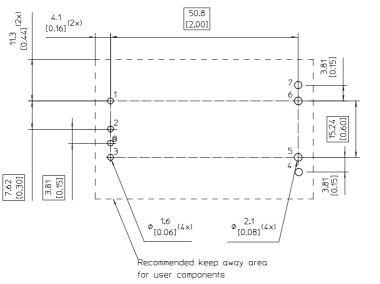
IBC 5:1 Ratio, Input 38-55 V, Output 63A / 480W

Technical Specification EN/LZT 146 405 R1B June 2009 © Ericsson AB

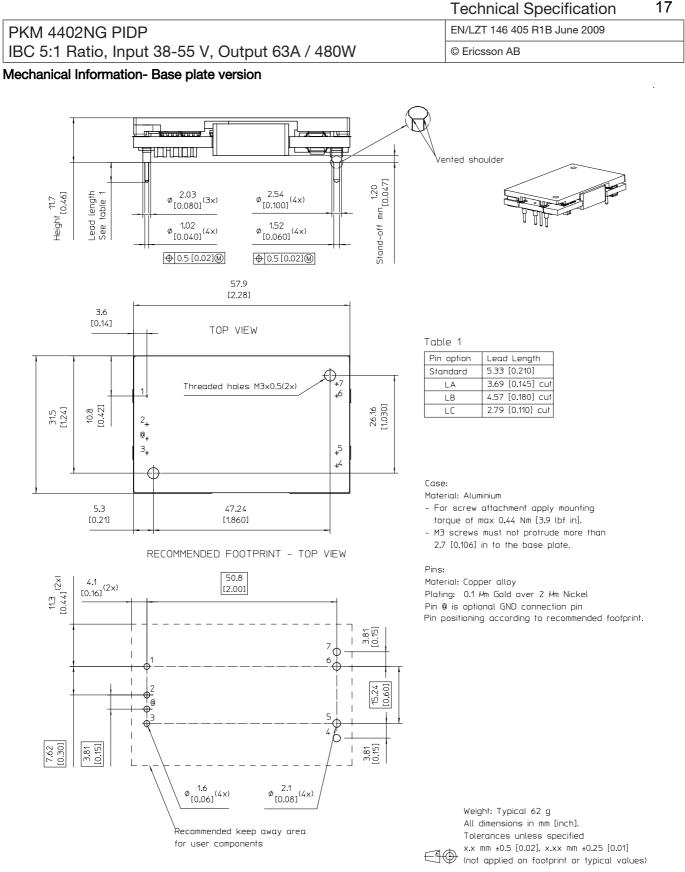
16

Mechanical Information





Weight: Typical 45 g All dimensions in mm [inch]. Tolerances unless specified x.x mm ±0.5 [0.02], x.xx mm ±0.25 [0.01] (not applied on footprint or typical values)



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Soldering Information – Through Hole Mounting

The product is intended for manual or wave soldering. When wave soldering is used, the temperature on the pins is specified to maximum 270°C for maximum 10 seconds.

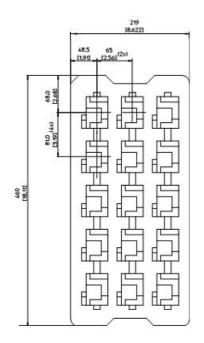
A maximum preheat rate of 4° C/s and a temperature of max $+150^{\circ}$ C is suggested. When soldering by hand, care should be taken to avoid direct contact between the hot soldering iron tip and the pins for more than a few seconds in order to prevent overheating.

A no-clean flux is recommended to avoid entrapment of cleaning fluids in cavities inside the product or between the product and the host board. The cleaning residues may affect long time reliability and isolation voltage.

Delivery Package Information

The products are delivered in antistatic trays

Tray Specifications		
Material	Antistatic PE foam	
Surface resistance	10 ⁵ < Ohm/square < 10 ¹²	
Bakability	The trays are not bakable	
Tray capacity	20 products/tray	
Tray thickness	26 mm [1.024 inch]	
Box capacity	60 products (3 full trays/box)	
Tray weight	Open frame version 140 g empty, 1040 g full tray Base plate version 140 g empty, 1380 g full tray	



PKM 4402NG PIDP

IBC 5:1 Ratio, Input 38-55 V, Output 63A / 480W

EN/LZT 146 405 R1B June 2009

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Product Qualification Specification

Characteristics			
External visual inspection	IPC-A-610		
Change of temperature (Temperature cycling)	IEC 60068-2-14 Na	Temperature range Number of cycles Dwell/transfer time	-40 to +100°C 1000 15 min/0-1 min
Cold (in operation)	IEC 60068-2-1 Ad	Temperature T _A Duration	-45°C 72 h
Damp heat	IEC 60068-2-67 Cy	Temperature Humidity Duration	+85°C 85 % RH 1000 hours
Dry heat	IEC 60068-2-2 Bd	Temperature Duration	+125°C 1000 h
Electrostatic discharge susceptibility	IEC 61340-3-1, JESD 22-A114 IEC 61340-3-2, JESD 22-A115	Human body model (HBM) Machine Model (MM)	Class 2, 2000 V Class 3, 200 V
Immersion in cleaning solvents	IEC 60068-2-45 XA Method 2	Water Glycol ether Isopropyl alcohol	+55°C +35°C +35°C
Mechanical shock	IEC 60068-2-27 Ea	Peak acceleration Duration	100 g 6 ms
Operational life test	MIL-STD-202G method 108A	Duration	1000 h
Resistance to soldering heat	IEC 60068-2-20 Tb Method 1A	Solder temperature Duration	270°C 10-13 s
Robustness of terminations	IEC 60068-2-21 Test Ua1	Through hole mount products	All leads
Solderability	IEC 60068-2-20 test Ta ²	Preconditioning Temperature, SnPb Eutectic Temperature, Pb-free	Steam ageing 235°C 245°C
Vibration, broad band random	IEC 60068-2-64 Fh, method 1	Frequency Spectral density Duration	10 to 500 Hz 0.07 g ² /Hz 10 min in each perpendicular direction