Technical Specification

PKD 4000 ASI series DC/DC converters, Input 36-75 V, Output 20 A/50 W	EN/LZT 146 346 R1A April 2006 © Ericsson Power Modules AB

Key Features

- Low profile SMD within Quarter-brick size 50.4 x 46.3 x 7.0 mm (1.98 x 1.82 x 0.276 in.)
- Low profile, max 7.5mm (0.30 in.)
- High efficiency, typ. 90 % at 3.3 Vout full load
- 1500 Vdc input to output isolation
- Meets isolation requirements equivalent to basic insulation according to IEC/EN/UL 60950
- More than 1.7 million hours MTBF

General Characteristics

- Suited for narrow board pitch applications (15 mm/0.6 in)
- Input under voltage shut-down
- Over temperature protection
- Soft start
- Output short-circuit protection
- Remote sense
- Remote control
- Output voltage adjust function
- Highly automated manufacturing ensures quality
- ISO 9001/14001 certified supplier

Safety Approvals

Design for Environment

Meets requirements in high-temperature lead-free soldering processes.

Contents

General Information

Ordering Information

See Contents for individual product ordering numbers.

Reliability

The Mean Time Between Failure (MTBF) is calculated at full output power and an operating ambient temperature (T_A) of +40°C, which is a typical condition in Information and Communication Technology (ICT) equipment. Different methods could be used to calculate the predicted MTBF and failure rate which may give different results. Ericsson Power Modules currently uses Telcordia SR332.

Predicted MTBF for the series is:
- 1.79 million hours according to Telcordia SR332, issue 1, Black box technique.

Telcordia SR332 is a commonly used standard method intended for reliability calculations in ICT equipment. The parts count procedure used in this method was originally modelled on the methods from MIL-HDBK-217F, Reliability Predictions of Electronic Equipment. It assumes that no reliability data is available on the actual units and devices for which the predictions are to be made, i.e. all predictions are based on generic reliability parameters.

Compatibility with RoHS requirements

The products are compatible with the relevant clauses and requirements of the RoHS directive 2002/95/EC and have a maximum concentration value of 0.1% by weight in homogeneous materials for lead in other applications other than lead in solder, lead in high melting temperature type solder, lead in glass of electronics components, lead in electronic ceramic parts and lead as an alloying element in copper containing up to 4% lead by weight, mercury, hexavalent chromium, PBB and PBDE and of 0.01% by weight in homogeneous materials for cadmium.

Exemptions in the RoHS directive utilized in the products:
- Lead as an alloying element in copper alloy containing up to 4% lead by weight (used in connection pins made of Brass)
- Lead in high melting temperature type solder (used to solder the die in semiconductor packages)
- Lead in glass of electronics components and in electronic ceramic parts (e.g. fill material in chip resistors)
- Lead in solder for servers, storage and storage array systems, network infrastructure equipment for switching, signaling, transmission as well as network management for telecommunication
(Note: the products are manufactured in lead-free soldering processes and the lead present in the solder is only located in the terminal plating finishes on some components)

Quality Statement

The products are designed and manufactured in an industrial environment where quality systems and methods like ISO 9000, 6σ (sigma), and SPC are intensively in use to boost the continuous improvements strategy. Infant mortality or early failures in the products are screened out and they are subjected to an ATE-based final test. Conservative design rules, design reviews and product qualifications, plus the high competence of an engaged work force, contribute to the high quality of our products.

Warranty

Warranty period and conditions are defined in Ericsson Power Modules General Terms and Conditions of Sale.

Limitation of Liability

Ericsson Power Modules does not make any other warranties, expressed or implied including any warranty of merchantability or fitness for a particular purpose (including, but not limited to, use in life support applications, where malfunctions of product can cause injury to a person's health or life).

Safety Specification

General information

Ericsson Power Modules DC/DC converters and DC/DC regulators are designed in accordance with safety standards IEC/EN/UL60950, *Safety of Information Technology Equipment*.

IEC/EN/UL60950 contains requirements to prevent injury or damage due to the following hazards:

- Electrical shock
- Energy hazards
- Fire
- Mechanical and heat hazards
- Radiation hazards
- Chemical hazards

On-board DC-DC converters are defined as component power supplies. As components they cannot fully comply with the provisions of any Safety requirements without "Conditions of Acceptability". It is the responsibility of the installer to ensure that the final product housing these components complies with the requirements of all applicable Safety standards and Directives for the final product.

Component power supplies for general use should comply with the requirements in IEC60950, EN60950 and UL60950 *"Safety of information technology equipment".*

There are other more product related standards, e.g. IEEE802.3af "Ethernet LAN/MAN Data terminal equipment power", and ETS300132-2 "Power supply interface at the input to telecommunications equipment; part 2: DC", but all of these standards are based on IEC/EN/UL60950 with regards to safety.

Ericsson Power Modules DC/DC converters and DC/DC regulators are UL60950 recognized and certified in accordance with EN60950.

The flammability rating for all construction parts of the products meets requirements for V-0 class material according to IEC 60695-11-10.

The products should be installed in the end-use equipment, in accordance with the requirements of the ultimate application. Normally the output of the DC/DC converter is considered as SELV (Safety Extra Low Voltage) and the input source must be isolated by minimum Double or Reinforced Insulation from the primary circuit (AC mains) in accordance with IEC/EN/UL60950.

Isolated DC/DC converters

It is recommended that a slow blow fuse with a rating twice the maximum input current per selected product be used at the input of each DC/DC converter. If an input filter is used in the circuit the fuse should be placed in front of the input filter.
In the rare event of a component problem in the input filter or in the DC/DC converter that imposes a short circuit on the input source, this fuse will provide the following functions:

- Isolate the faulty DC/DC converter from the input power source so as not to affect the operation of other parts of the system.
- Protect the distribution wiring from excessive current and power loss thus preventing hazardous overheating.

The galvanic isolation is verified in an electric strength test. The test voltage (V_{iso}) between input and output is 1500 Vdc or 2250 Vdc for 60 seconds (refer to product specification).
Leakage current is less than 1 μA at nominal input voltage.

24 V DC systems

The input voltage to the DC/DC converter is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

48 and 60 V DC systems

If the input voltage to Ericsson Power Modules DC/DC converter is 75 Vdc or less, then the output remains SELV (Safety Extra Low Voltage) under normal and abnormal operating conditions.

Single fault testing in the input power supply circuit should be performed with the DC/DC converter connected to demonstrate that the input voltage does not exceed 75 Vdc.

If the input power source circuit is a DC power system, the source may be treated as a TNV2 circuit and testing has demonstrated compliance with SELV limits and isolation requirements equivalent to Basic Insulation in accordance with IEC/EN/UL60950.

Non-isolated DC/DC regulators

The input voltage to the DC/DC regulator is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

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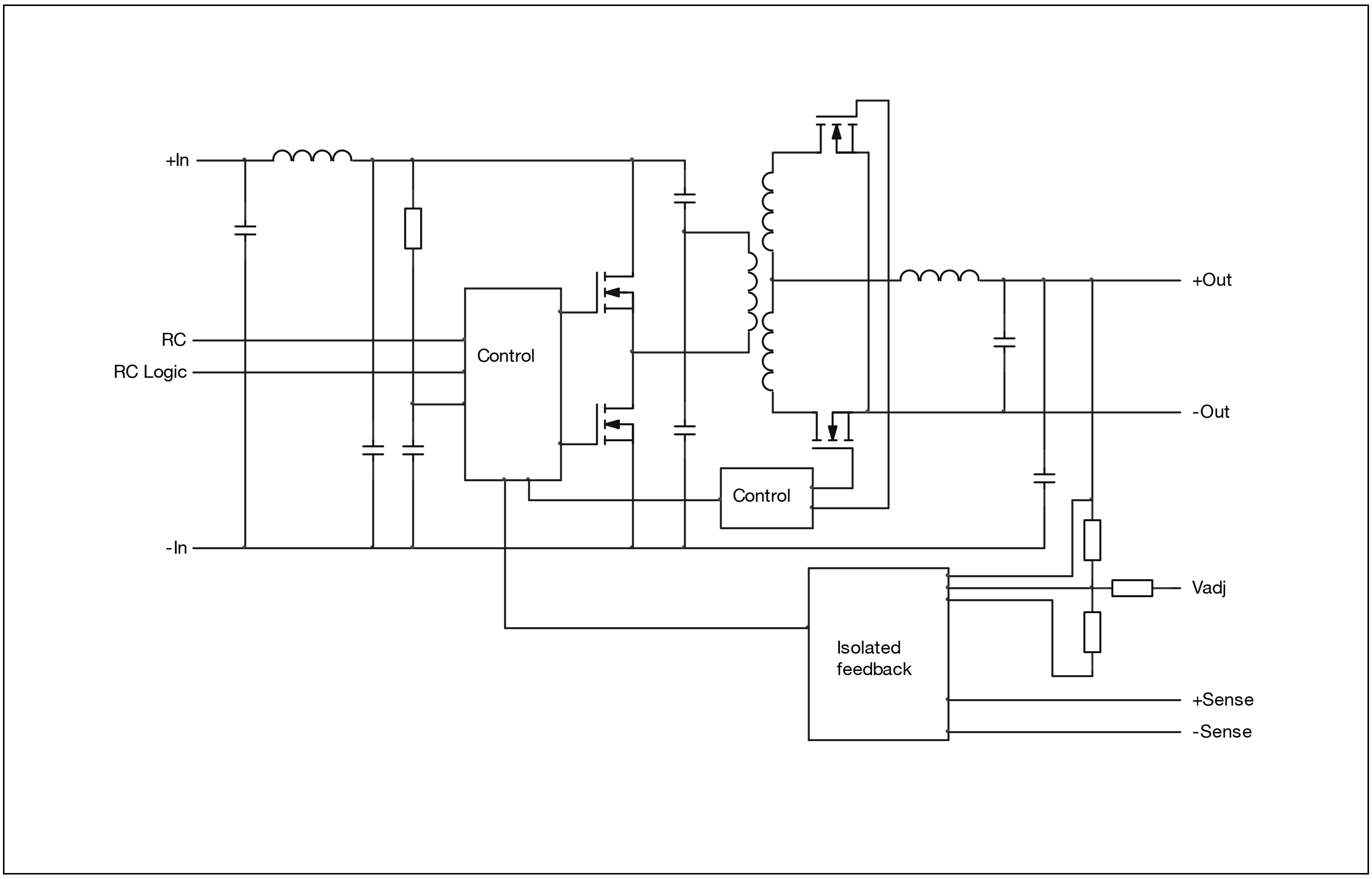
Absolute Maximum Ratings

Characteristics			min	typ	max	Unit
T_{ref}	Operating Temperature (see Thermal Consideration section)		-45		+110	°C
T_S	Storage temperature		-55		+125	°C
V_I	Input voltage		-0.5		+80	V
V_{iso}	Isolation voltage (input to output test voltage)		1500			V
V_{tr}	Input voltage transient (Tp 100 ms)				100	V
V_{RC}	Remote Control pin voltage (see Operating Information section)	Positive logic option	-0.5		9	V
V_{adj}	Adjust pin voltage (see Operating Information section)		-0.5		$2xV_{oi}$	V

Stress in excess of Absolute Maximum Ratings may cause permanent damage. Absolute Maximum Ratings, sometimes referred to as no destruction limits, are normally tested with one parameter at a time exceeding the limits of Output data or Electrical Characteristics. If exposed to stress above these limits, function and performance may degrade in an unspecified manner.

Fundamental Circuit Diagram



1.2 V/20 A Electrical Specification PKD 4218L ASI

T_{ref} = -25 to +90ºC, V_I = 36 to 75 V, sense pins connected to output pins unless otherwise specified under Conditions.
Typical values given at: T_{ref} = +25°C, V_I= 53 V, max I_O , unless otherwise specified under Conditions.

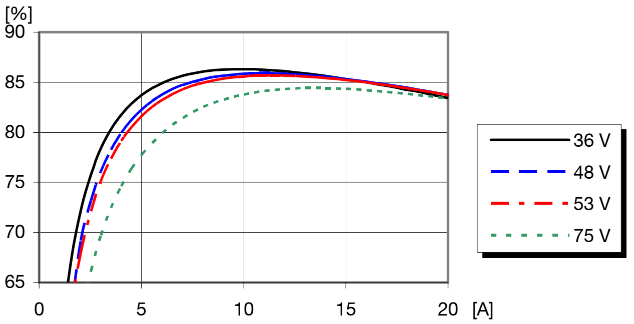
Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		36		75	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	29			V
V_{Ion}	Turn-on input voltage	Increasing input voltage			36	V
C_I	Internal input capacitance			2.5		μF
P_O	Output power	Output voltage initial setting	0		24	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sinewave, 1 Vp-p		66		dB
η	Efficiency	50 % of max I_O		85.5		%
		max I_O		83.5		
		50 % of max I_O , V_I = 48 V		86.0		
		max I_O , V_I = 48 V		83.5		
P_d	Power Dissipation	max I_O		4.7		W
P_{li}	Input idling power	I_O = 0 A, V_I = 53 V		2		W
P_{RC}	Input standby power	V_I = 53 V (turned off with RC)		1		W
f_s	Switching frequency	0-100 % of max I_O	162	180	198	kHz

V_{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V_I = 53 V, I_O = 20 A	1.19	1.20	1.21	V
V_O	Output adjust range	See operating information	1.08		1.32	V
	Output voltage tolerance band	10-100% of max I_O	1.16		1.24	V
	Idling voltage	I_O = 0 A	1.16		1.24	V
	Line regulation	max I_O			5	mV
	Load regulation	V_I = 53 V, 1-100% of max I_O			5	mV
V_{tr}	Load transient voltage deviation	V_I = 53 V, Load step 25-75-25 % of max I_O, di/dt = 1 A/μs		±350		mV
t_{tr}	Load transient recovery time			100		μs
t_r	Ramp-up time (from 10–90 % of V_{Oi})	10-100% of max I_O		6		ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})			8		ms
t_f	Vin shutdown fall time (from V_I off to 10% of V_O)	max I_O		N/A		ms
		I_O = 0 A		N/A		s
t_{RC}	RC start-up time	max I_O		N/A		ms
	RC shutdown fall time (from RC off to 10% of V_O)	max I_O		N/A		ms
		I_O = 0 A		N/A		s
I_O	Output current		0		20	A
I_{lim}	Current limit threshold	T_{ref} < max T_{ref}		25		A
I_{sc}	Short circuit current	T_{ref} = 25ºC, V_O < 0.5 V			30	A
V_{Oac}	Output ripple & noise	See ripple & noise section, V_I = 53 V, I_O = 20 A		75	100	mVp-p

1.2 V/20 A Typical Characteristics

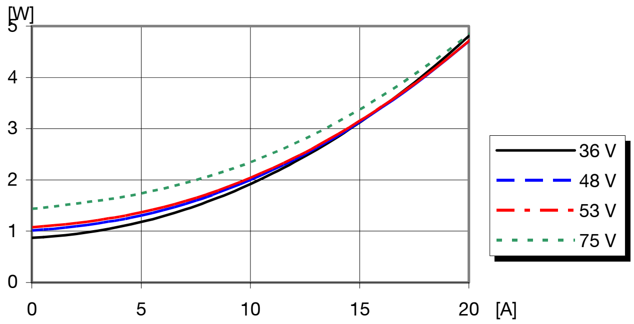
PKD 4218L ASI

Efficiency



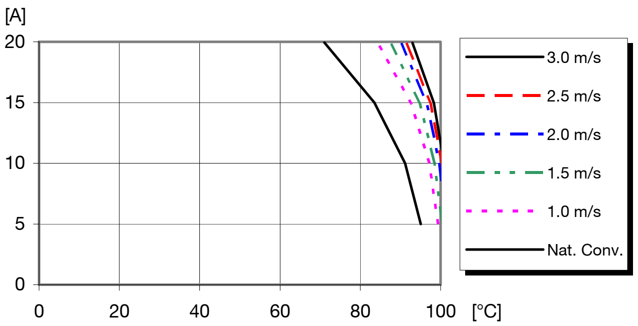
Efficiency vs. load current and input voltage at T_{ref} = +25°C

Power Dissipation



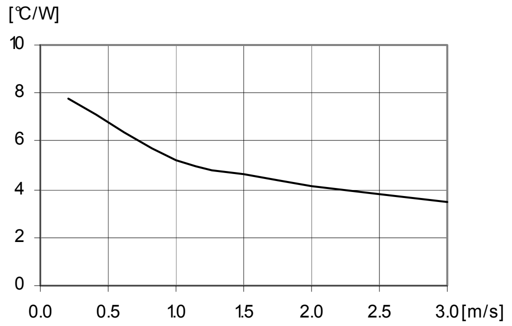
Dissipated power vs. load current and input voltage at T_{ref} = +25°C

Output Current Derating



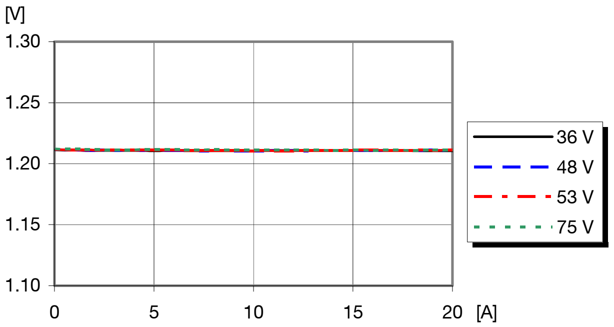
Available load current vs. ambient air temperature and airflow at V_I = 53 V. See Thermal Consideration section.

Thermal Resistance



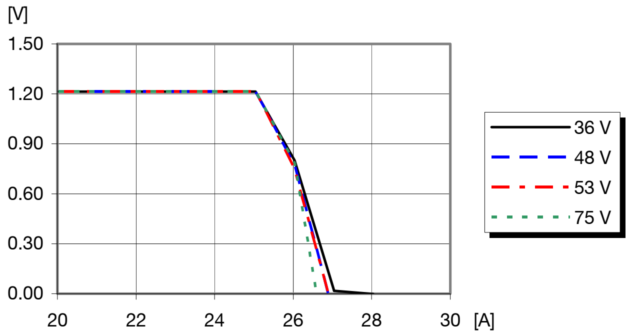
Thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Output Characteristics



Output voltage vs. load current at T_{ref} = +25°C

Current Limit Characteristics

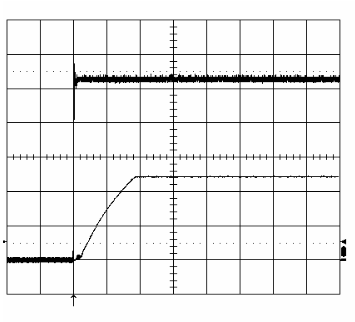


Output voltage vs. load current at I_O > max I_O, T_{ref} = +25°C

1.2 V/20 A Typical Characteristics

PKD 4218L ASI

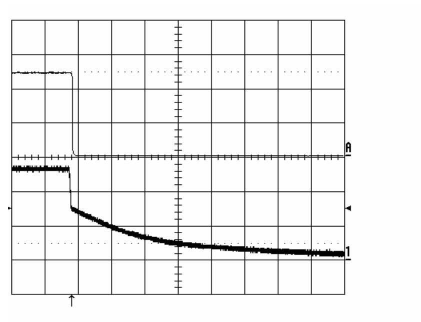
Start-up



Start-up enabled by connecting V_I at:
T_{ref} = +25°C, V_I = 53 V,
I_O = 20 A resistive load.

Top trace: intput voltage (10 V/div).
Bottom trace: output voltage (0.5 V/div).
Time scale: 5 ms/div.

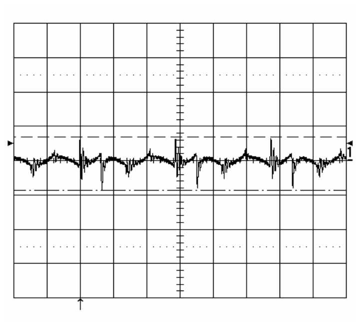
Shut-down



Shut-down enabled by disconnecting V_I at:
T_{ref} = +25°C, V_I = 53 V,
I_O = 20 A resistive load.

Top trace: output voltage (0.5 V/div).
Bottom trace: input voltage (20 V/div).
Time scale: 2 ms/div.

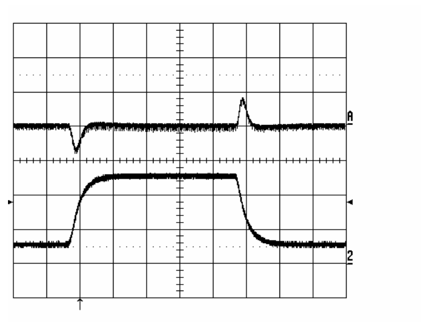
Output Ripple & Noise



Output voltage ripple at:
T_{ref} = +25°C, V_I = 53 V,
I_O = 20 A resistive load.

Trace: output voltage (50mV/div).
Time scale: 2 µs/div.

Output Load Transient Response



Output voltage response to load current step-change (2.5-12.5-2.5 A)) at:
T_{ref} =+25°C, V_I = 53 V.

Top trace: output voltage (100mV/div).
Bottom trace: load current (5 A/div).
Time scale: 0.1 ms/div.

Output Voltage Adjust (see operating information)

Passive adjust
The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:
R_{adj}= 6 / ((V_{out} / 1.2) - 1) - 40 kΩ

Example: Increase 4% =>V_{out} = 1.248 Vdc
6 / ((1.248 / 1.2) - 1) – 40 = 110 kΩ

Output Voltage Adjust Downwards, Decrease:
R_{adj}= ((15 x V_{out} – 7.2) / (1.2 – V_{out})) – 40 kΩ

Example: Decrease 2% =>V_{out} = 1.176 Vdc
((15 x 1.176 - 7.2) / (1.2 – 1.176)) – 40 = 395 kΩ

PKD 4000 ASI series DC/DC converters, Input 36-75 V, Output 20 A/50 W	EN/LZT 146 346 R1A April 2006 © Ericsson Power Modules AB

3.3 V/15 A Electrical Specification

PKD 4510 ASI

T_{ref} = -25 to +90ºC, V_I = 36 to 75 V, sense pins connected to output pins unless otherwise specified under Conditions.
Typical values given at: T_{ref} = +25°C, V_I= 53 V, max I_O , unless otherwise specified under Conditions.

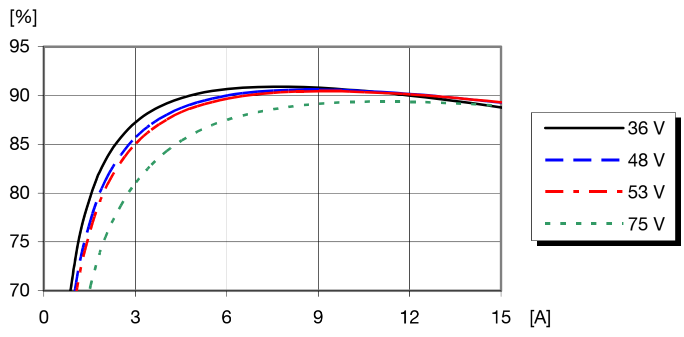
Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		36		75	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	29			V
V_{Ion}	Turn-on input voltage	Increasing input voltage			36	V
C_I	Internal input capacitance			2.5		μF
P_O	Output power	Output voltage initial setting	0		50	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sinewave, 1 Vp-p		71		dB
η	Efficiency	50 % of max I_O		90.5		%
		max I_O		89.5		
		50 % of max I_O , V_I = 48 V		90.5		
		max I_O , V_I = 48 V		89.5		
P_d	Power Dissipation	max I_O		6		W
P_{li}	Input idling power	I_O = 0 A, V_I = 53 V		2		W
P_{RC}	Input standby power	V_I = 53 V (turned off with RC)		1		W
f_s	Switching frequency	0-100 % of max I_O	162	180	198	kHz

V_{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V_I = 53 V, I_O = 15 A	3.28	3.30	3.32	V
V_O	Output adjust range	See operating information	2.97		3.63	V
	Output voltage tolerance band	10-100% of max I_O	3.25		3.35	V
	Idling voltage	I_O = 0 A	3.25		3.35	V
	Line regulation	max I_O			5	mV
	Load regulation	V_I = 53 V, 0-100% of max I_O			5	mV
V_{tr}	Load transient voltage deviation	V_I = 53 V, Load step 25-75-25 % of max I_O, di/dt = 1 A/μs		±250		mV
t_{tr}	Load transient recovery time			100		μs
t_r	Ramp-up time (from 10–90 % of V_{Oi})	10-100% of max I_O		6		ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})			8		ms
t_f	Vin shutdown fall time (from V_I off to 10% of V_O)	max I_O		N/A		ms
		I_O = 0 A		N/A		s
t_{RC}	RC start-up time	max I_O		N/A		ms
	RC shutdown fall time (from RC off to 10% of V_O)	max I_O		N/A		ms
		I_O = 0 A		N/A		s
I_O	Output current		0		15	A
I_{lim}	Current limit threshold	T_{ref} < max T_{ref}		20		A
I_{sc}	Short circuit current	T_{ref} = 25ºC, V_O < 0.5 V			25	A
V_{Oac}	Output ripple & noise	See ripple & noise section, V_I = 53 V, I_O = 20 A		50	100	mVp-p

3.3 V/15 A Typical Characteristics

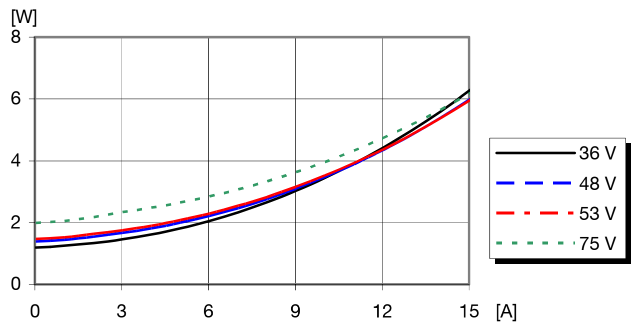
PKD 4510 ASI

Efficiency



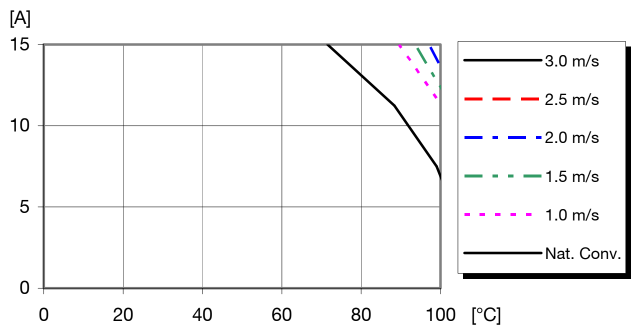
Efficiency vs. load current and input voltage at T_{ref} = +25°C

Power Dissipation



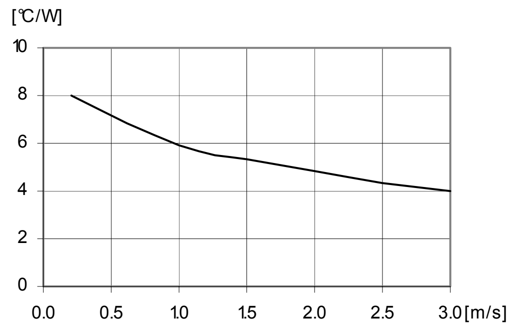
Dissipated power vs. load current and input voltage at T_{ref} = +25°C

Output Current Derating



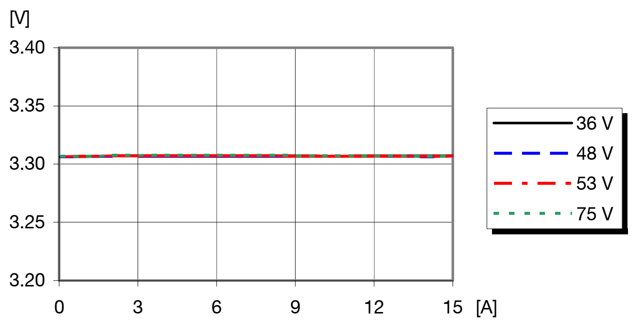
Available load current vs. ambient air temperature and airflow at V_I = 53 V. See Thermal Consideration section.

Thermal Resistance



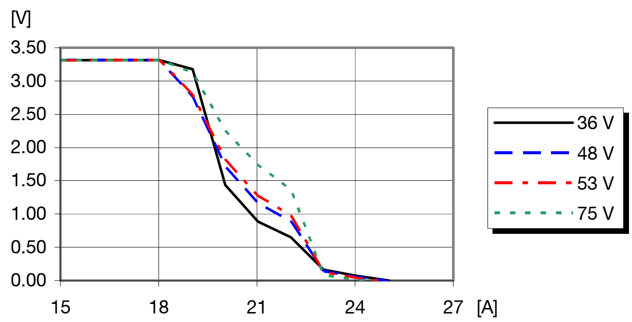
Thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Output Characteristics



Output voltage vs. load current at T_{ref} = +25°C

Current Limit Characteristics

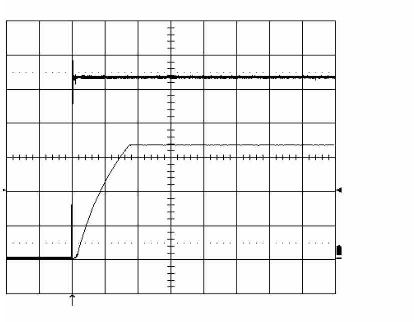


Output voltage vs. load current at I_O > max I_O , T_{ref} = +25°C

3.3 V/15 A Typical Characteristics

PKD 4510 ASI

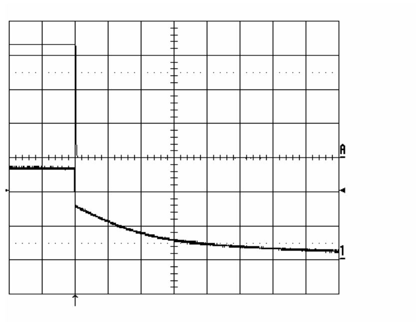
Start-up



Start-up enabled by connecting V_I at:
T_{ref} = +25°C, V_I = 53 V,
I_O = 15 A resistive load.

Top trace: intput voltage (10 V/div).
Bottom trace: output voltage (1 V/div).
Time scale: 5 ms/div.

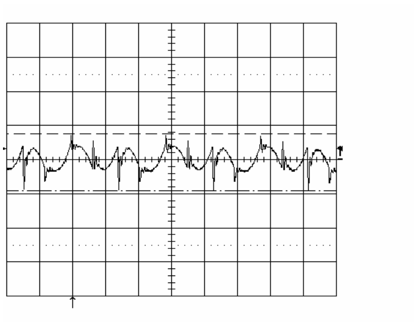
Shut-down



Shut-down enabled by disconnecting V_I at:
T_{ref} = +25°C, V_I = 53 V,
I_O = 15 A resistive load.

Top trace: output voltage (1 V/div).
Bottom trace: input voltage (20 V/div).
Time scale: 2 ms/div.

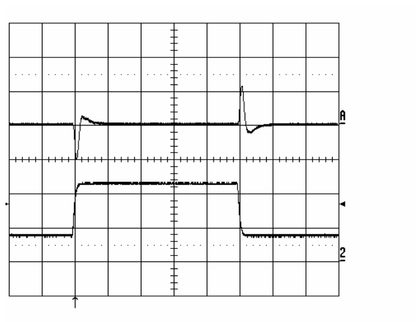
Output Ripple & Noise



Output voltage ripple at:
T_{ref} = +25°C, V_I = 53 V,
I_O = 15 A resistive load.

Trace: output voltage (20mV/div).
Time scale: 2 μs/div.

Output Load Transient Response



Output voltage response to load current step-change (3.75-11.25-3.75 A}) at:
T_{ref} =+25°C, V_I = 53 V.

Top trace: output voltage (200mV/div).
Bottom trace: load current (5 A/div).
Time scale: 0.1 ms/div.

Output Voltage Adjust (see operating information)

Passive adjust
The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:
R_{adj}= 6 / ((V_{out} / 3.3) - 1) - 40 kΩ

Example: Increase 4% =>V_{out} = 3.432 Vdc
6 / ((3.432 / 3.3) - 1) – 40 = 110 kΩ

Output Voltage Adjust Downwards, Decrease:
R_{adj}= ((15 x V_{out} – 19.8) / (3.3 – V_{out})) – 40 kΩ

Example: Decrease 2% =>V_{out} = 3.234 Vdc
((15 x 3.234 – 19.8) / (3.3 – 3.234)) – 40 = 395 kΩ

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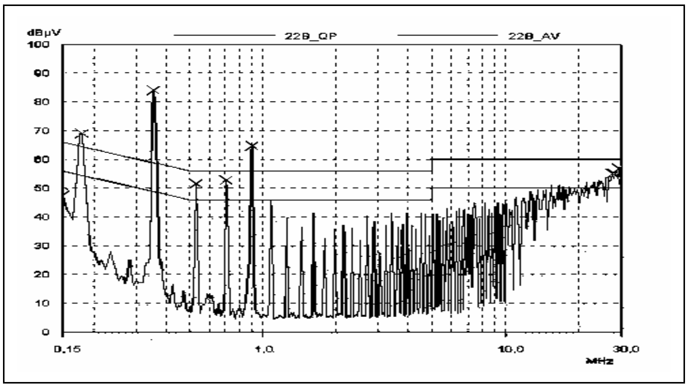
EMC Specification

Conducted EMI measured according to EN55022, CISPR 22 and FCC part 15J (see test set-up). See Design Note 009 for further information. The fundamental switching frequency is 180 kHz for PKD4510ASI @ V_I = 53 V, max I_O.

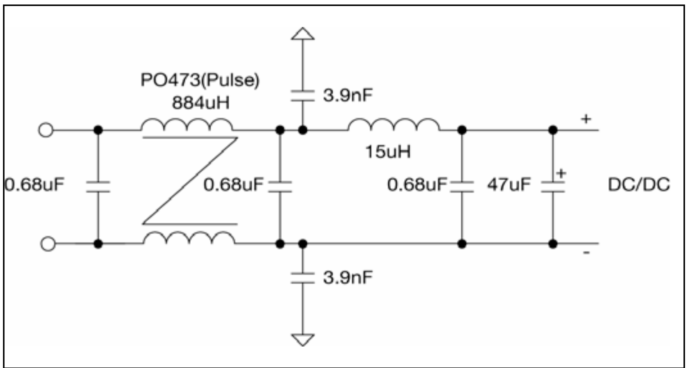
Conducted EMI Input terminal value (typ)

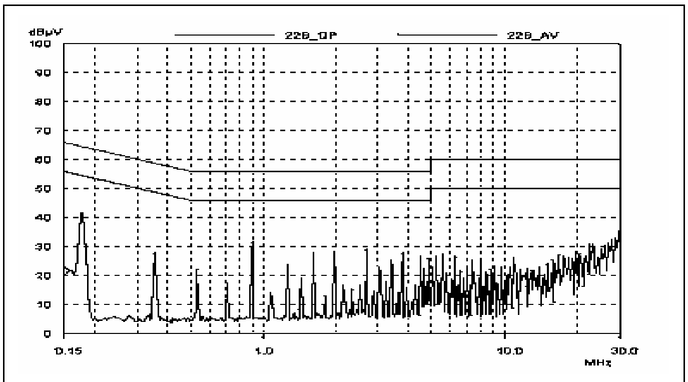


EMI without filter

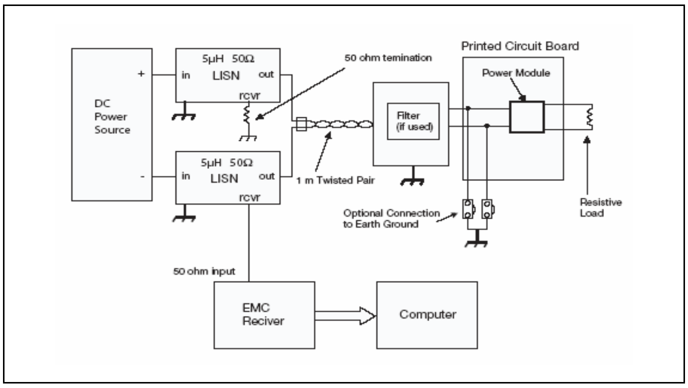
External filter (class B)

Required external input filter in order to meet class B in EN 55022, CISPR 22 and FCC part 15J.





EMI with filter



Test set-up

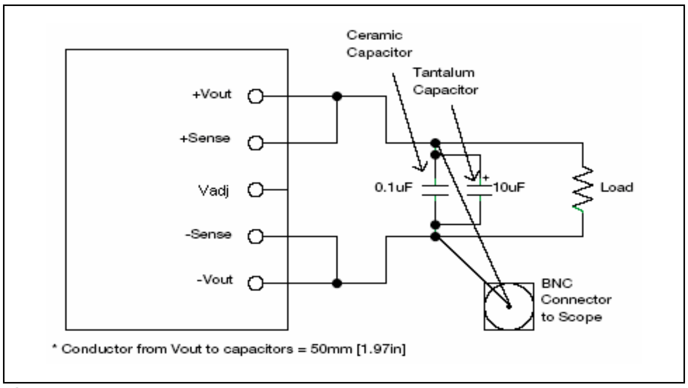
Layout recommendation

The radiated EMI performance of the DC/DC converter will depend on the PCB layout and ground layer design.
It is also important to consider the stand-off of the DC/DC converter.
If a ground layer is used, it should be connected to the output of the DC/DC converter and the equipment ground or chassis.

A ground layer will increase the stray capacitance in the PCB and improve the high frequency EMC performance.

Output ripple and noise

Output ripple and noise measured according to figure below. See Design Note 022 for detailed information.



Output ripple and noise test setup

Operating information

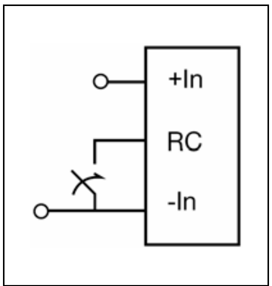
Input Voltage

The input voltage range 36 to 75 Vdc meets the requirements of the European Telecom Standard ETS 300 132-2 for normal input voltage range in –48 and –60 Vdc systems, -40.5 to -57.0 V and –50.0 to -72 V respectively.
At input voltages exceeding 75 V, the power loss will be higher than at normal input voltage and T_{ref} must be limited to absolute max +110°C. The absolute maximum continuous input voltage is 80 Vdc.

Turn-off Input Voltage

The DC/DC converters monitor the input voltage and will turn on and turn off at predetermined levels.
The minimum hysteresis between turn on and turn off input voltage is 2V.

Remote Control (RC)



The products are fitted with a remote control function referenced to the primary negative input connection (- In), with negative and positive logic options available. The RC function allows the converter to be turned on/off by an external device like a semiconductor or mechanical switch.
The maximum required sink current is 1 mA. When the RC pin is left open, the voltage generated on the RC pin is 3.5 – 6.0 V.
The default option is "positive logic" remote control, which only requires that RC logic (pin 6) is left open. The converter will turn on when the input voltage is applied with the RC pin open. Turn off is achieved by connecting the RC pin to the - In. To ensure safe turn off the voltage difference between RC pin and the - In pin shall be less than 1V. The converter will restart automatically when this connection is opened.
The second option is "negative logic" remote control, which requires that RC logic (pin 6) is connected to –In (pin 3). The converter will be off until the RC pin is connected to the –In. To turn on the converter the voltage between RC pin and –In should be less than 1 V. To turn off the converter the RC pin should be left open, or connected to a voltage higher than 4V referenced to –In. In situations were it is desired to have the converter to power up automatically without the need for control signals or a switch, the RC pin can be wired directly to –In.

Input and Output Impedance

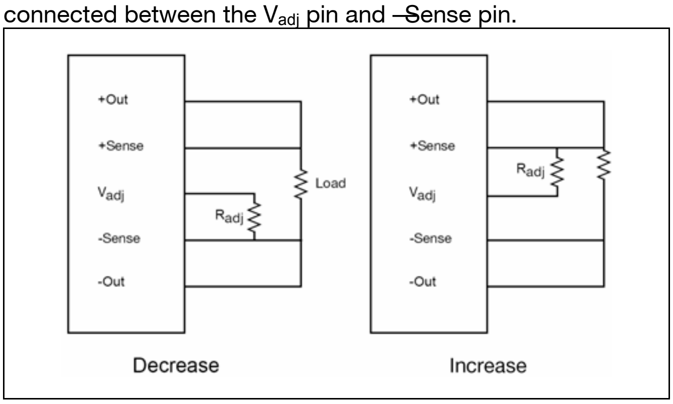
The impedance of both the input source and the load will interact with the impedance of the DC/DC converter. It is important that the input source has low characteristic impedance. The converters are designed for stable operation without external capacitors connected to the input or output. The performance in some applications can be enhanced by addition of external capacitance as described under External Decoupling Capacitors. If the input voltage source contains significant inductance, the addition of a 100 µF capacitor across the input of the converter will ensure stable operation. The capacitor is not required when powering the DC/DC converter from an input source with an inductance below 10 µH.

External Decoupling Capacitors

When powering loads with significant dynamic current requirements, the voltage regulation at the point of load can be improved by addition of decoupling capacitors at the load. The most effective technique is to locate low ESR ceramic and electrolytic capacitors as close to the load as possible, using several parallel capacitors to lower the effective ESR. The ceramic capacitors will handle high-frequency dynamic load changes while the electrolytic capacitors are used to handle low frequency dynamic load changes. Ceramic capacitors will also reduce any high frequency noise at the load.
It is equally important to use low resistance and low inductance PCB layouts and cabling.
External decoupling capacitors will become part of the control loop of the DC/DC converter and may affect the stability margins. As a "rule of thumb", 100 µF/A of output current can be added without any additional analysis. The ESR of the capacitors is a very important parameter. Power Modules guarantee stable operation with a verified ESR value of >10 mΩ across the output connections.
For further information please contact your local Ericsson Power Modules representative.

Output Voltage Adjust (V_{adj})

The DC/DC converters have an Output Voltage Adjust pin (V_{adj}). This pin can be used to adjust the output voltage above or below Output voltage initial setting.
At increased output voltages the maximum power rating of the converter remains the same, and the max output current must be decreased correspondingly.
To increase the voltage the resistor should be connected between the V_{adj} pin and +Sense pin. The resistor value of the Output voltage adjust function is according to information given under the Output section for the respective product.
To decrease the output voltage, the resistor should be connected between the V_{adj} pin and –Sense pin.



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Operating information continued

Parallel Operation

Two converters may be paralleled for redundancy if the total power is equal or less than P_O max. It is not recommended to parallel the converters without using external current sharing circuits.

{See Design Note 006 for detailed information.}

Remote Sense

The DC/DC converters have remote sense that can be used to compensate for voltage drops between the output and the point of load. The sense traces should be located close to the PCB ground layer to reduce noise susceptibility. The remote sense circuitry will compensate for up to 10% voltage drop between output pins and the point of load.

If the remote sense is not needed +Sense should be connected to +Out and -Sense should be connected to -Out.

Over Temperature Protection (OTP)

The converters are protected from thermal overload by an internal over temperature shutdown circuit.
When T_{ref} as defined in thermal consideration section exceeds 160°C the converter will shut down. The DC/DC converter will make continuous attempts to start up (non-latching mode) and resume normal operation automatically when the temperature has dropped >30°C below the temperature threshold.

Over Voltage Protection (OVP)

In the event of an overvoltage condition due to malfunction in the voltage monotoring circuits, the converter's PWM controller will automatically dictate minimum duty-cycle thus reducing the output voltage to a minimum.

Over Current Protection (OCP)

The converters include current limiting circuitry for protection at continuous overload.
The output voltage will decrease towards zero for output currents in excess of max output current (max I_O). The converter will resume normal operation after removal of the overload. The load distribution should be designed for the maximum output short circuit current specified.

Thermal Consideration

General

The converters are designed to operate in different thermal environments and sufficient cooling must be provided to ensure reliable operation.
Cooling is achieved mainly by conduction, from the pins to the host board, and convection, which is dependant on the airflow across the converter. Increased airflow enhances the cooling of the converter.

The Output Current Derating graph found in the Output section for each model provides the available output current vs. ambient air temperature and air velocity at V_{in} = 53 V.

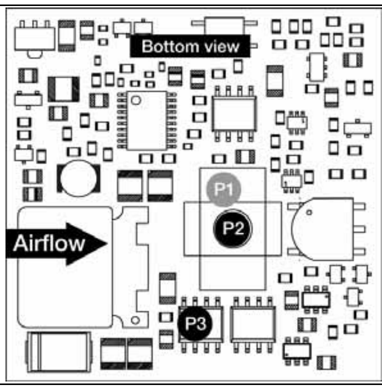
The DC/DC converter is tested on a 254 x 254 mm, 35 µm (1 oz), 8-layer test board mounted vertically in a wind tunnel with a cross-section of 305 x 305 mm.

Proper cooling of the DC/DC converter can be verified by measuring the temperature at positions P1, P2 and P3. The temperature at these positions should not exceed the max values provided in the table below.

Note that the max value is the absolute maximum rating (non destruction) and that the electrical Output data is guaranteed up to T_{ref} +90°C.

See Design Note 019 for further information.

Position	Device	Designation	max value
P_1	Case (topside)	T_{ref}	110° C
P_2	Transformer	T_{core}	125° C
P_3	Mosfet	$T_{surface}$	125° C



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Thermal Consideration continued

Definition of reference temperature (T_{ref})

The reference temperature is used to monitor the temperature limits of the product. Temperatures above maximum T_{ref} are not allowed and may cause degradation or permanent damage to the product. T_{ref} is also used to define the temperature range for normal operating conditions.
T_{ref} is defined by the design and used to guarantee safety margins, proper operation and high reliability of the module.

Ambient Temperature Calculation

By using the thermal resistance the maximum allowed ambient temperature can be calculated.

1. The power loss is calculated by using the formula
$((1/\eta) - 1) \times$ output power = power losses (Pd).
η = efficiency of converter. E.g 89 % = 0.89

2. Find the thermal resistance (Rth) in the Thermal Resistance graph found in the Output section for each model.
Calculate the temperature increase (ΔT).
ΔT = Rth x Pd

3. Max allowed ambient temperature is:
Max Tref - ΔT.

E.g PKD 4510 ASI at 1m/s:

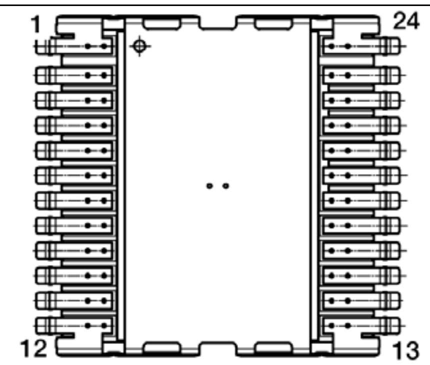
1. $((\frac{1}{0.90}) - 1) \times 49.5$ W = 5.5 W

2. 5.5 W $\times$ 4.9°C/W = 27.0°C

3. 110 °C - 27.0°C = max ambient temperature is 83°C

The actual temperature will be dependent on several factors such as the PCB size, number of layers and direction of airflow.

Connections



Pin	Designation	Function
2	+ In	Positive input
3	- In	Negative input
5	RC	Remote control pin
6	RC logic	Select pin for neg/pos RC[1]
15	+ Sen	Positive remote sense
16	Vadj	Output voltage adjust
17	- Sen	Negative remote sense
18-20	- Out	Negative output
21-23	+ Out	Positive output
1,12,13,24		Case connection[2]
4,7-11,14	NC	Not connected

1) Connect –In for negative logic or leave open for positive logic on RC pin.
2) Case is floating and may be connected either to +Vin,-Vin,+Vout or –Vout to optimize EMI performance.

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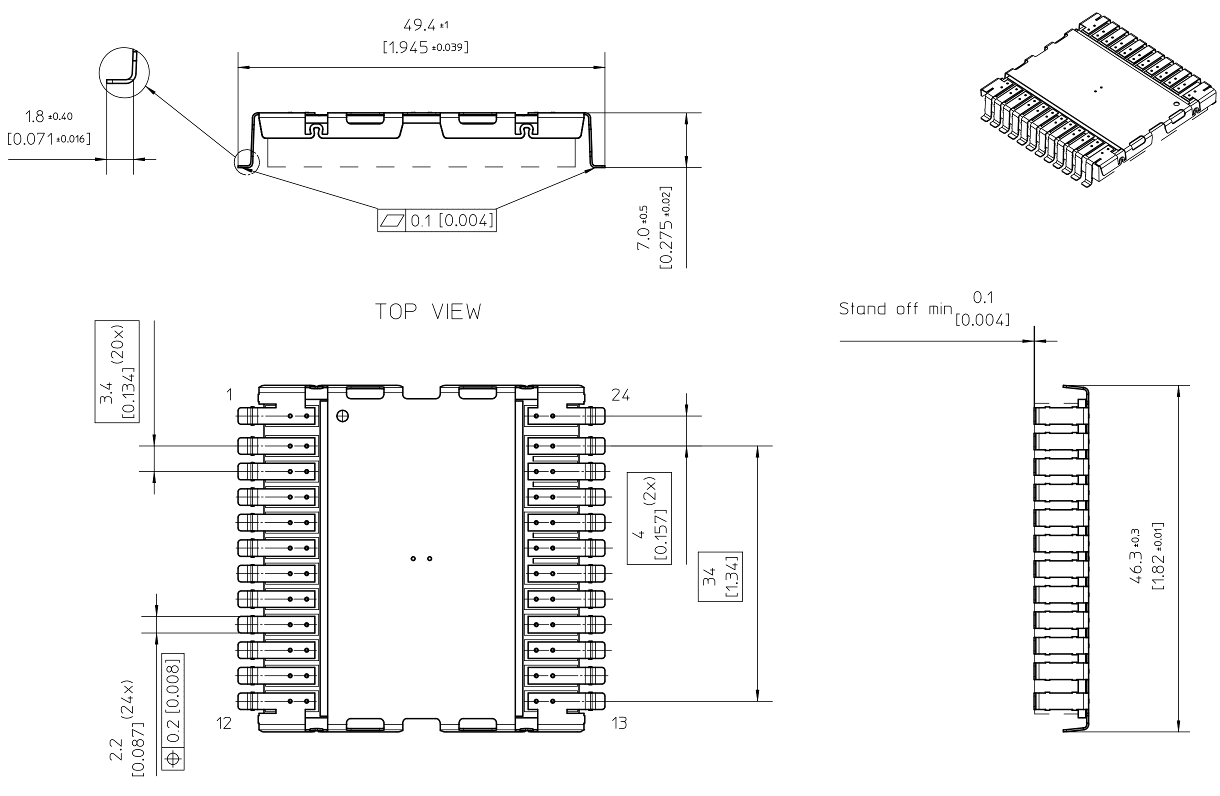
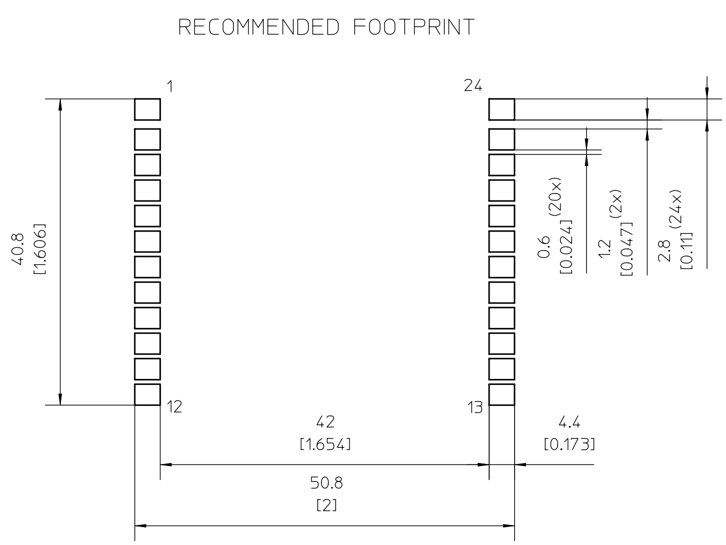
Mechanical Information

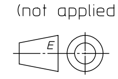




Case:
Integrated into lead frame, to be used
for pick and place, shielding and heat sink.

Leads:
Material: Copper alloy
Plating: 0.05 μm Palladium over 2-5 μm Nickel

Weight: typical 22 g
All dimensions in mm [inch]
Tolerances unless specified
x.x mm ±0.25 mm [0.01]
x.xx mm ±0.5 mm [0.02]
(not applied on footprint or typical values)



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Soldering Information - Surface mounting

The product is intended for convection or vapor phase reflow SnPb [and/or] Pb-free processes. To achieve a good and reliable soldering result, make sure to follow the recommendations from the solder paste supplier, to use state-of-the-art reflow equipment and reflow profiling techniques as well as the following guidelines.

A no-clean flux is recommended to avoid entrapment of cleaning fluids in cavities inside the product. The cleaning residues may affect long time reliability and isolation voltage.

A sufficiently extended preheat time is recommended to ensure an even temperature across the host PCB, for both small and large devices. To reduce the risk of excessive heating is also recommended to reduce the time in the reflow zone as much as possible.

Minimum pin temperature recommendations

Pin number 22 is chosen as reference location for the minimum pin temperature recommendations.

SnPb solder processes

For Pb solder processes, a pin temperature (T_{PIN}) in excess of the solder melting temperature, (T_L, +183°C for Sn63/Pb37) for more than 30 seconds, and a peak temperature of +210°C is recommended to ensure a reliable solder joint.

Lead-free (Pb-free) solder processes

For Pb-free solder processes, a pin temperature (T_{PIN}) in excess of the solder melting temperature (T_L, +217 to +221 °C for Sn/Ag/Cu solder alloys) for more than 30 seconds, and a peak temperature of +235°C on all solder joints is recommended to ensure a reliable solder joint.

SnPb solder processes

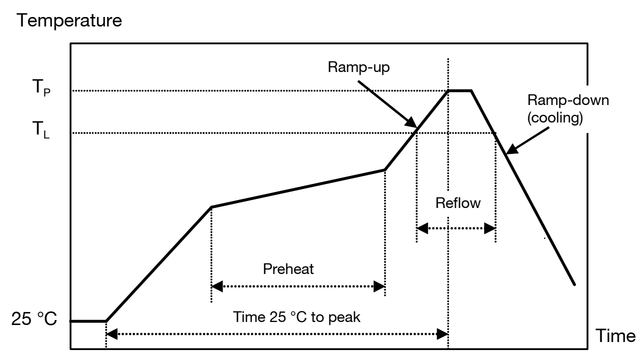
For conventional SnPb solder processes, the product is qualified for MSL 1 according to IPC/JEDEC standard J-STD-020C.

During reflow, T_{Pin} must not exceed +235 °C at any time.
Min 10 s over 200 °C
30-100 s over 183 °C

Lead-free (Pb-free) solder processes- TBD AFTER REVIEW

For Pb-free solder processes, the product is qualified for MSL 1 according to IPC/JEDEC standard J-STD-020C.

During reflow, T_{Pin} must not exceed +260 °C at any time.
max 20 s over 230 °C
30-100 s over 221 °C



Profile features		Sn/Pb eutectic assembly	Pb-free assembly
Average ramp-up rate		3 °C/s max	3 °C/s max
Solder melting temperature (typical)	T_L	+183 °C	+221 °C
Peak product temperature	T_P	+235 °C	+260 °C
Average ramp-down rate		4 °C/s max	4 °C/s max
Time 25 °C to peak temperature		6 minutes max	8 minutes max

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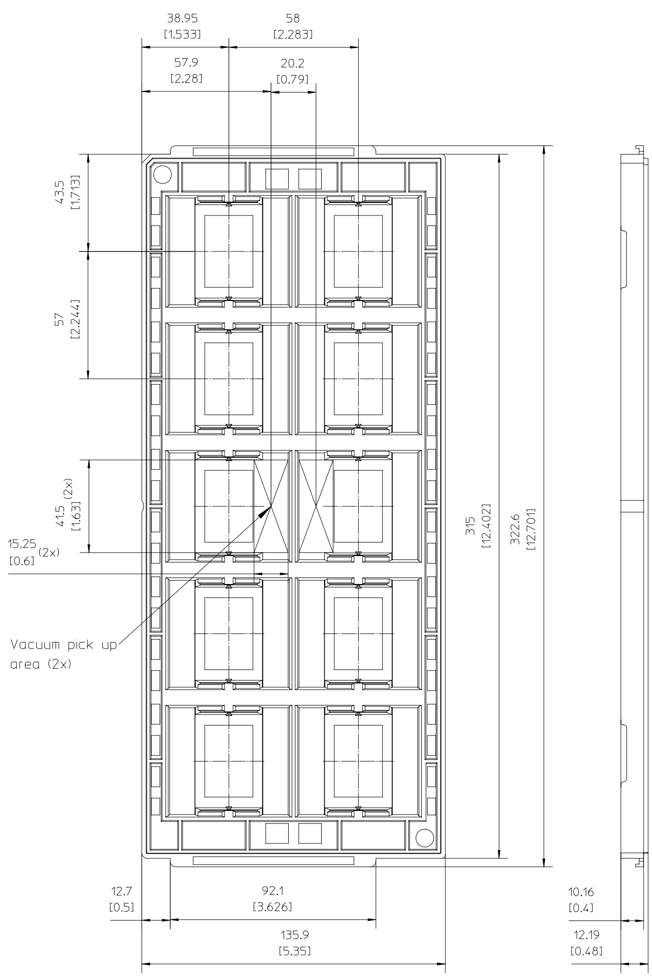
Delivery package information

The products are delivered in antistatic injection molded trays (Jedec design guide 4.10D standard)

Tray specifications	
Material	Dissipative PS
Surface resistance	$10^5 < \Omega/\text{square} < 10^{12}$
Bake ability	Non bakeable
Tray capacity	10 products/tray
Tray height	12.19 mm [0.48 inch]
Tray stacking pitch	10.16 mm [0.4 inch]
Box capacity	50 products (10 full trays/box)
Tray weight	110 g empty, 330 g full maximum



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Product Qualification Specification – acc. to 10563-BMR622-B

Characteristics			
External visual inspection	IPC-A-610		
Bump	IEC 60068-2-29 Eb	Peak acceleration Duration Directions Number of bumps	40 g 6 ms 6 1000/direction
Change of temperature (Temperature cycling)	IEC 60068-2-14 N_a	Temperature range Number of cycles Time in end position	-40 to +125 °C 300 30 min
Damp heat	IEC 60068-2-3 C_a	Temperature Humidity Duration	+85 °C 85 % RH 1000 hours
Dry heat	IEC 60068-2-2 Ba	Temperature Duration	+125 °C 1000 h
Immersion in cleaning solvents	IEC 60068-2-45 XA Method 2	Distilled water Glycol ether Isopropyl alcohol	+55 ±5 °C +35 ±5 °C +35 ±5 °C
Moisture reflow sensitivity classification	IPC/JEDEC J-STD-020C	Pb free	MSL 1, peak reflow at 260 °C
Operational life test	-	T_{ref} Load Duration	According to Absolute Maximum Ratings Maximum output power 1000 h
Robustness of terminations	IEC 60068-2-21 Ua1	Tensile force	20 N for 10 s
Solderability	IEC 60068-2-54 Td	Preconditioning Temperature, SnPb Eutectic	Ageing 240 h 85 °C/ 85% RH 235 ±5 °C
Vibration, broad band random	IEC 60068-2-34 Ed	Frequency Spectral density Duration	10 to 500 Hz 0.07 g^2/Hz 15 min in each 3 perpendicular directions