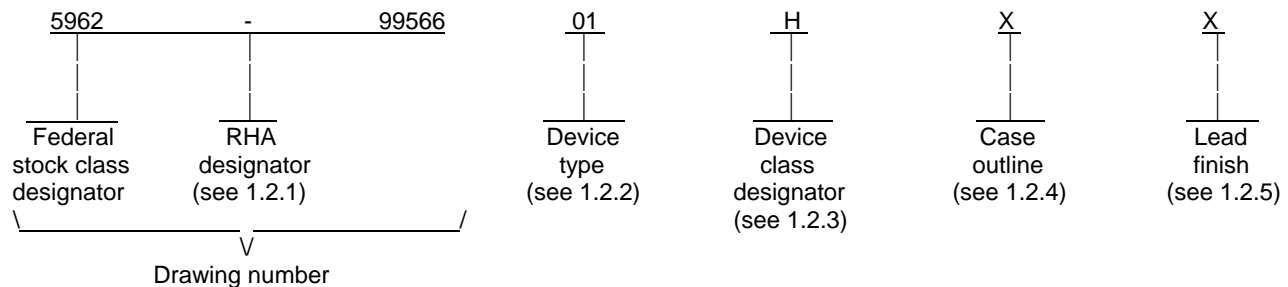


REVISIONS																				
LTR	DESCRIPTION										DATE (YR-MO-DA)				APPROVED					
A	Table I, Efficiency test, change minimum limit for subgroup 1 from 83% to 82% and subgroups 2, 3 from 83% to 80%. Update drawing boilerplate.										03-05-28				Raymond Monnin					
B	Added footnote 1 to table II, under group C end-point electricals. Updated drawing paragraphs. -sld										10-12-03				Charles F. Saffle					
REV																				
SHEET																				
REV																				
SHEET																				
REV STATUS OF SHEETS					REV		B	B	B	B	B	B	B	B	B	B	B	B	B	
					SHEET		1	2	3	4	5	6	7	8	9	10	11	12	13	14
PMIC N/A					PREPARED BY Steve L. Duncan					<b>DLA LAND AND MARITIME</b> <b>COLUMBUS, OHIO 43218-3990</b> <a href="http://www.dscc.dla.mil">http://www.dscc.dla.mil</a>										
<b>STANDARD MICROCIRCUIT DRAWING</b>  THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE  AMSC N/A					CHECKED BY Michael C. Jones															
					APPROVED BY Kendall A. Cottongim					<b>MICROCIRCUIT, HYBRID, LINEAR, <math>\pm 9.5</math> VOLT, DUAL CHANNEL, DC/DC CONVERTER</b>										
					DRAWING APPROVAL DATE 99-06-24															
										REVISION LEVEL B					SIZE A	CAGE CODE 67268	<b>5962-99566</b>			
										SHEET 1 OF 14										

## 1. SCOPE

1.1 Scope. This drawing documents five product assurance classes as defined in paragraph 1.2.3 and MIL-PRF-38534. A choice of case outlines and lead finishes which are available and are reflected in the Part or Identifying Number (PIN). When available, a choice of radiation hardness assurance levels are reflected in the PIN.

1.2 PIN. The PIN shall be as shown in the following example:



1.2.1 Radiation hardness assurance (RHA) designator. RHA marked devices shall meet the MIL-PRF-38534 specified RHA levels and shall be marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.

1.2.2 Device type(s). The device type(s) identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01	MOR289R5D	DC/DC converter, 105 W, $\pm 9.5$ V outputs

1.2.3 Device class designator. This device class designator shall be a single letter identifying the product assurance level. All levels are defined by the requirements of MIL-PRF-38534 and require QML Certification as well as qualification (Class H, K, and E) or QML Listing (Class G and D). The product assurance levels are as follows:

<u>Device class</u>	<u>Device performance documentation</u>
K	Highest reliability class available. This level is intended for use in space applications.
H	Standard military quality class level. This level is intended for use in applications where non-space high reliability devices are required.
G	Reduced testing version of the standard military quality class. This level uses the Class H screening and In-Process Inspections with a possible limited temperature range, manufacturer specified incoming flow, and the manufacturer guarantees (but may not test) periodic and conformance inspections (Group A, B, C, and D).
E	Designates devices which are based upon one of the other classes (K, H, or G) with exception(s) taken to the requirements of that class. These exception(s) must be specified in the device acquisition document; therefore the acquisition document should be reviewed to ensure that the exception(s) taken will not adversely affect system performance.
D	Manufacturer specified quality class. Quality level is defined by the manufacturers internal, QML certified flow. This product may have a limited temperature range.

**STANDARD  
MICROCIRCUIT DRAWING**  
DLA LAND AND MARITIME  
COLUMBUS, OHIO 43218-3990

SIZE  
**A**

REVISION LEVEL  
**B**

**5962-99566**

SHEET  
**2**

1.2.4 Case outline(s). The case outline(s) are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
T	See figure 1	12	Tabbed flange mount, lead formed up
U	See figure 1	12	Flange mount, lead formed down
X	See figure 1	12	Flange mount, short lead
Y	See figure 1	12	Tabbed flange mount, short lead
Z	See figure 1	12	Tabbed flange mount, lead formed down

1.2.5 Lead finish. The lead finish shall be as specified in MIL-PRF-38534.

1.3 Absolute maximum ratings. 1/

Input voltage range ( $V_{IN}$ ) 2/ .....	-0.5 V dc to +50 V dc
Power dissipation ( $P_D$ ).....	24 W
Lead temperature (10 seconds) .....	+300°C
Storage temperature range .....	-65°C to +150°C

1.4 Recommended operating conditions.

Input voltage range ( $V_{IN}$ ) .....	+16 V dc to +40 V dc
Output power .....	≤105 W
Case operating temperature range ( $T_C$ ) .....	-55°C to +125°C

## 2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

### DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38534 - Hybrid Microcircuits, General Specification for.

### DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard Microcircuits.  
MIL-STD-1835 - Interface Standard for Electronic Component Case Outlines.

### DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.  
MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <https://assist.daps.dla.mil/quicksearch/> or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

- 1/ Stresses above the absolute maximum ratings may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.
- 2/ An undervoltage lockout circuit shuts the unit off when the input voltage drops to approximately 14.5 volts. Operation of the unit between 14.5 volts and 16 volts is nondestructive, but performance is not guaranteed.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	<b>SIZE A</b>		<b>5962-99566</b>
		<b>REVISION LEVEL B</b>	<b>SHEET 3</b>

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

### 3. REQUIREMENTS

3.1 Item requirements. The individual item performance requirements for device classes D, E, G, H, and K shall be in accordance with MIL-PRF-38534. Compliance with MIL-PRF-38534 may include the performance of all tests herein or as designated in the device manufacturer's Quality Management (QM) plan or as designated for the applicable device class. The manufacturer may eliminate, modify or optimize the tests and inspections herein, however the performance requirements as defined in MIL-PRF-38534 shall be met for the applicable device class. In addition, the modification in the QM plan shall not affect the form, fit, or function of the device for the applicable device class.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38534 and herein.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.4 herein and figure 1.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 2.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are defined in table I.

3.5 Marking of device(s). Marking of device(s) shall be in accordance with MIL-PRF-38534. The device shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's vendor similar PIN may also be marked.

3.6 Data. In addition to the general performance requirements of MIL-PRF-38534, the manufacturer of the device described herein shall maintain the electrical test data (variables format) from the initial quality conformance inspection group A lot sample, for each device type listed herein. Also, the data should include a summary of all parameters manually tested, and for those which, if any, are guaranteed. This data shall be maintained under document revision level control by the manufacturer and be made available to the preparing activity (DLA Land and Maritime -VA) upon request.

3.7 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to supply to this drawing. The certificate of compliance (original copy) submitted to DLA Land and Maritime -VA shall affirm that the manufacturer's product meets the performance requirements of MIL-PRF-38534 and herein.

3.8 Certificate of conformance. A certificate of conformance as required in MIL-PRF-38534 shall be provided with each lot of microcircuits delivered to this drawing.

### 4. VERIFICATION

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38534 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	<b>SIZE A</b>		<b>5962-99566</b>
		<b>REVISION LEVEL B</b>	<b>SHEET 4</b>

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T <sub>C</sub> ≤ +125°C V <sub>IN</sub> = 28 V dc ±0.5 V dc no external sync, C <sub>L</sub> = 0 unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Output Voltage      (+V <sub>OUT</sub> )  (-V <sub>OUT</sub> )	V <sub>OUT</sub>	I <sub>OUT</sub> = +5.53 A dc, (main)	1	01	+9.410	+9.580	V dc
			2,3		+9.400	+9.600	
		I <sub>OUT</sub> = -5.53 A dc, (dual)	1		-9.360	-9.630	
			2,3		-9.350	-9.650	
Output current <u>1</u> /	I <sub>OUT</sub>	V <sub>IN</sub> = 16 V dc, 28 V dc, and 40 V dc, sum of both outputs	1,2,3			11	A
V <sub>OUT</sub> ripple    voltage	V <sub>RIP</sub>	I <sub>OUT</sub> = 5.53 A, B.W. = 10 kHz to 20 MHz	1			100	mV p-p
			2,3			120	
V <sub>OUT</sub> line regulation	V <sub>RLINE</sub>	V <sub>IN</sub> = 40 V dc to 16 V dc, I <sub>OUT</sub> = 5.53 A dc, (main)	1,2,3			50	mV
		V <sub>IN</sub> = 40 V dc to 16 V dc, I <sub>OUT</sub> = 5.53 A dc, (dual)			100		
V <sub>OUT</sub> load      regulation (+V <sub>OUT</sub> ) (-V <sub>OUT</sub> )	V <sub>RLOAD</sub>	I <sub>OUT</sub> = 0 to 5.53 A, (main)	1,2,3		50	mV	
		I <sub>OUT</sub> = 0 to 5.53 A, (dual)			200		
Input current	I <sub>IN</sub>	I <sub>OUT</sub> = 0, inhibit (pin 4) = 0	1, 2, 3		10	mA	
		I <sub>OUT</sub> = 0, inhibit (pin 12) = 0			60		
		I <sub>OUT</sub> = 0, inhibit pins (pins 4 and 12) = open			160		
I <sub>IN</sub> ripple current	I <sub>RIP</sub>	I <sub>OUT</sub> = 5.53 A, B.W. = 10 kHz to 20 MHz	1		120	mA p-p	
			2, 3		130		
Efficiency	Eff	I <sub>OUT</sub> =5.53 A	1	82		%	
			2,3	80			
Isolation	ISO	Input to output or any pin to case   at 500 V dc, T <sub>C</sub> =+25°C	1	100		MΩ	
Capacitive    load <u>2</u> / <u>3</u> /	C <sub>L</sub>	No effect on dc performance, T <sub>C</sub> =+25°C	4		1000	μF	

See footnotes at end of table.

**STANDARD  
MICROCIRCUIT DRAWING**  
DLA LAND AND MARITIME  
COLUMBUS, OHIO 43218-3990

SIZE  
**A**

REVISION LEVEL  
**B**

5962-99566

SHEET  
**5**

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C ≤ T <sub>C</sub> ≤ +125°C V <sub>IN</sub> = 28 V dc ±0.5 V dc no external sync, C <sub>L</sub> = 0 unless otherwise specified	Group A subgroups	Device type	Limits		Unit	
					Min	Max		
Power dissipation, load fault	P <sub>D</sub>	Short circuit	1,2,3	01		24	W	
Switching frequency	F <sub>S</sub>	I <sub>OUT</sub> = 5.53 A	4		480	580	kHz	
			5, 6		460	600		
External sync range <u>4/</u>	F <sub>SYNC</sub>	I <sub>OUT</sub> = 5.53 A, TTL level to pin 6	4,5,6		525	625	kHz	
Step V <sub>OUT</sub> load transient <u>5/</u>	V <sub>TLOAD</sub>	50 percent load to/from 100 percent load			-500	+500	mV pk	
Recovery time, step V <sub>OUT</sub> transient <u>3/ 5/ 6/</u>	T <sub>TLOAD</sub>	50 percent load to/from 100 percent load				300	μs	
Step V <sub>OUT</sub> transient lines <u>3/ 7/</u>	V <sub>TLINE</sub>	Input step from 40 V dc to 16 V dc, I <sub>OUT</sub> = 5.53 A			-600	+600	mV pk	
		Input step from 16 V dc to 40 V dc, I <sub>OUT</sub> = 5.53 A			-600	+600		
Recovery step V <sub>OUT</sub> line transient <u>3/ 6/</u>	T <sub>TLINE</sub>	Input step from 16 V dc to 40 V dc, I <sub>OUT</sub> = 5.53 A				300	μs	
		Input step from 40 V dc to 16 V dc, I <sub>OUT</sub> = 5.53 A				300		
Start up overshoot <u>3/</u>	V <sub>tonos</sub>	I <sub>OUT</sub> = 5.53 A, V <sub>IN</sub> = 0 to 28 V dc					50	mV pk
Start up delay <u>8/</u>	T <sub>onD</sub>	I <sub>OUT</sub> = 5.53 A, V <sub>IN</sub> = 0 to 40 V dc					10	ms
Load fault recovery <u>3/</u>	T <sub>rLF</sub>	I <sub>OUT</sub> = 5.53 A				10	ms	

- 1/ The output power available from either output is limited to 73.5 watts (i.e. 70 percent of the total output power), while the other maintains a minimum of 15 percent of the total output power used.
- 2/ Capacitive load may be any value from 0 to the maximum limit without compromising dc performance.
- 3/ Parameter shall be tested as part of design characterization and after design or process changes; therefore, the parameter shall be guaranteed to the limits specified in table I.
- 4/ A TTL level waveform (V<sub>IH</sub> = 4.5 V minimum, V<sub>IL</sub> = 0.8 V maximum) with a 50 percent ±10 percent duty cycle applied to the sync input pin (pin 6) within the sync range frequency shall cause the converter's switching frequency to become synchronous with the frequency applied to the sync input pin (pin 6).
- 5/ Load step transition time is 50 microseconds minimum.
- 6/ Recovery time is measured from the initiation of the transient until V<sub>OUT</sub> has returned to within ± 1 percent of its final value.
- 7/ Input step transition time greater than 10 microseconds.
- 8/ Start up delay time measurement is either for a step application of power at the input or the removal of a ground signal from the inhibit pin (pin 4) or inhibit 2 pin (pin 12) while power is applied to the input.

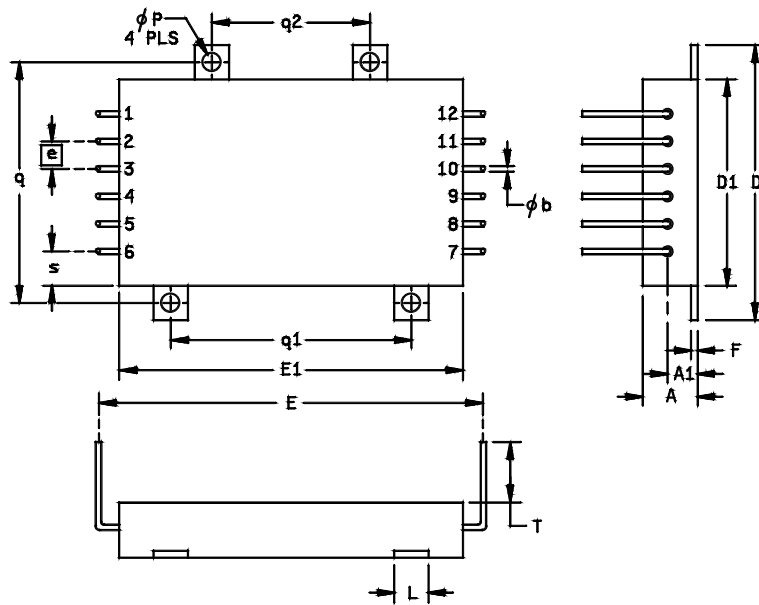
**STANDARD  
MICROCIRCUIT DRAWING**  
DLA LAND AND MARITIME  
COLUMBUS, OHIO 43218-3990

SIZE  
**A**

REVISION LEVEL  
**B**

**5962-99566**  
SHEET  
**6**

Case outline T.



Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A		10.16		0.400
A1	5.33	5.84	0.210	0.230
ϕb	0.89	1.14	0.035	0.045
D	50.55	51.05	1.990	2.010
D1	37.85	38.35	1.490	1.510
e	5.08 BSC		0.200 BSC	
E	69.85	72.39	2.750	2.850
E1	63.25	63.75	2.490	2.510
F	1.14	1.40	0.045	0.055
L	6.10	6.60	0.240	0.260
ϕP	3.43	3.68	0.135	0.145
q/q1	44.32	44.58	1.745	1.755
q2	29.08	29.34	1.145	1.155
s	6.22	6.48	0.245	0.255
T	9.91	12.45	0.390	0.490

NOTES:

1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Device weight: 110 grams maximum.

FIGURE 1. Case outline(s).

**STANDARD  
MICROCIRCUIT DRAWING**  
DLA LAND AND MARITIME  
COLUMBUS, OHIO 43218-3990

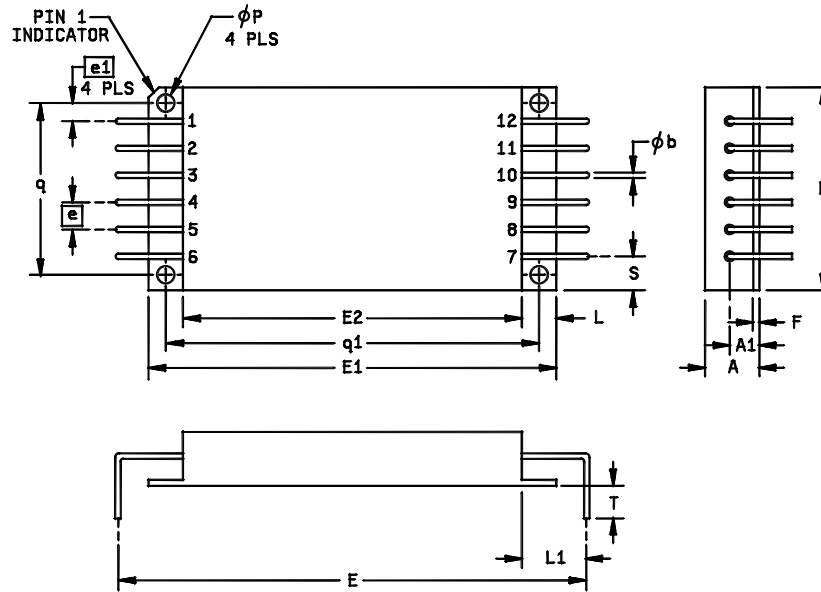
SIZE  
**A**

REVISION LEVEL  
**B**

**5962-99566**

SHEET  
**7**

Case outline U.



Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A		10.16		0.400
A1	4.95	6.22	0.195	0.245
$\phi b$	0.89	1.14	0.035	0.045
D	37.72	38.23	1.485	1.505
e	5.08 BSC		0.200 BSC	
e1	3.30 BSC		0.130 BSC	
E	87.38	87.88	3.440	3.460
E1	75.82	76.33	2.985	3.005
E2	63.37	63.63	2.495	2.505
F	1.14	1.40	0.045	0.055
L	6.10	6.60	0.240	0.260
L1	11.81	12.32	0.465	0.485
$\phi P$	3.12	3.38	0.123	0.133
q	31.88	32.13	1.255	1.265
q1	69.98	70.23	2.755	2.765
S	6.22	6.48	0.245	0.255
T	5.08	7.62	0.200	0.300

NOTES:

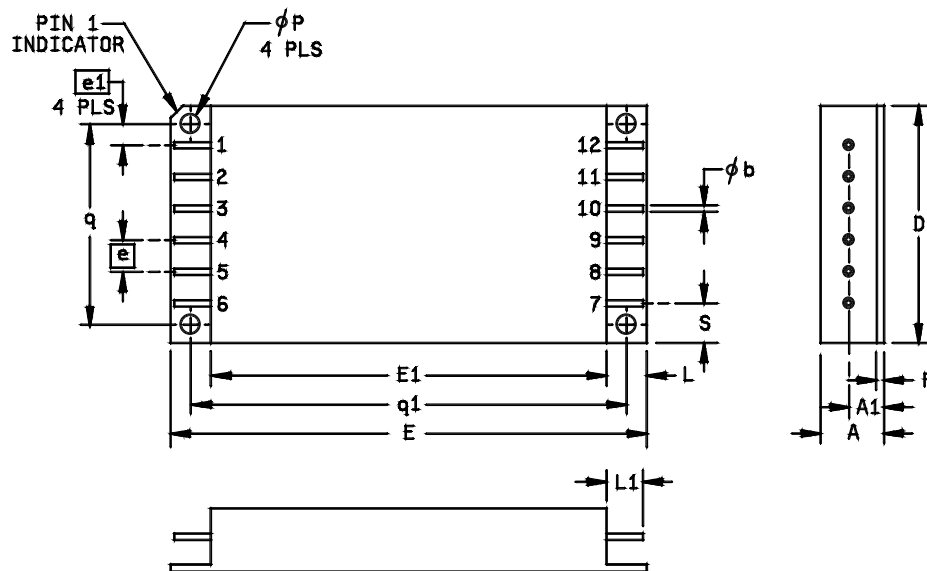
1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Device weight: 110 grams maximum.

FIGURE 1. Case outline(s) - Continued.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-99566</b>
		REVISION LEVEL <b>B</b>	SHEET <b>8</b>



Case outline X.



Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A		10.16		0.400
A1	5.33	5.84	0.210	0.230
ϕb	0.89	1.14	0.035	0.045
D	37.72	38.23	1.485	1.505
e	5.08 BSC		0.200 BSC	
e1	3.30 BSC		0.130 BSC	
E	75.82	76.33	2.985	3.005
E1	63.37	63.63	2.495	2.505
F	1.14	1.40	0.045	0.055
L	6.10	6.60	0.240	0.260
L1	6.35	8.89	0.250	0.350
ϕP	3.12	3.38	0.123	0.133
q	31.88	32.13	1.255	1.265
q1	69.98	70.23	2.755	2.765
S	6.22	6.48	0.245	0.255

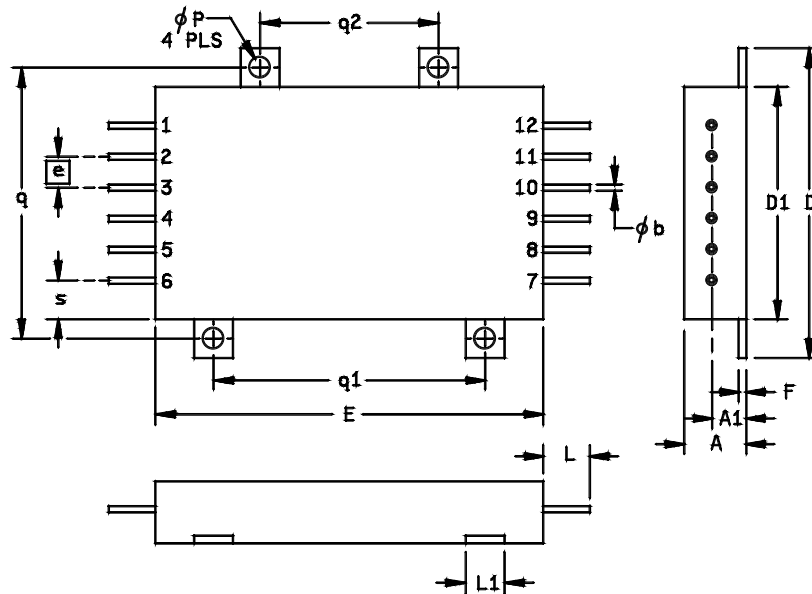
NOTES:

1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Device weight: 110 grams maximum.

FIGURE 1. Case outline(s) - Continued.

<b>STANDARD</b> <b>MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-99566</b>
		REVISION LEVEL <b>B</b>	SHEET <b>9</b>

Case outline Y.



Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A		10.16		0.400
A1	5.33	5.84	0.210	0.230
$\phi b$	0.89	1.14	0.035	0.045
D	50.55	51.05	1.990	2.010
D1	37.85	38.35	1.490	1.510
e	5.08 BSC		0.200 BSC	
E	63.25	63.75	2.490	2.510
F	1.14	1.40	0.045	0.055
L	6.35	8.89	0.250	0.350
L1	6.10	6.60	0.240	0.260
$\phi P$	3.43	3.68	0.135	0.145
q/q1	44.32	44.58	1.745	1.755
q2	29.08	29.34	1.145	1.155
s	6.22	6.48	0.245	0.255

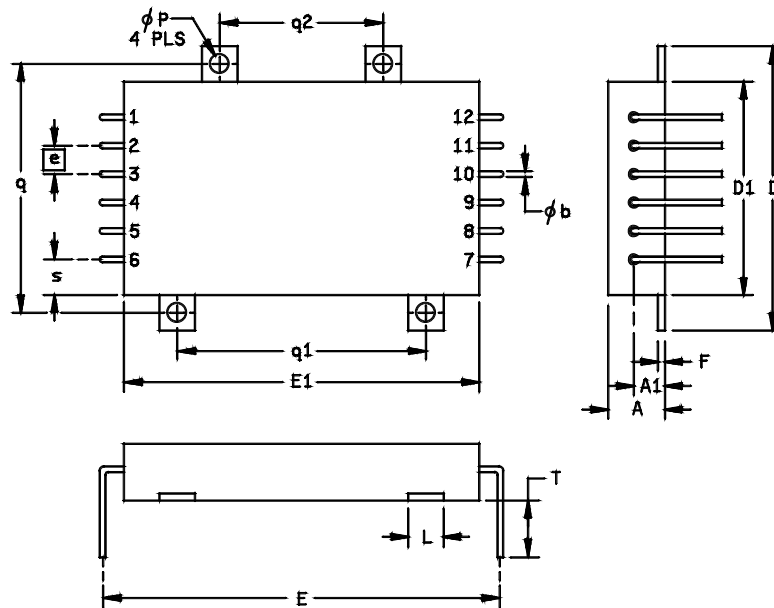
NOTES:

1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Device weight: 110 grams maximum.

FIGURE 1. Case outline(s) - Continued.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-99566</b>
		REVISION LEVEL <b>B</b>	SHEET <b>10</b>

Case outline Z.



Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A		10.16		0.400
A1	5.33	5.84	0.210	0.230
$\phi b$	0.89	1.14	0.035	0.045
D	50.55	51.05	1.990	2.010
D1	37.85	38.35	1.490	1.510
e	5.08 BSC		0.200 BSC	
E	69.85	72.39	2.750	2.850
E1	63.25	63.75	2.490	2.510
F	1.14	1.40	0.045	0.055
L	6.10	6.60	0.240	0.260
$\phi P$	3.43	3.68	0.135	0.145
$q/q1$	44.32	44.58	1.745	1.755
$q2$	29.08	29.34	1.145	1.155
s	6.22	6.48	0.245	0.255
T	7.87	10.41	0.310	0.410

NOTES:

1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Device weight: 110 grams maximum.

FIGURE 1. Case outline(s) - Continued.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-99566</b>
		REVISION LEVEL <b>B</b>	SHEET <b>11</b>

Device types	01
Case outlines	T, U, X, Y, and Z
Terminal number	Terminal symbol
1	Positive Input
2	Input common
3	Case
4	Inhibit 1
5	Sync output
6	Sync input
7	Positive output
8	Output return
9	Negative output
10	Trim
11	Share
12	Inhibit 2

FIGURE 2. Terminal connections.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	<b>SIZE A</b>		<b>5962-99566</b>
		<b>REVISION LEVEL B</b>	<b>SHEET 12</b>

TABLE II. Electrical test requirements.

MIL-PRF-38534 test requirements	Subgroups (in accordance with MIL-PRF-38534, group A test table)
Interim electrical parameters	---
Final electrical parameters	1*, 2, 3, 4, 5, 6
Group A test requirements	1, 2, 3, 4, 5, 6
Group C end-point electrical <u>1/</u> parameters	1, 2, 3
End-point electrical parameters for radiation hardness assurance (RHA) devices	Not applicable

1/ As a minimum, for all Group C testing performed after (10-12-03) manufacturers shall perform subgroups 1, 2, and 3 from the Group A electrical test table (Table C-Xa of MIL-PRF-38534).

\* PDA applies to subgroup 1.

4.2 Screening. Screening shall be in accordance with MIL-PRF-38534. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DLA Land and Maritime -VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.

(2) T<sub>A</sub> as specified in accordance with table I of method 1015 of MIL-STD-883.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Conformance and periodic inspections. Conformance inspection (CI) and periodic inspection (PI) shall be in accordance with MIL-PRF-38534 and as specified herein.

4.3.1 Group A inspection (CI). Group A inspection shall be in accordance with MIL-PRF-38534 and as follows:

a. Tests shall be as specified in table II herein.

b. Subgroups 7, 8, 9, 10, and 11 shall be omitted.

4.3.2 Group B inspection (PI). Group B inspection shall be in accordance with MIL-PRF-38534.

<b>STANDARD</b> <b>MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-99566</b>
		REVISION LEVEL <b>B</b>	SHEET <b>13</b>

4.3.3 Group C inspection (PI). Group C inspection shall be in accordance with MIL-PRF-38534 and as follows:

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test, method 1005 of MIL-STD-883.
  - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DLA Land and Maritime -VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
  - (2)  $T_A$  as specified in accordance with table I of method 1005 of MIL-STD-883.
  - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.3.4 Group D inspection (PI). Group D inspection shall be in accordance with MIL-PRF-38534.

4.3.5 Radiation Hardness Assurance (RHA) inspection. RHA inspection is not currently applicable to this drawing.

## 5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38534.

## 6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated as specified in MIL-PRF-38534.

6.4 Record of users. Military and industrial users shall inform DLA Land and Maritime when a system application requires configuration control and the applicable SMD. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DLA Land and Maritime -VA, telephone (614) 692-0544.

6.5 Comments. Comments on this drawing should be directed to DLA Land and Maritime -VA, Columbus, Ohio 43218-3990, or telephone (614) 692-1081.

6.6 Sources of supply. Sources of supply are listed in MIL-HDBK-103 and QML-38534. The vendors listed in MIL-HDBK-103 and QML-38534 have submitted a certificate of compliance (see 3.7 herein) to DLA Land and Maritime -VA and have agreed to this drawing.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	<b>SIZE A</b>		<b>5962-99566</b>
		<b>REVISION LEVEL B</b>	<b>SHEET 14</b>

## STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 10-12-03

Approved sources of supply for SMD 5962-99566 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38534 during the next revisions. MIL-HDBK-103 and QML-38534 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime -VA. This bulletin is superseded by the next dated revisions of MIL-HDBK-103 and QML-38534. DLA Land and Maritime maintains an online database of all current sources of supply at <http://www.dscc.dla.mil/Programs/Smcr/>.

Standard microcircuit drawing PIN <u>1</u> /	Vendor CAGE number	Vendor similar PIN <u>2</u> /
5962-9956601HTA 5962-9956601HTC	50821 50821	MOR289R5DW/883 MOR289R5DW/883
5962-9956601HUA 5962-9956601HUC	50821 50821	MOR289R5DV/883 MOR289R5DV/883
5962-9956601HXA 5962-9956601HXC	50821 50821	MOR289R5D/883 MOR289R5D/883
5962-9956601HYA 5962-9956601HYC	50821 50821	MOR289R5DY/883 MOR289R5DY/883
5962-9956601HZA 5962-9956601HZC	50821 50821	MOR289R5DZ/883 MOR289R5DZ/883

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the Vendor to determine its availability.
- 2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE  
number

50821

Vendor name  
and address

Interpoint Corporation  
10301 Willows Road  
Redmond, WA 98052-2529

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.