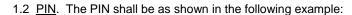
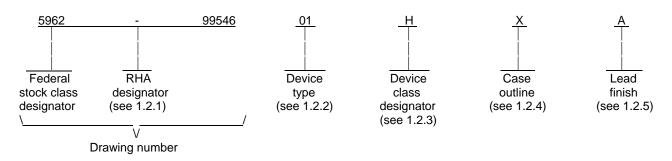
								R	REVISI	ONS										
LTR	DESCRIPTION							DA	TE (Y	R-MO	-DA)	APPROVED)						
A	Table I, Efficiency test, change minimum limit for subg 83% to 81% and subgroups 2, 3 from 81% to 80%. U boilerplate.						group Updat	1 fron e drav	n ving	03-05-28		R	Raymond Monnin							
В	Table I, V _{OUT} line regulation test, subgroups 2 and 3 maximum limit change 20 mV to 40 mV. Table I, V _{OUT} load regulation test, subgroup 1 maximum limit, change 20 mV to 30 mV, subgroups 2 and 3 maximum limit, change 20 mV to 95 mV.								05-11-16		R	Raymond Monnin		inin						
С		ed foot lated d						C end	I-point	electri	cals.			11-0)3-07		C	Charles F.Saffle		
REV																				
SHEET																				
REV																				
SHEET																				
REV STATU				RE			С	С	С	С	С	С	С	С	С	С	С	С	С	С
OF SHEETS PMIC N/A	5			PRE	EET EPARE y Zahn		1	2	3	4	5	6		8	9			12 	13	14
MICR	ANDAF OCIRC AWIN	CUIT		CHE	ECKED Chael () BY	es			DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990 http://www.dscc.dla.mil										
THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS			APPROVED BY Kendall A. Cottongim				MICROCIRCUIT, HYBRID, LINEAR, 9.5 VOL SINGLE CHANNEL, DC/DC CONVERTER			LT,										
AND AGENCIES OF THE DEPARTMENT OF DEFENSE				DRA	WING		ROVA 15-18	L DAT	E											
A۸	/ISC N//	Ą		REVISION LEVEL C			SIZE CAGE CODE A 67268 5962-99546				;									
										SHE	ET		1	OF	14					
DSCC FOR	1 0000									•			-							

1. SCOPE

1.1 <u>Scope</u>. This drawing documents five product assurance classes as defined in paragraph 1.2.3 and MIL-PRF-38534. A choice of case outlines and lead finishes which are available and are reflected in the Part or Identifying Number (PIN). When available, a choice of radiation hardness assurance levels are reflected in the PIN.





1.2.1 <u>Radiation hardness assurance (RHA) designator</u>. RHA marked devices shall meet the MIL-PRF-38534 specified RHA levels and shall be marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.

1.2.2 <u>Device type(s)</u>. The device type(s) identify the circuit function as follows:

Device type	<u>Generic number</u>	Circuit function
01	MOR289R5S	DC/DC converter, 105 W, +9.5 V output

1.2.3 <u>Device class designator</u>. This device class designator shall be a single letter identifying the product assurance level. All levels are defined by the requirements of MIL-PRF-38534 and require QML Certification as well as qualification (Class H, K, and E) or QML Listing (Class G and D). The product assurance levels are as follows:

Device class	Device performance documentation
К	Highest reliability class available. This level is intended for use in space applications.
Н	Standard military quality class level. This level is intended for use in applications where non-space high reliability devices are required.
G	Reduced testing version of the standard military quality class. This level uses the Class H screening and In-Process Inspections with a possible limited temperature range, manufacturer specified incoming flow, and the manufacturer guarantees (but may not test) periodic and conformance inspections (Group A, B, C, and D).
E	Designates devices which are based upon one of the other classes (K, H, or G) with exception(s) taken to the requirements of that class. These exception(s) must be specified in the device acquisition document; therefore the acquisition document should be reviewed to ensure that the exception(s) taken will not adversely affect system performance.
D	Manufacturer specified quality class. Quality level is defined by the manufacturers internal, QML certified flow. This product may have a limited temperature range.

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	he case outline(s) are as desig	gnated in MIL-ST	D-1835 and as	follows:	
1. <u>Outline letter</u>	Descriptive designator	Terminals		Package style	3
	<u>Descriptive designator</u>	<u>reminais</u>		<u>r dokage style</u>	<u>-</u>
Т	See figure 1	12	Tabbed fla	ange mount, lead f	ormed up
U	See figure 1	12		ount, lead formed o	
X	See figure 1	12		ount, short lead	
Ŷ	See figure 1	12		ange mount, short	load
Z	See figure 1	12	Tabbed ha	ange mount, lead f	ormed down
1.2.5 Lead finish. The le	ead finish shall be as specified	in MIL-PRF-385	534.		
1.3 Absolute maximum r	atings. <u>1</u> /				
Input voltage range (Vis) <u>2</u> /		-0.5 V dc to +5	50 V dc	
Power dissipation (P _D).	, =		30 W		
Lead temperature (10 s	econds)		+300°C		
			-65°C to +150°	°C	
eterage temperature				0	
1.4 Recommended oper	ating conditions.				
Input voltage range (V	N)		+16 V dc to +4	0 V dc	
	•)		≤ 105 W		
	ature range (T _C)		-55°C to +125°	°C.	
Case operating tempera			-00 0 10 +120	0	
2. APPLICABLE DOCU	MENTS				
	ation, standards, and handboo t specified herein. Unless othe				
DEPARTMENT OF DE	FENSE SPECIFICATION				
MIL-PRF-38534 - 1	Hybrid Microcircuits, General S	Specification for.			
DEPARTMENT OF DE	FENSE STANDARDS				
	est Method Standard Microcin terface Standard for Electroni		ase Outlines.		
DEPARTMENT OF DE	FENSE HANDBOOKS				
	List of Standard Microcircuit Dr Standard Microcircuit Drawings				
	ents are available online at <mark>htt</mark>) Robbins Avenue, Building 4D				e Standardization
	 In the event of a conflict bet dence. Nothing in this docume n obtained. 				
1/ Stresses above the ab	solute maximum ratings may c	cause permanen	t damage to the	device. Extended	operation at the
maximum levels may c 2/ An undervoltage locko	legrade performance and affect ut circuit shuts the unit off whe s and 16 volts is nondestructive	ct reliability. In the input volta	ge drops to appi	oximately 14.5 vol	
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		A			5962-99546
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3. REQUIREMENTS

3.1 <u>Item requirements</u>. The individual item performance requirements for device classes D, E, G, H, and K shall be in accordance with MIL-PRF-38534. Compliance with MIL-PRF-38534 may include the performance of all tests herein or as designated in the device manufacturer's Quality Management (QM) plan or as designated for the applicable device class. The manufacturer may eliminate, modify or optimize the tests and inspections herein, however the performance requirements as defined in MIL-PRF-38534 shall be met for the applicable device class. In addition, the modification in the QM plan shall not affect the form, fit, or function of the device for the applicable device class.

3.2 <u>Design, construction, and physical dimensions</u>. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38534 and herein.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.4 herein and figure 1.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 2.

3.3 <u>Electrical performance characteristics</u>. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.

3.4 <u>Electrical test requirements</u>. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are defined in table I.

3.5 <u>Marking of device(s)</u>. Marking of device(s) shall be in accordance with MIL-PRF-38534. The device shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's vendor similar PIN may also be marked.

3.6 <u>Data</u>. In addition to the general performance requirements of MIL-PRF-38534, the manufacturer of the device described herein shall maintain the electrical test data (variables format) from the initial quality conformance inspection group A lot sample, for each device type listed herein. Also, the data should include a summary of all parameters manually tested, and for those which, if any, are guaranteed. This data shall be maintained under document revision level control by the manufacturer and be made available to the preparing activity (DLA Land and Maritime-VA) upon request.

3.7 <u>Certificate of compliance</u>. A certificate of compliance shall be required from a manufacturer in order to supply to this drawing. The certificate of compliance (original copy) submitted to DLA Land and Maritime- VA shall affirm that the manufacturer's product meets the performance requirements of MIL-PRF-38534 and herein.

3.8 <u>Certificate of conformance</u>. A certificate of conformance as required in MIL-PRF-38534 shall be provided with each lot of microcircuits delivered to this drawing.

4. VERIFICATON

4.1 <u>Sampling and inspection</u>. Sampling and inspection procedures shall be in accordance with MIL-PRF-38534 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

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		TABLE I. Electrical perf	ormance characte	eristics.			
Test	Symbol	Conditions	Group A	Device	Limits		Unit
		$\label{eq:linear} \begin{array}{l} -55^\circ C \leq T_C \leq +125^\circ C \\ V_{\text{IN}} = 28 \ \text{V} \ \text{dc} \pm 0.5 \ \text{V} \ \text{dc} \\ \text{no external sync, } C_L = 0 \\ \text{unless otherwise specified} \end{array}$	subgroups	type	Min	Max	
Output voltage	Vout	I _{OUT} = 11 A dc	1	01	9.40	9.60	v
			2,3		9.310	9.690	
Output current	I _{OUT}	V_{IN} = 16, 28, and 40 V dc	1,2,3	01		11	А
V _{OUT} ripple voltage	V _{RIP}	Ι _{ουτ} = 11 Α, B.W. = 10 kHz to 20 MHz	1	01		100	mV p-p
		B.VV. = 10 KHZ to 20 MHZ	2,3			120	
V _{OUT} line regulation	VR _{LINE}	$I_{OUT} = 11 \text{ A}$	1	01		20	mV
		$V_{IN} = 16 V dc to 40 V dc,$	2,3			40	
V_{OUT} load regulation	VR_LOAD	I _{OUT} = 0 to 11 A	1	01		30	mV
			2,3			95	
Input current	I _{IN}	I _{OUT} = 0 A, inhibit (pins 4 and pin 12) = open	1, 2, 3	01		150	mA
		I _{OUT} = 0 A, inhibit pin (pin 4) = 0				10	
		I _{OUT} = 0 A, inhibit 2 pin (pin 12) = 0				70	
I _{IN} ripple current	I _{RIP}	I _{OUT} = 11 A, B.W. = 10 kHz to 20 MHz	1	01		120	mA p-p
			2, 3			130	
Efficiency	E _{FF}	I _{OUT} = 11 A	1	01	81		%
			2, 3		80		
Isolation	ISO	Input to output or any pin to case at 500 V dc, T _C = +25°	1 C	01	100		MΩ
Capacitive load <u>1/2/</u>	CL	No effect on dc performance $T_c = +25^{\circ}C$, 4	01		1000	μF
See footnotes at end o	of table.						
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	TABL	E I. Electrical performance cha	racteristics - C	ontinued.				
Test	Symbol	Conditions	Group A	Device	Limits		Unit	
		$\begin{array}{l} -55^\circ C \leq T_C \leq +125^\circ C \\ V_{\text{IN}} = 28 \ \text{V} \ dc \ \pm 0.5 \ \text{V} \ dc \\ \text{no external sync, } C_{\text{L}} = 0 \\ \text{unless otherwise specified} \end{array}$	subgroups type		Min	Max		
Short circuit power dissipation	PD	Short circuit	1,2,3	01		24	W	
Switching frequency	Fs	I _{OUT} = 11 A	4	01	480	580	kHz	
			5, 6		460	600		
External sync range 3/	F _{SYNC}	I _{OUT} = 11 A, TTL level to pin 6	4,5,6	01	525	625	kHz	
Step V _{OUT} load <u>4</u> / transient	VT _{LOAD}	50 percent load to/from 100 percent load	4,5,6	01	-500	500	mV pk	
Recovery time, step V _{OUT} transient <u>2/ 4/ 5</u> /	TT _{LOAD}	50 percent load to/from 100 percent load	4,5,6	01		300	μs	
Step V _{OUT} line transient		I _{OUT} = 11 A, input step from 16 V dc to 40 v dc	4,5,6	01	-500	500	mV pk	
<u>2/ 6</u> /		I _{OUT} = 11 A, input step from 40 V dc to 16 V dc			-500	500		
Recovery step VOUT		I _{OUT} = 11 A, input step from 16 V dc to 40 V dc	4,5,6	01		300	μS	
line transient <u>2</u> / <u>5</u> /		$I_{OUT} = 11 \text{ A}$, input step from 40 V dc to 16 V dc				300		
Start up overshoot 2/	Vton _{os}	I_{OUT} = 11 A, V_{IN} = 0 to 28 V dc	4,5,6	01		50	mV pk	
Turn-on delay time <u>7</u> /	Ton _D	I_{OUT} = 11 A, V_{IN} = 0 to 40 V dc	4,5,6	01		10	ms	
Load fault recovery 2/	Tr _{LF}	I _{OUT} = 11 A	4,5,6	01		10	ms	

1/ Capacitive load may be any value from 0 to the maximum limit without compromising dc performance.

2/ Parameter shall be tested as part of design characterization and after design or process changes; therefore, the parameter shall be guaranteed to the limits specified in table I.

3/ A TTL level waveform (V_{IH} = 4.5 V minimum, V_{IL} = 0.8 V maximum) with a 50 percent ±10 percent duty cycle applied to the sync input pin (pin 6) within the sync range frequency shall cause the converter's switching frequency to become synchronous with the frequency applied to the sync input pin (pin 6).

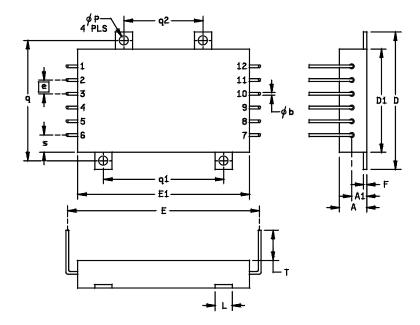
4/ Load step transition time is 50 microseconds minimum.

5/ Recovery time is measured from the initiation of the transient until V_{OUT} has returned to within ± 1 percent of its final value.

<u>6</u>/ Input step transition time greater than 10 microseconds

7/ Start up delay time measurement is either for a step application of power at the input or the removal of a ground signal from the inhibit pin (pin 4) or inhibit 2 (pin 12) while power is applied to the input.

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Symbol	Millin	neters	Inc	hes
-	Min	Max	Min	Max
A		10.16		.400
A1	5.33	5.84	.210	.230
φb	0.89	1.14	.035	.045
D	50.55	51.05	1.990	2.010
D1	37.85	38.35	1.490	1.510
е	5.08	BSC	.200	BSC
E	69.85	72.39	2.750	2.850
E1	63.25	63.75	2.490	2.510
F	1.14	1.40	.045	.055
L	6.10	6.60	.240	.260
φP	3.43	3.68	.135	.145
q/q1	44.32	44.58	1.745	1.755
q2	29.08	29.34	1.145	1.155
S	6.22	6.48	.245	.255
Т	9.91	12.45	.390	.490

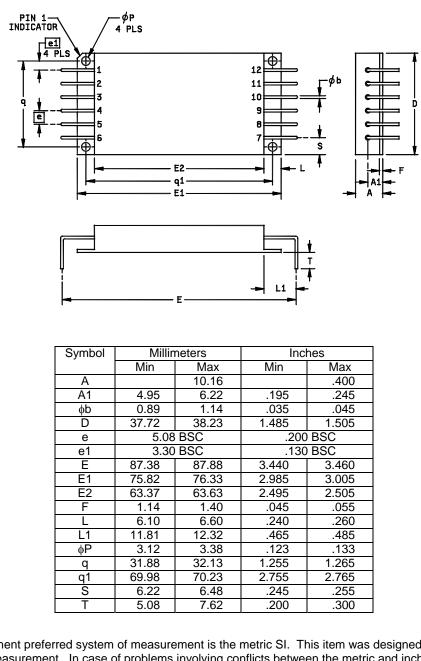
NOTES:

1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule. 2. Device weight: 110 grams maximum.

FIGURE 1. Case outline(s).

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Case outline U.



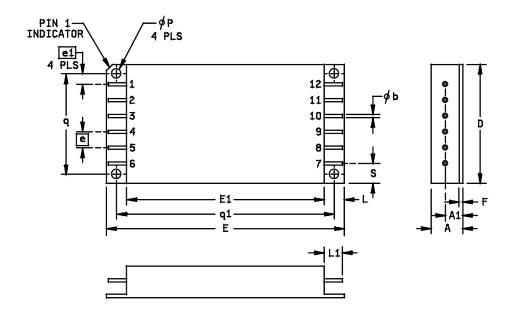
NOTES:

1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inchpound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.

2. Device weight: 110 grams maximum.

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Case outline X.

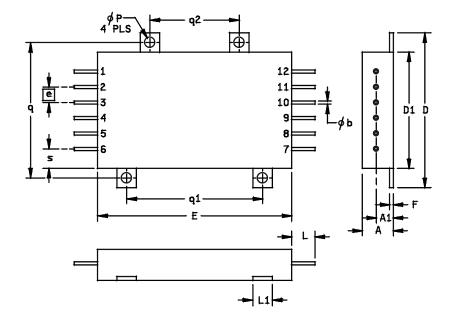


Symbol	Millin	neters	Inc	hes
-	Min	Max	Min	Max
A		10.16		.400
A1	5.33	5.84	.210	.230
φb	0.89	1.14	.035	.045
D	37.72	38.23	1.485	1.505
е	5.08	BSC	.200	BSC
e1	3.30	BSC	.130	BSC
E	75.82	76.33	2.985	3.005
E1	63.37	63.63	2.495	2.505
F	1.14	1.40	.045	.055
L	6.10	6.60	.240	.260
L1	6.35	8.89	.250	.350
φP	3.12	3.38	.123	.133
q	31.88	32.13	1.255	1.265
q1	69.98	70.23	2.755	2.765
S	6.22	6.48	.245	.255

NOTES:

- 1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inchpound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
- 2. Device weight: 110 grams maximum.

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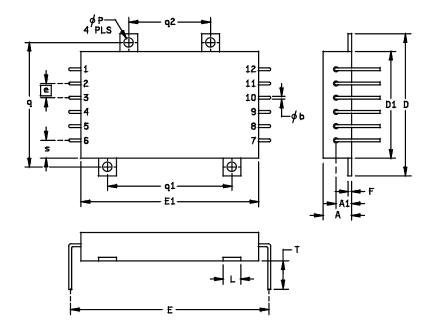


Symbol	Millir	neters	Inc	hes
-	Min	Max	Min	Max
A		10.16		.400
A1	5.33	5.84	.210	.230
φb	0.89	1.14	.035	.045
D	50.55	51.05	1.990	2.010
D1	37.85	38.35	1.490	1.510
е	5.08	BSC	.200 BSC	
E	63.25	63.75	2.490	2.510
F	1.14	1.40	.045	.055
L	6.35	8.89	.250	.350
L1	6.10	6.60	.240	.260
φP	3.43	3.68	.135	.145
q/q1	44.32	44.58	1.745	1.755
q2	29.08	29.34	1.145	1.155
S	6.22	6.48	.245	.255

NOTES:

- 1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inchpound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
- 2. Device weight: 110 grams maximum.

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Symbol	Millin	neters	Inc	hes
-	Min	Max	Min	Max
A		10.16		.400
A1	5.33	5.84	.210	.230
φb	0.89	1.14	.035	.045
D	50.55	51.05	1.990	2.010
D1	37.85	38.35	1.490	1.510
е	5.08	BSC	.200 BSC	
E	69.85	72.39	2.750	2.850
E1	63.25	63.75	2.490	2.510
F	1.14	1.40	.045	.055
L	6.10	6.60	.240	.260
φP	3.43	3.68	.135	.145
q/q1	44.32	44.58	1.745	1.755
q2	29.08	29.34	1.145	1.155
S	6.22	6.48	.245	.255
Т	7.87	10.41	.310	.410

NOTES:

- 1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inchpound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
- 2. Device weight: 110 grams maximum.

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Device type	01
Case outlines	T, U, X, Y, and Z
Terminal number	Terminal symbol
1 2 3 4 5 6 7 8 9 10 11 12	Input Input common Trim Inhibit Sync output Sync input Positive output Output return Remote sense return Positive remote sense Share Inhibit 2

FIGURE 2. Terminal connections.

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MIL-PRF-38534 test requirements	Subgroups (in accordance with MIL-PRF-38534, group A test table)
Interim electrical parameters	
Final electrical parameters	1*, 2, 3, 4, 5, 6
Group A test requirements	1, 2, 3, 4, 5, 6
Group C end-point electrical <u>1</u> / parameters	1, 2, 3
End-point electrical parameters for radiation hardness assurance (RHA) devices	Not applicable

TABLE II. Electrical test requirements.

- 1/ As a minimum, for all Group C testing performed after (11-03-07) manufacturers shall perform subgroups 1, 2, and 3 from the Group A electrical test table (Table C-Xa of MIL-PRF-38534). *
- PDA applies to subgroup 1.
- 4.2 <u>Screening</u>. Screening shall be in accordance with MIL-PRF-38534. The following additional criteria shall apply:
 - a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DLA Land and Maritime- VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.
 - (2) T_A as specified in accordance with table I of method 1015 of MIL-STD-883.
 - b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Conformance and periodic inspections. Conformance inspection (CI) and periodic inspection (PI) shall be in accordance with MIL-PRF-38534 and as specified herein.

- 4.3.1 Group A inspection (CI). Group A inspection shall be in accordance with MIL-PRF-38534 and as follows:
 - a. Tests shall be as specified in table II herein.
 - b. Subgroups 7, 8, 9, 10, and 11 shall be omitted.
- 4.3.2 Group B inspection (PI). Group B inspection shall be in accordance with MIL-PRF-38534.

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- 4.3.3 <u>Group C inspection (PI)</u>. Group C inspection shall be in accordance with MIL-PRF-38534 and as follows:
 - a. End-point electrical parameters shall be as specified in table II herein.
 - b. Steady-state life test, method 1005 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DLA Land and Maritime- VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.
 - (2) T_A as specified in accordance with table I of method 1005 of MIL-STD-883.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.3.4 <u>Group D inspection (PI)</u>. Group D inspection shall be in accordance with MIL-PRF-38534.

4.3.5 Radiation Hardness Assurance (RHA) inspection. RHA inspection is not currently applicable to this drawing.

5. PACKAGING

5.1 <u>Packaging requirements</u>. The requirements for packaging shall be in accordance with MIL-PRF-38534.

6. NOTES

6.1 <u>Intended use</u>. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 <u>Replaceability</u>. Microcircuits covered by this drawing will replace the same generic device covered by a contractorprepared specification or drawing.

6.3 <u>Configuration control of SMD's</u>. All proposed changes to existing SMD's will be coordinated as specified in MIL-PRF-38534.

6.4 <u>Record of users</u>. Military and industrial users shall inform DLA Land and Maritime when a system application requires configuration control and the applicable SMD. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DLA Land and Maritime- VA, telephone (614) 692-0544.

6.5 <u>Comments</u>. Comments on this drawing should be directed to DLA Land and Maritime- VA, Columbus, Ohio 43218-3990, or telephone (614) 692-1081.

6.6 <u>Sources of supply</u>. Sources of supply are listed in MIL-HDBK-103 and QML-38534. The vendors listed in MIL-HDBK-103 and QML-38534 have submitted a certificate of compliance (see 3.7 herein) to DLA Land and Maritime- VA and have agreed to this drawing.

STANDARD MICROCIRCUIT DRAWING	SIZE A		5962-99546
DLA LAND AND MARITIME		REVISION LEVEL	SHEET
COLUMBUS, OHIO 43218-3990		C	14

STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 11-03-07

Approved sources of supply for SMD 5962-99546 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38534 during the next revisions. MIL-HDBK-103 and QML-38534 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime- VA. This bulletin is superseded by the next dated revisions of MIL-HDBK-103 and QML-38534. DLA Land and Maritime maintains an online database of all current sources of supply at http://www.dscc.dla.mil/Programs/Smcr/.

Standard	Vendor	Vendor
microcircuit drawing	CAGE	similar
PIN <u>1</u> /	number	PIN <u>2</u> /
5962-9954601HTA	50821	MOR289R5SW/883
5962-9954601HTC	50821	MOR289R5SW/883
5962-9954601HUA	50821	MOR289R5SV/883
5962-9954601HUC	50821	MOR289R5SV/883
5962-9954601HXA	50821	MOR289R5S/883
5962-9954601HXC	50821	MOR289R5S/883
5962-9954601HYA	50821	MOR289R5SY/883
5962-9954601HYC	50821	MOR289R5SY/883
5962-9954601HZA	50821	MOR289R5SZ/883
5962-9954601HZC	50821	MOR289R5SZ/883

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the Vendor to determine its availability.
- 2/ <u>Caution</u>. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE <u>number</u>

50821

Vendor name and address

Interpoint Corporation DBA Crane Electronics-Redmond 10301 Willows Road Redmond, WA 98052

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.