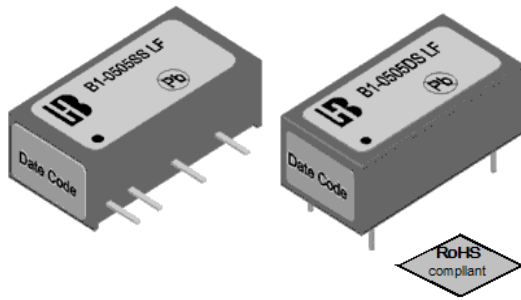


**Features:**

- Input Filter With Internal Capacitor
- Input / Output Isolation 1K Vdc Or 3K Vdc(H)
- Low Ripple And Noise
- 100% Burn-In
- Un-Regulated type
- Net Weight : 2.2 g / 2.7 g Typical
- 7 Pin SIL / 14 Pin DIL Package
- Custom Design Available

Technical Specification (1/2):

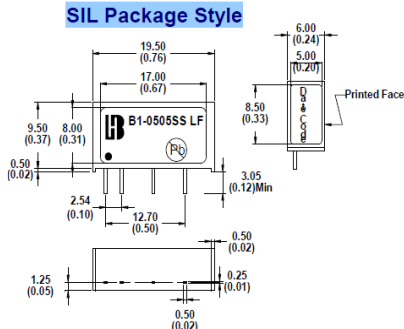
Model No.	Input Voltage (Vdc)	Output Voltage (Vdc)	Output Current (mA) Max	Input Current @No Load (mA) Typ.	Input Current @Max. Load (mA) Typ.	Output Ripple (mV) Max.	Load Regulation (%) Max.	Efficiency (%)Typ.
1) Single Output SIL Package Style								
B1-0505SS(H) LF	5	5	200	30	253	60	± 8	79
B1-0512SS(H) LF		12	84	28	255	100	± 8	79
B1-0515SS(H) LF		15	67	28	251	120	± 8	80
B1-1205SS(H) LF	12	5	200	18	105	60	± 8	79
B1-1212SS(H) LF		12	84	18	104	100	± 8	80
B1-1215SS(H) LF		15	67	15	105	120	± 8	80
B1-243R3SS(H) LF	24	3.3	303	5	56	50	± 8	74
B1-2405SS(H) LF		5	200	4	54	60	± 8	77
B1-2412SS(H) LF		12	84	4	55	100	± 8	76
B1-2415SS(H) LF		15	67	4	55	120	± 8	76
2) Single Output DIL Package Style								
B1-0505DS(H) LF	5	5	200	30	253	60	± 8	79
B1-0512DS(H) LF		12	84	28	254	100	± 8	79
B1-0515DS(H) LF		15	67	28	250	120	± 8	80
B1-1205DS(H) LF	12	5	200	18	105	60	± 8	79
B1-1212DS(H) LF		12	84	18	104	100	± 8	80
B1-1215DS(H) LF		15	67	15	105	120	± 8	80
B1-243R3DS(H) LF	24	3.3	303	5	56	50	± 8	74
B1-2405DS(H) LF		5	200	4	54	60	± 8	77
B1-2412DS(H) LF		12	84	4	55	100	± 8	76
B1-2415DS(H) LF		15	67	4	55	120	± 8	76
3) Dual Output SIL Package Style								
B1-053R3S(H) LF	5	± 3.3	± 150	30	274	60	± 12	73
B1-0505S(H) LF		± 5	± 100	30	263	60	± 10	76
B1-0512S(H) LF		± 12	± 42	28	255	100	± 8	79
B1-0515S(H) LF		± 15	± 34	28	255	120	± 8	80
B1-1205S(H) LF	12	± 5	± 100	18	104	60	± 8	80
B1-1212S(H) LF		± 12	± 42	18	104	100	± 8	80
B1-243R3S(H) LF	24	± 3.3	± 150	5	56	60	± 8	74
B1-2405S(H) LF		± 5	± 100	4	55	60	± 8	76
B1-2412S(H) LF		± 12	± 42	4	55	100	± 8	76

Technical Specification (2/2):

Model No.	Input Voltage (Vdc)	Output Voltage (Vdc)	Output Current (mA) Max	Input Current @No Load (mA) Typ.	Input Current @Max. Load (mA) Typ.	Output Ripple (mV) Max.	Load Regulation (%) Max.	Efficiency (%)Typ.
4) Dual Output DIL Package Style								
B1-053R3D(H) LF	5	± 3.3	± 150	30	274	60	± 12	73
B1-0505D(H) LF		± 5	± 100	30	263	60	± 10	76
B1-0512D(H) LF		± 12	± 42	28	253	100	± 8	79
B1-0515D(H) LF		± 15	± 34	28	250	120	± 8	80
B1-1205D(H) LF	12	± 5	± 100	18	104	60	± 8	80
B1-1212D(H) LF		± 12	± 42	18	104	100	± 8	80
B1-243R3D(H) LF	24	± 3.3	± 150	5	56	60	± 8	74
B1-2405D(H) LF		± 5	± 100	4	55	60	± 8	76
B1-2412D(H) LF		± 12	± 42	4	55	100	± 8	76
5) Dual Separate Output SIL Package Style								
B1-0505S01 LF	5	5/5	100/100	30	263	60	± 10	76
B1-0515S01 LF		5/15	100/33	28	250	120	± 8	80
B1-1205S01 LF	12	5/5	100/100	18	107	60	± 8	78
B1-1212S01 LF		5/12	100/42	18	104	100	± 8	80
B1-1215S01 LF		5/15	100/33	18	106	120	± 8	78
B1-2405S01 LF	24	5/5	100/100	5	54	60	± 8	76
6) Dual Separate Output DIL Package Style								
B1-0505D01 LF	5	5/5	100/100	30	263	60	± 10	76
B1-0515D01 LF		5/15	100/33	28	250	120	± 8	80
B1-1205D01 LF	12	5/5	100/100	18	107	60	± 8	78
B1-1212D01 LF		5/12	100/42	18	104	100	± 8	80
B1-1215D01 LF		5/15	100/33	13	106	120	± 8	78
B1-2405D01 LF	24	5/5	100/100	5	54	60	± 8	76

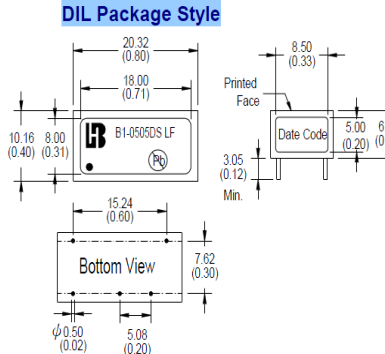
Mechanical Dimensions

SIL Package Style



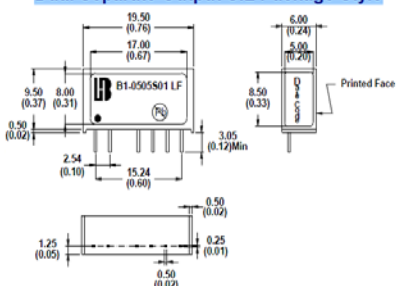
Pin	1K Vdc	3K Vdc
1	+Vin	+Vin
2	-Vin	-Vin
3	No Pin	No Pin
4	-Vo	-Vo
5	common	-Vo
6	+Vo	common
7	No Pin	+Vo

DIL Package Style



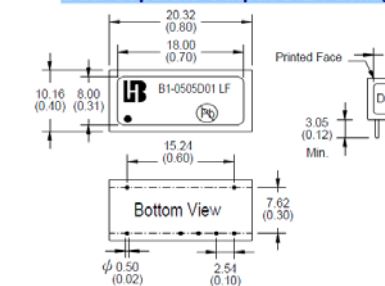
Pin	1K Vdc: Single	Pin	Pin	1K Vdc: Dual	Pin
1	-Vin	+Vin	14	-Vin	+Vin
2	No Pin	No Pin	13	No Pin	No Pin
3	No Pin	-Vo	12	No Pin	-Vo
4	No Pin	-Vo	11	No Pin	-Vo
5	No Pin	10	5	No Pin	10
6	+Vo	9	6	+Vo	9
7	NC	No Pin	8	NC	Commo

Dual Separate Output SIL Package Style



Pin	1K Vdc	Dual Separate
1	+Vin	+Vin
2	-Vin	-Vin
3	No Pin	No Pin
4	+Vo1	+Vo1
5	-Vo1	-Vo1
6	+Vo2	+Vo2
7	-Vo2	-Vo2

Dual Separate Output DIL Package Style



Pin	1K Vdc: Dual Separate	Pin	Pin	1K Vdc: Dual Separate	Pin
1	-Vin	+Vin	14	-Vin	+Vin
2	No Pin	No Pin	13	No Pin	No Pin
3	No Pin	+Vo1	12	No Pin	+Vo1
4	No Pin	+Vo1	11	No Pin	+Vo1
5	No Pin	-Vo1	10	No Pin	-Vo1
6	+Vo2	+Vo2	9	+Vo2	+Vo2
7	NC	-Vo2	8	NC	-Vo2