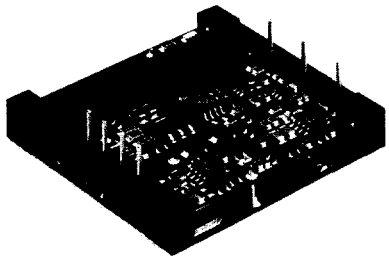


Input voltage range up to 75 V DC
1 or 3 outputs up to 15 V DC
1500 V DC I/O electric strength test voltage

- 100°C base plate operation
- 5 V, 3.3 V, 2.5 V and 2.1 V outputs
- Open frame 2" x 2" packaging

Selection chart

Output 1 $U_{o\,nom}$ [V DC]	Output 1 $I_{o\,nom}$ [A]	Output 2 $U_{o\,nom}$ [V DC]	Output 2 $I_{o\,nom}$ [A]	Output 3 $U_{o\,nom}$ [V DC]	Output 3 $I_{o\,nom}$ [A]	Input U_i [V DC]	Rated power $P_{o\,tot}$ [W]	Efficiency η_{typ} [%]	Type
2.1	10	-	-	-	-	36...72	21	81	OES 021ZC-A
2.5	10	-	-	-	-	36...72	25	82	OES 025ZD-A
3.3	10	-	-	-	-	36...72	33	85	OES 033ZE-A
5	8	-	-	-	-	36...72	40	89	OES 040ZG-A
3.3	3.5	+12	0.35	−12	0.35	18...36	20	85	OET 020YEHH-A
3.3	3.5	+12	0.35	−12	0.35	34...75	20	85	OET 020ZEHH-A
3.3	3.5	+15	0.28	−15	0.28	18...36	20	85	OET 020YEJJ-A
3.3	3.5	+15	0.28	−15	0.28	34...75	20	85	OET 020ZEJJ-A
5	3.5	+12	0.31	−12	0.31	18...36	25	86	OET 025YGHH-A
5	3.5	+12	0.31	−12	0.31	34...75	25	85	OET 025ZGHH-A
5	3.5	+15	0.25	−15	0.25	18...36	25	86	OET 025YGJJ-A
5	3.5	+15	0.25	−15	0.25	34...75	25	85	OET 025ZGJJ-A

Input

Input voltage	continuous range, 24 V, triple output models only	18...36 V DC
	continuous range, 48 V, single output models	36...72 V DC
	continuous range, 48 V, triple output models	34...75 V DC
Reverse voltage protection	shunt diode	

Output

Output voltage setting accuracy	$U_{i\,nom}$, $I_{o\,nom}$	±1% $U_{o\,nom}$
Minimum load	recommended on all outputs	10% $I_{o\,nom}$
Line regulation	$U_{i\,min}$...$U_{i\,max}$, $I_{o\,nom}$, 5 V, 3.3 V and triple output	typ. ±0.2% $U_{o\,nom}$
	$U_{i\,min}$...$U_{i\,max}$, $I_{o\,nom}$, 2.5 V and 2.1 V	typ. ±0.4% $U_{o\,nom}$
Load regulation	$U_{i\,nom}$, 1...100% $I_{o\,nom}$	typ. 0.5% $U_{o\,nom}$
Output voltage switching noise	$U_{i\,nom}$, $I_{o\,nom}$, peak-peak, total	max. 6% $U_{o\,nom}$
Voltage trim range		±10% $U_{o\,nom}$

Control and protection

Overload protection	shut down, hiccup, self recovering	110...140% $I_{o\ nom}$
	triple output models	110...130% $I_{o\ nom}$, max. 200% $I_{o\ nom}$
Overvoltage protection	self recovering, single output models	120...140% $U_{o\ nom}$
	self recovering, triple output models	115...140% $U_{o\ nom}$
Thermal shutdown		105...115°C
Remote shutdown	positive logic, negative reference	

Safety and EMC

Electric strength test voltage	I/O	1500 V DC
Electromagnetic interference	conducted	tbd

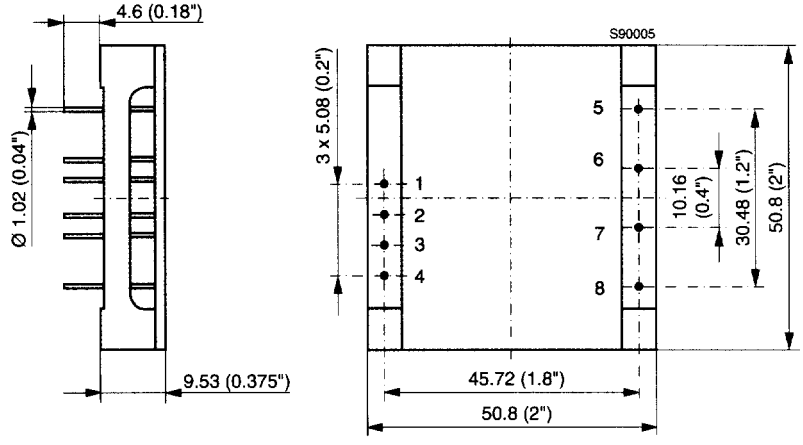
Environmental

Operating case temperature T_C	$U_{i\ nom}$, $I_{o\ nom}$	–40...100°C
Storage temperature	non operational	–40...125°C
Relative humidity	non condensing	95%
MTBF	Bellcore TR-NWT-000332	1'500'000 h

Mechanical data

Tolerances ±0.3 mm (0.012") unless otherwise indicated.

European Projection



Pin allocation OES

Pin	Single output units
1	Vi+
2	Vi–
3	n.c.
4	Enable
5	Vo+
6	Vo–
7	Trim

Pin allocation OET

Pin	Triple output units
1	Vi+
2	Vi–
3	Case
4	Enable
5	Vo+ (aux)
6	Vo+ (main)
7	Common
8	Vo– (aux)