

FEATURES

- High Efficiency & High Power Factor – Meets Energy Star & DLC
- 5 Year Warranty
- Long Life Japanese Capacitors
- Over Voltage, Short Circuit & Optional Over Temperature Protection
- Waterproof (IP67), Damp & Wet Location (Some Models)
- Class 2 Output (Some Models)
- Certified to UL8750 & EN61347
- Wide Range Input 90-305 VAC
- RoHS Compliant

100W Constant Voltage LED Driver

LWV100 Series



SPECIFICATION

See Part Number Builder to create your customized part	Model # (1)	Output Range (A)	Output Voltage	Max. Output Power	Efficiency (Typ.)		PF (4)
					110V	220V	277V
	LWV100-012S1	0-8.0A	12V	100 W	91%	92%	92%
	LWV100-024S2 (2)	0-4.0A	24V	96 W	91%	92%	92%
	LWV100-036S2 (2)	0-2.67A	36V	96 W	91%	92%	92%
	LWV100-042S3 (3)	0-2.29A	42V	96 W	91%	92%	92%
	LWV100-048S3 (3)	0-2.0A	48V	96 W	91%	92%	92%
	LWV100-054S3 (3)	0-1.78A	54V	96 W	91%	92%	92%
	LWV100-105S1	0-0.95A	105V	100 W	91%	92%	92%

Output	Load Regulation	±5%	
	Line Regulation	±2%	
	Turn-on Delay	0.6 sec. (Typ.) Measured at 110Vac input. / 0.6 sec. (Typ.) Measured at 220Vac input.	
	Ripple & Noise (pk-pk)	3% Vo Measured by 20 MHz bandwidth oscilloscope and the output paralleled a 0.1 uF ceramic capacitor and a 10 uF electrolytic capacitor	
Input	Input voltage	100~277 Vac	
	Frequency Range	50Hz ~60Hz	
	Inrush Current	65A at 230Vac input, 25°C Cold Start	
	AC Current (Typ.)	1.5 A measured at full load and 100 Vac input / 0.8 measured at full load and 220 Vac input	
	THD	< 20% measured at 100-277 Vac, from 75% to 100% Load	
	Leakage Current	0.25 mA Measured at 277Vac 60Hz Input	
Protections	Short Circuit	Protection type : Hiccup mode, recovers automatically after fault condition is removed	
	Over Temperature	110°C Maximum temperature of components inside the case, auto recovers after cooling (Optional)	
	Over Current Protection	135% of I _o (Typ.). Hiccup mode, recovers automatically after fault condition is removed	
	Over Voltage	125% of rated voltage (Typ.) Protection: Latch mode	
Environment	Temperature Range	Operational	- 40°C ~+ 70°C (See Derating Curve)
		Storage	- 40 ~ +85°C
	Case Temperature	Max. +90 °C (See Mechanical Drawing for exact location)	
	Humidity	Operational	10% ~ 100% RH
		Storage	5% ~100% R.H

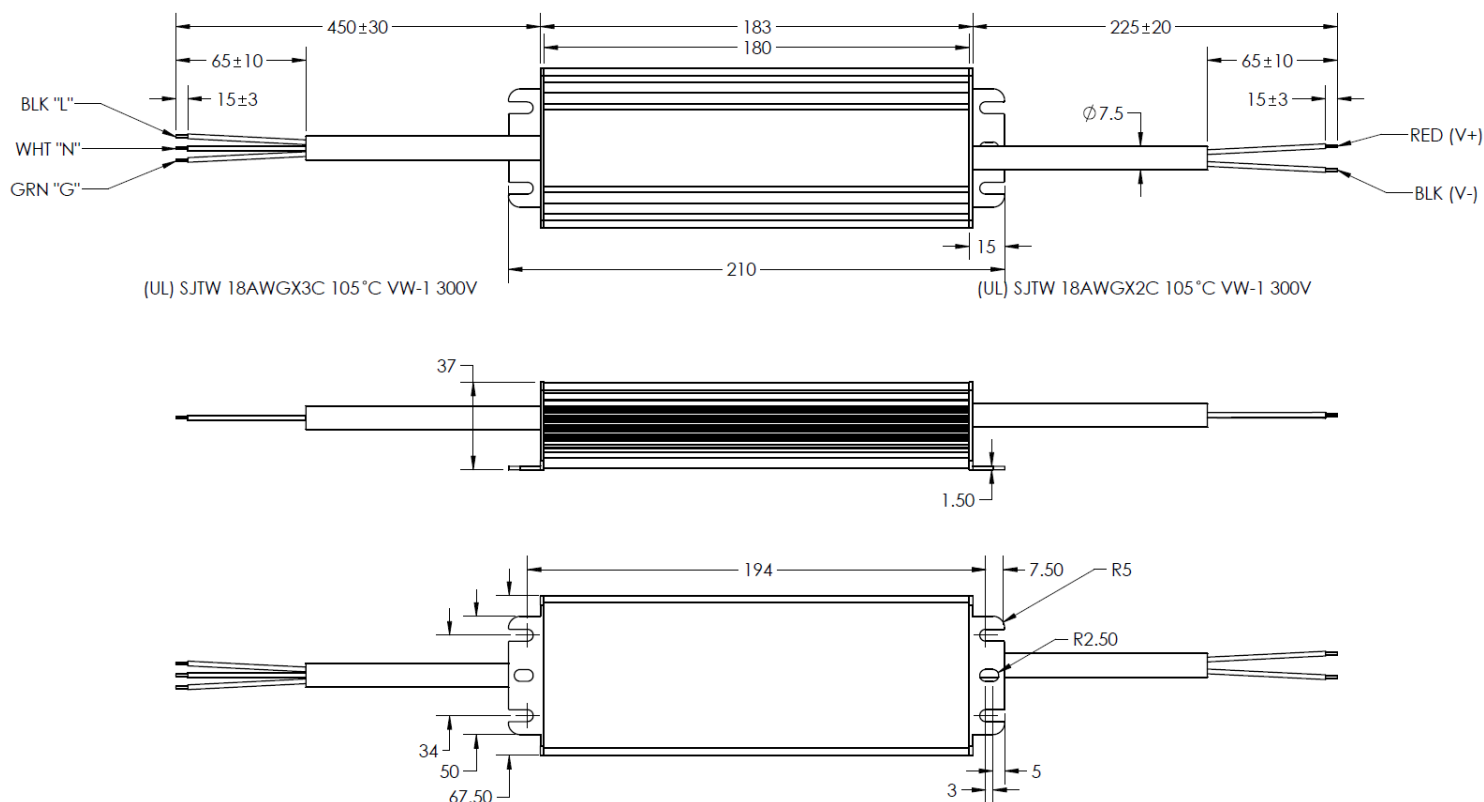


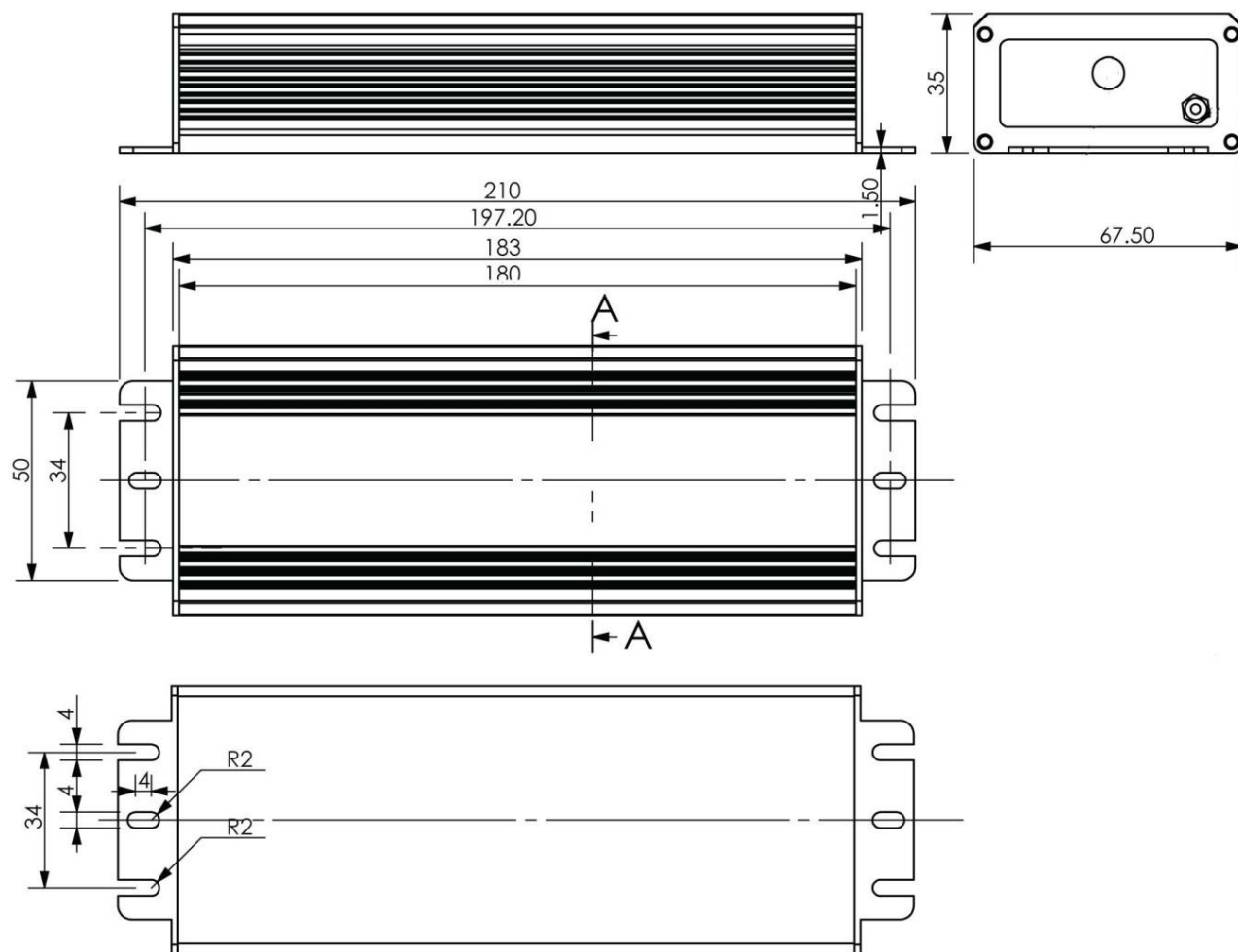
Safety & EMC	Safety Standards	UL8750, Compliance to UL1012 UL1310. CAN/CSA-C22.2 No. 223, CSA-C22.2 No. 107.1, CSA-C22.2 No. 250.0-13-12 EN61347.1, EN61347-2-13 and EN60529
	EMI Conduction & Radiation	Meet EN55015 FCC Part 15 Class B
	EMS Immunity	EN 61000-3-2, EN 61000-3-3, EN 61000-4-2, EN61000-4-3, EN61000-4-4, EN61000-4-5, EN 61000-4-6, EN 61000-4-8, EN 61000-4-11, EN 61547
	Surge	EN61000-4-2 4kV contact discharge (6kV optional available)
Others	MTBF	415,000 hours For 1400 mA output model, measured at 110V input, 80%Load and 25°C ambient temperature (MIL-HDBK-217F)
	Lifetime	88,000 hours For 1400 mA output model, measured at 220V input, 80%Load and 45°C ambient temperature
	Dimensions	Millimeters (L*W*H) 183*67.5*35
	Weight	1000g

Notes:

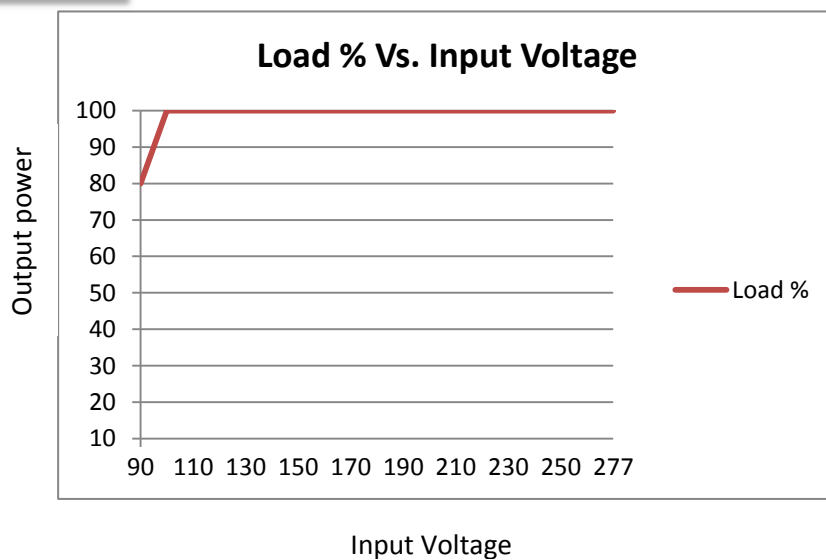
1. A suffix-xxx may be added to denote variations or modifications to the base product, where x can be any alphanumeric character or blank.
2. Class 2 output for USA & Canada.
3. Class 2 output for USA only.
4. Power Factor Correction measured at 277Vac and 70% load after thermal stabilization
5. If not specified all measurement done at full load and 230 Vac input.

Mechanical Specification

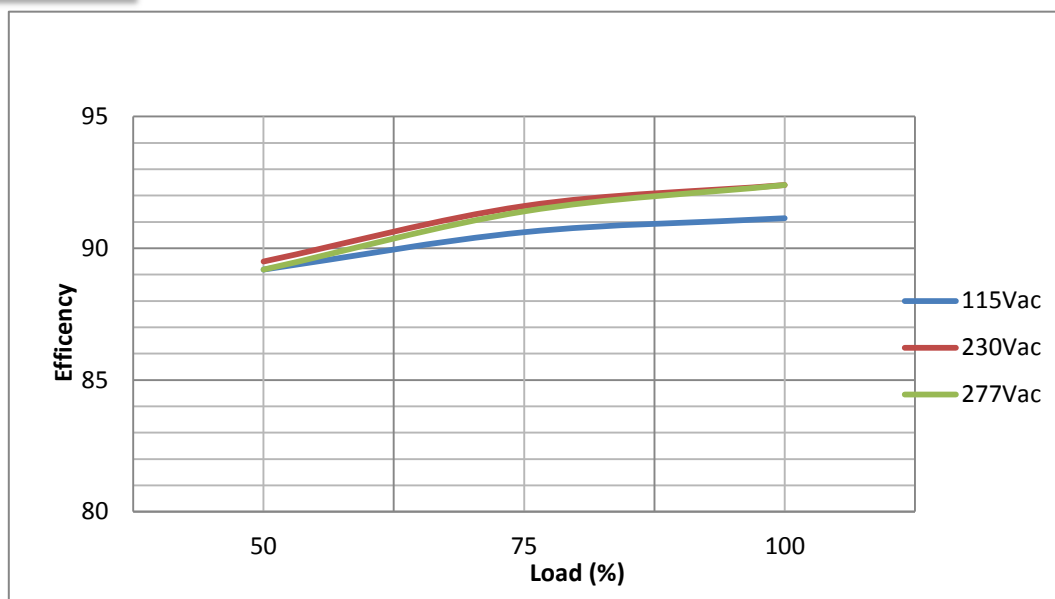




Derating Curve

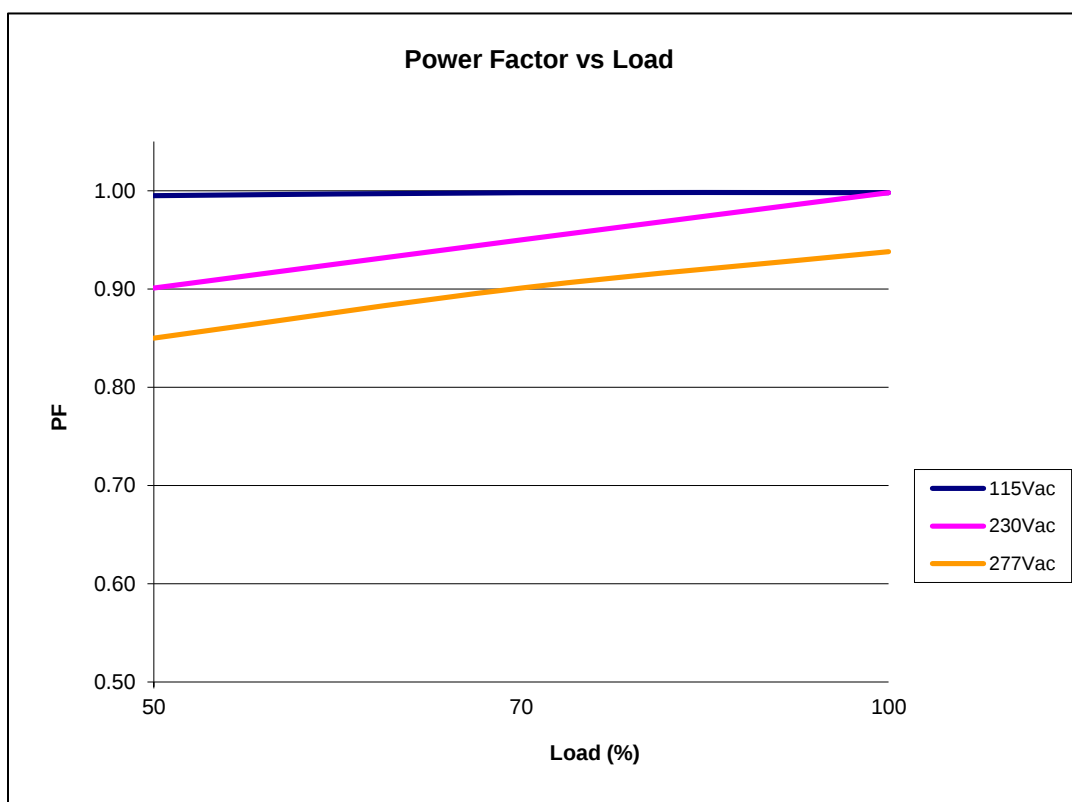


Efficiency vs. Load



54V model

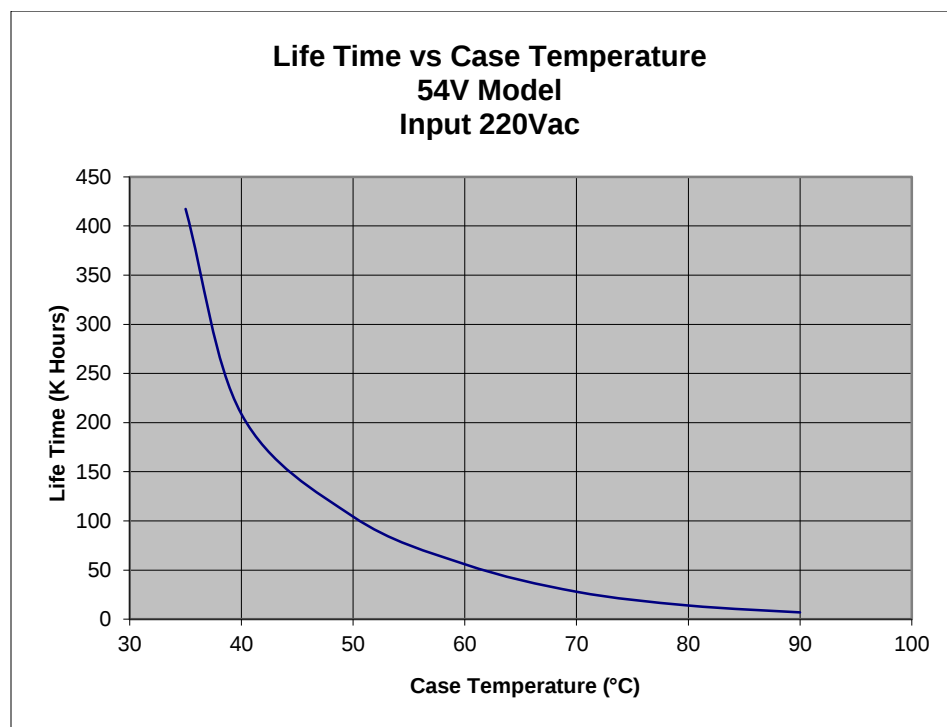
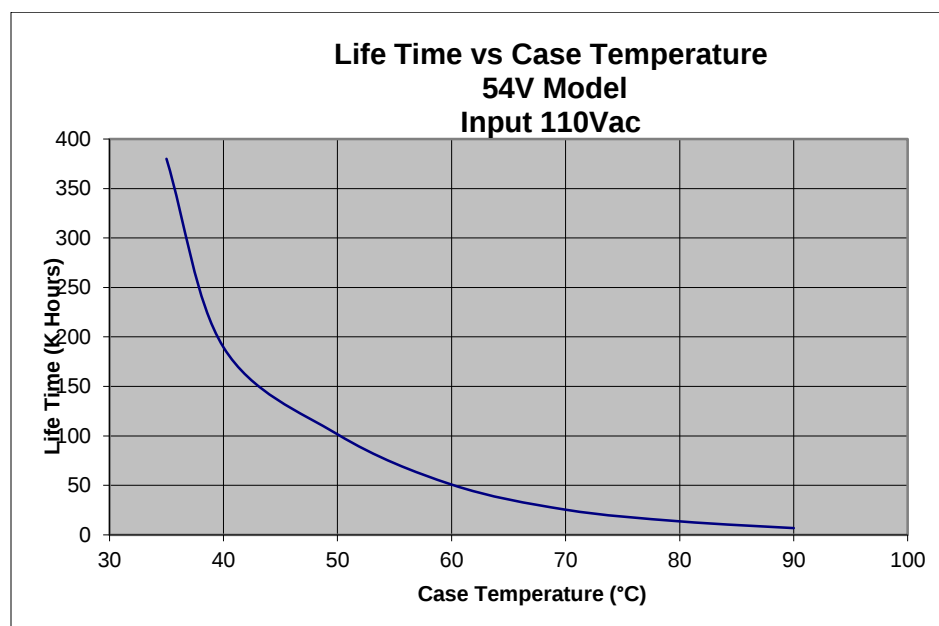
Power Factor Vs. Load



54V model



Life Time Vs. Case Temperature



PART NUMBER BUILDER

LWV100-048SX40T-XXX

L=AC/DC LED Driver

W=Wide Input Voltage 90-305V

V=Constant Voltage

100=Output Power (Watts)

048=Output Voltage

Non-Standard Part Indicator

T= Thermal Switch, blank for none

Dimming Options:
0= Non dimming

Surge Protection:
4=4kv (standard)
6=6kv (optional)

Output class:
1=Non Class 2
2=Class 2 US & Canada (up to 42V)
3= Class 2 US & Canada (43-60V)

S=Single output

SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE
AUTECH IS NOT RESPONSIBLE FOR ISSUES ARISING FROM ERRORS OR OMMISIONS

