LSDCD150 Series

150W, 20~30Vdc Input, Constant Current, DC-DC LED Driver

RoHS Compliant

Features

- Power Rating: 150W
- Input Voltage: 20~30Vdc
- Constant current design
- Output current(2100mA-3150mA)
- Efficiency up to 93%
- Dimmable with 0-10V dimming (optional)
- OVP and SCP
- Potted, watertight

*Product images are for illustrative purposes only and may vary from actual design.

Application

- Indoor or outdoor lights

Model List *(See part number scheme for model number details)

Model Number	Input Voltage Range	Output Power	Output Voltage	Output Current Min.	Output Current Max.	Efficiency	Certification
LSDC-150S210SS	20~30Vdc	150W	35~72V	2100mA	2100mA	92%	CE
LSDCD150S210SS	20~30Vdc	150W	35~72V	2100mA	2100mA	92%	CE
LSDC-150S245SS	20~30Vdc	150W	35~61V	2450mA	2450mA	92%	CE
LSDCD150S245SS	20~30Vdc	150W	35~61V	2450mA	2450mA	92%	CE
LSDC-150S280SS	20~30Vdc	150W	35~54V	2800mA	2800mA	93%	CE
LSDCD150S280SS	20~30Vdc	150W	35~54V	2800mA	2800mA	93%	CE
LSDC-150S315SS	20~30Vdc	150W	35~48V	3150mA	3150mA	93%	CE
LSDCD150S315SS	20~30Vdc	150W	35~48V	3150mA	3150mA	93%	CE
LSDC-150S360SS	20~30Vdc	150W	35~42V	3600mA	3600mA	92%	CE
LSDCD150S360SS	20~30Vdc	150W	35~42V	3600mA	3600mA	92%	CE

*(-=No dimming/D=Dimming)

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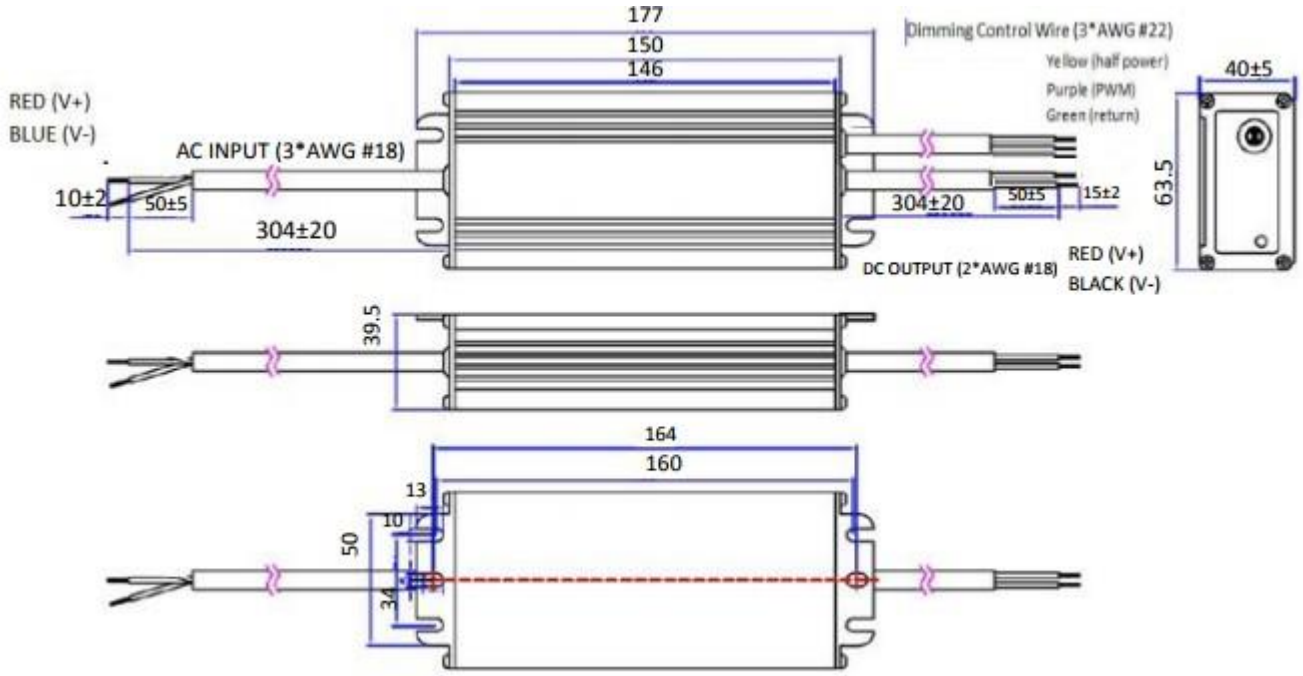
■ Technical Data

Input voltage range	20~30Vdc
Inrush current	12A Cold start, Vin=24V, Po=150W
Max input current	6.8A @full load, Vin=24V, Po=150W
Load Regulation	5%
Line Regulation	3%
Turn-on Delay Time	0.4~0.6s
Ripple & Noise (pk-pk)	0.38V, 0.38V, 0.36V, 0.33V, 0.32V
Protection	Over voltage protection: 1.3 Vo ± 5% floating deviation. Latch mode, recycle AC after resolving problem. Short circuit protection: Latch mode, recycle AC after resolving
Operating temperature	-35 ~ 50°C
Storage temperature	-40 ~ 85°C
Humidity	Operational: 10~100% RH non-condensing Storage: 5~100% RH
MTBF	100,000 hours
Life rating	60,000 hours
Length (L)	5.91” (150mm)
Width (W)	2.58” (65.5mm)
Height (H)	1.56” (39.5mm)

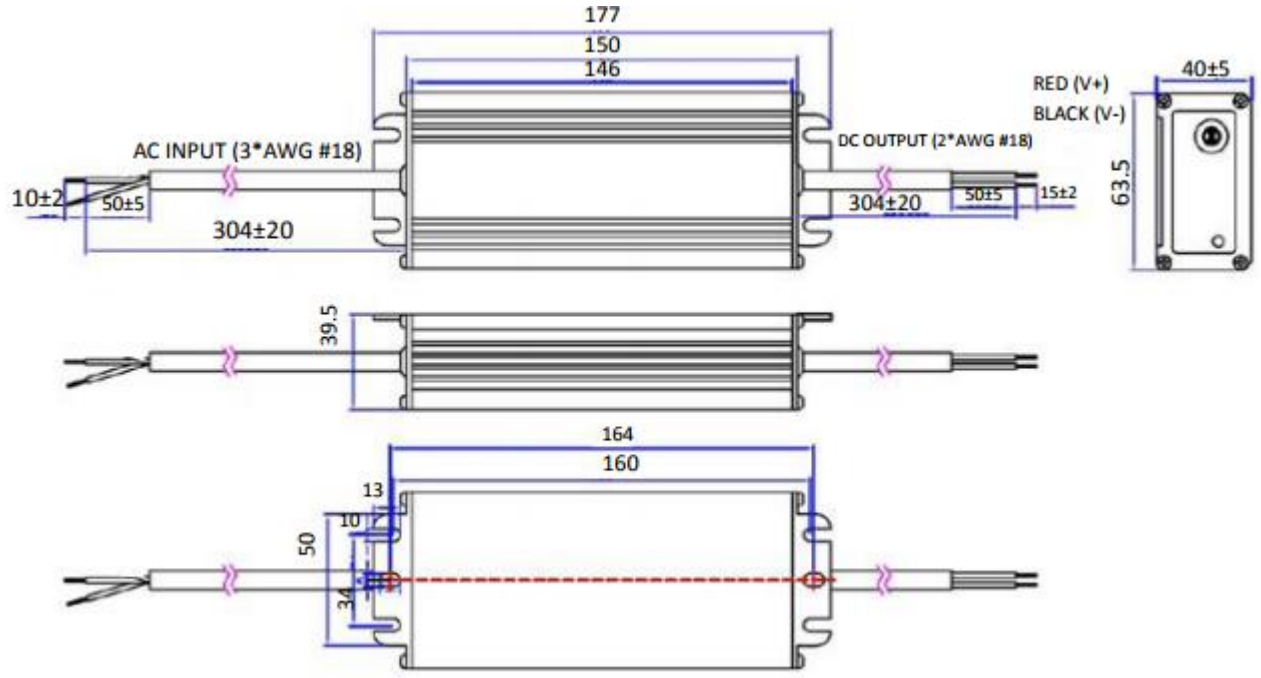
Notes:

1. All specifications are typical at 25°C unless otherwise stated.
2. The “Efficiency” values are measured at full load, after the unit is thermally stabilized, otherwise they will be lower about 1%.
3. The “Ripple & Noise” values are measured at 20HMz bandwidth oscilloscope and the output paralleled a 0.1uF ceramic capacitor and a 10uF electrolytic capacitor.

■ Dimming Mechanical Diagram



■ Non-Dimming Mechanical Diagram



***Requires a slow blow in-line fuse and housing on the input cable, contact the factory.**

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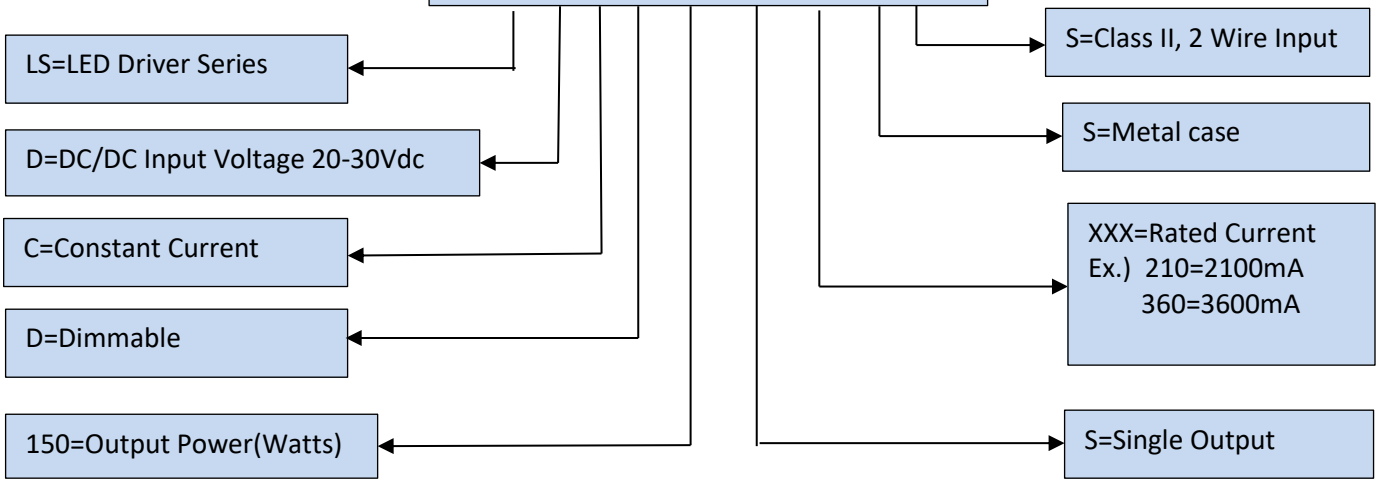
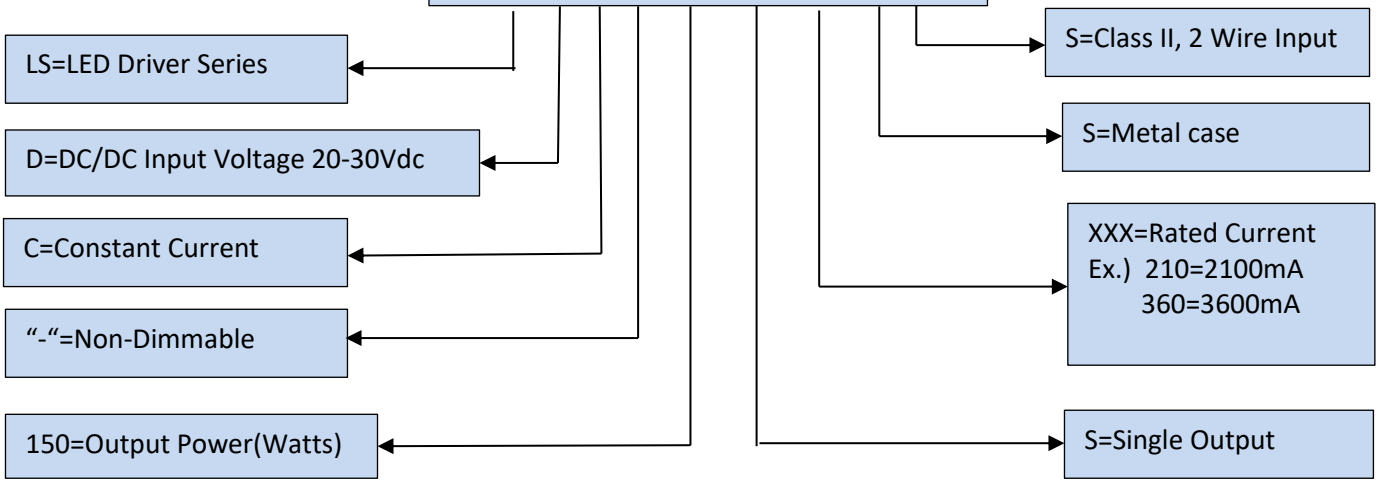
Part Number Scheme

LS D C D 150 S XXX S S



Part Number Scheme

LS D C - 150 S XXX S S



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***Specifications are subject to change without notice. Autec is not responsible for issues arising from errors or omissions.**

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