

EXB250 48V SERIES

Single Output

Embedded Power for
Business-Critical Continuity

EXB250 48V SERIES

Total Power: 165 W
Input Voltage: 33 - 75 VDC
of Outputs: Single

Special Features

- High efficiency topology, 90% typical at 3.3V
- High output current, high useable power density
- Industry standard footprint
- Wide operating temperature, -40°C to +100°C (baseplate temperature)
- 90% to 110% output trim
- No minimum load
- Overvoltage protection
- Remote on/off
- Approvals to EN60950 (TÜV Rheinland) and UL/cUL1950
- Complies with ETS 300 019-1-3/2-3
- Complies with ETS 300 132-2 (input voltage and current requirements)
- Complies with ETS 300 386-1
- Available RoHS compliant
- 2 year warranty



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NOTICE SOME MODELS LISTED IN THIS DOCUMENT HAVE BEEN DISCONTINUED

Please contact your local Artesyn representative or use the on line model number search tool at <http://www.artesyn.com/powergroup/products.htm> to find a suitable alternative.

The EXB250 is a new high efficiency, open-frame, isolated 165 Watt converter series. The EXB250 delivers very high output current at low voltages and excellent useable power density for today's high-end applications. The design takes advantage of open-frame construction to provide a low weight, low thermal impedance baseplate design. The simplicity of the baseplate design enables customers to easily manage cooling of the product, eliminating the guesswork associated with competing products. The seven models in the series feature an input voltage range of 33 to 75VDC and are available in output voltages of 12V, 5V, 3.3V, 2.5V, 1.8V, 1.5V and 1.2V. The output voltage on each model is adjustable from 90% to 110% of the nominal value. Typical efficiencies for the models are 90% for the 3.3V, 88% for the 2.5V, and 87% for the 1.8V version. The EXB250 series also has a remote on/off capability. Overcurrent and overvoltage protection features are included as standard. With full international safety approval including EN60950 (TÜV Rheinland) and UL/cUL1950, the EXB250 reduces compliance costs and time to market.

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Stresses in excess of the maximum ratings can cause permanent damage to the device. Operation of the device is not implied at these or any other conditions in excess of those given in the specification. Exposure to absolute maximum ratings can adversely affect device reliability.

Absolute Maximum Ratings

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Input voltage - continuous	V _{in} (cont)	-0.3		75	V DC	V _{in(+)} - V _{in(-)}
Input voltage - peak/surge	V _{in} (peak)	-0.3		100	V DC	Peaks of any duration, converter OFF above 80V, non-latching, unit ON once V _{in} < 75V
Input voltage - remote pin	V _{rem} (peak)	-0.3		75	V DC	Peaks of any duration
Operating temperature	T _{op}	-40		100	°C	Measured at baseplate
Storage temperature	T _{storage}	-40		125	°C	
Output power (1.2V)	P _{out} (max)			72	W	
Output power (1.5V)	P _{out} (max)			90	W	
Output power (1.8V)	P _{out} (max)			108	W	
Output power (2.5V)	P _{out} (max)			150	W	
Output power (3.3V)	P _{out} (max)			165	W	
Output power (5V)	P _{out} (max)			165	W	
Output power (12V)	P _{out} (max)			165	W	

All specifications are typical at nominal input Vin = 48V, full load at 25 °C unless otherwise specified.

Input Characteristics

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Input voltage - operating	V _{in} (oper)	33	48	75	V DC	
Input current - no load (1.2V)	I _{in}		67	85	mADC	V _{in} (min) - V _{in} (max), enabled
Input current - no load (1.5V)	I _{in}		80	100	mADC	V _{in} (min) - V _{in} (max), enabled
Input current - no load (1.8V)	I _{in}		110	135	mADC	V _{in} (min) - V _{in} (max), enabled
Input current - no load (2.5V)	I _{in}		95	115	mADC	V _{in} (min) - V _{in} (max), enabled
Input current - no load (3.3V)	I _{in}		114	145	mADC	V _{in} (min) - V _{in} (max), enabled
Input current - no load (5V)	I _{in}		110	145	mADC	V _{in} (min) - V _{in} (max), enabled
Input current - no load (12V)	I _{in}		112	145	mADC	V _{in} (min) - V _{in} (max), enabled
Input current - Quiescent	I _{in} (off)		26	35	mADC	Converter disabled
Input voltage variation	dv/dt			5	V/ms	Complies with ETS300 132 Part 4.4
Inrush current (i ² t)	I _{inrush}		0.03		A ² s	Complies with ETS300 132 Part 4.7, with recommended LISN and recommended external bypass
Inrush current ratio	I _t /I _m		6.8		capacitor	Complies with ETS300 132 Part 4.7, with recommended LISN and recommended external bypass
Input ripple rejection			50		capacitor	Frequency <1 kHz
Input fuse				10	dB A	Slow Blow/Antisurge HRC recommended 200V Rating. See Application Note 119

Turn On/Off

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Input voltage - turn on	V _{in} (on)	30	32.5	34.0	V DC	
Input voltage - turn off	V _{in} (off)	29	30.5	33.0	V DC	
Hysteresis			3		V DC	
Turn on delay - enabled, then power applied	T _{delay} (power)		10	14	msec	With the Remote On/Off signal asserted, time from when V _{in} > V _{in} (oper) until V _{out} is within total regulation band
Turn on delay - power applied, then enabled	T _{delay} (enable)		3	5	msec	With V _{in} = V _{in} (nom), then Remote On/Off asserted, time until V _o is within total error band
Rise time	T _{rise}		1.0	2.0	msec	From 10% to 90%, full resistive load, no external capacitance

Signal Electrical Interface

Characteristic - Signal Name	Symbol	Min	Typ	Max	Units	Notes and Conditions
At Remote ON/OFF pin Open collector or equivalent compatible						See Notes 1 and 2
Control pin open circuit voltage	V _{ih}	4	4.5	5	V	I _{ih} = 0µA; open circuit voltage
High level input voltage	V _{ih}	4			V	Converter guaranteed ON when control pin is greater than V _{ih} (min)
High level input current	I _{ih}			10	µA	Current flowing into control pin when pin is pulled high (max. at V _{ih} = 75V)
Acceptable high level leakage current	I _{ih} (leakage)			-10	µA	Acceptable leakage current from signal pin into the open collector driver (neg = from converter)
Low level input voltage	V _{il}	-0.3		1.2	V	Converter guaranteed off when control pin is less than V _{il} (max)
Low level input current	I _{il}		-0.2	-0.4	mA	V _{il} = 0.4 V
Low level input current	I _{il} (max)		-0.2	-0.4	mA	V _{il} = 0.0 V, maximum source current from converter with short circuit

Common Protection/Control

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Overtemperature shutdown threshold	T _{ots}	110	115	120	°C	Baseplate temperature, non-latching shutdown protection
Overtemperature shutdown - restart hysteresis			4		°C	
Remote sense compensation				10	%	% of Vo (nom), compensation includes trim. See Application Note

Reliability and Service Life

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Mean time between failure	MTBF	1,598,243			Hours	Telcordia SR-332, V _{in} = V _{in} (nom); I _{out} = I _{out} (max); ambient 40°C; ground benign environment
Mean time between failure	MTBF	>2,000,000			Hours	Demonstrated
HALT testing						Completed
Asynchronous dynamic testing						Completed

Isolation

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Input to output test voltage				1500	V DC	Test duration 1s
Input to output capacitance			2000		pF	
Input to output resistance		100			MΩ	Measured with 500 V DC
Input to output insulation system			Operational			

Other Specifications

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Switching frequency	f_{sw}	340	400	460	kHz	Fixed frequency (all models)
Weight			73		g	Statistical weight data available

Environmental Requirements

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Thermal performance		-40		100	°C	Baseplate temperature
Altitude				3000	m	Derate total max. output current by 20%
				9843	ft	Derate total max. output current by 20%
				10000	m	Derate total max. output current by 50%
				32808	ft	Derate total max. output current by 50%
Type	Parameter	Reference		Test Level		Notes and Conditions
Air temperature	Low	IEC 68-2-1		-40 °C		All characteristics and parameters extracted from ETS 300 019 classes 3.1, 3.2, 3.3, 3.4 and 3.5 $T_{max.} = +65^{\circ}\text{C}$ for T3.4
	High	IEC 68-2-2		+65 °C		
	Change	IEC 68-2-14		-40 °C to +65 °C		
Relative humidity	Low			10%		
	High	IEC 68-2-56		100%		
	Condensation	IEC 68-2-30		90 to 100%		
Vibration IEC class 3M5	Freq. velocity	IEC 68-2-6		5-9Hz 5mm/s		
	Freq. acceleration	IEC 68-2-6		9-200Hz 1g		
Shocks IEC class 3M5	Acceleration	IEC 68-2-29		10g		

Referenced ETSI standards:

ETS 300 019: Environmental conditions and environmental tests for telecommunications equipment
 ETS 300 019: Part 1-3 (1997) Classification of environmental conditions stationary use at weather protected locations
 ETS 300 019: Part 2-3 (1997) Specification of environmental tests stationary use at weather protected locations

EMC Electromagnetic Compatibility

Phenomenon	Port	Standard	Test level	Criteria	Notes and conditions
Immunity:					
ESD	Enclosure	EN61000-4-2	6kV contact 8kV air	NP RP	As per ETS 300 386-1 table 5
EFT	DC power	EN61000-4-4	2kV 4kV	NP LFS	As per ETS 300 386-1 table 5
	Signal	EN61000-4-4	1kV 2kV	NP LFS	See Application Note 119 See Application Note 119
Radiated field	Enclosure	EN61000-4-3	10V/m	NP	As per ETS 300 386-1 table 5
Conducted	DC power	EN61000-4-6	10V	NP	As per ETS 300 386-1 table 5
	Signal	EN61000-4-6	10V	NP	See Application Note 119
Input transients	DC power	ETS 300 132 ETR 283			

EMC Electromagnetic Compatibility

Phenomenon	Port	Standard	Test level	Criteria	Notes and conditions
Emission:					
Conducted	DC power	EN55022	Level A		With recommended external filter for compliance bandwidth 20 kHz to 30 MHz, as per ETS 300 386-1
			EN55022	Level B	With recommended external filter for compliance bandwidth 20 kHz to 30 MHz, as per ETS 300 386-1
	Signal	EN55022	TBD		Bandwidth 150kHz to 30MHz, as per ETS 300 386-1
Radiated		EN55022	TBD		Bandwidth 30 MHz to 1 GHz, as per ETS 300 386-1

Performance criteria:

NP: Normal Performance: EUT shall withstand applied test and operate within relevant limits as specified without damage

RP: Reduced Performance: EUT shall withstand applied test. Reduced performance is permitted within specified limits, resumption to normal performance shall occur at the cessation of the test

LFS: Loss of Function (self recovery): EUT shall withstand applied test without damage, temporary loss of function permitted during test. Unit will self recover to normal performance after test

Standards Compliance List

Characteristic	
EN60950	
UL/cUL 1950	3rd edition
TÜV Rheinland	

Safety Agency Approvals

Standard	Category
UL/cUL 1950 File Number	E135734
TÜV Rheinland Certificate No.	R2172505

Material Ratings

Characteristic - Signal Name	Notes and Conditions
Flammability rating	UL94V-0
Material type	FR4 PCB, T-LAM IMS - Baseplate

Model Numbers

Model Number	Input Voltage	Output Voltage	Oversupply Protection	Output Current (Max.)	Typical Efficiency
EXB250-48S1V2J	33-75 VDC	1.2V	1.44V	60A	84.0%
EXB250-48S1V5J	33-75 VDC	1.5V	1.9V	60A	85.5%
EXB250-48S1V8J	33-75 VDC	1.8V	2.5V	60A	87.0%
EXB250-48S2V5J	33-75 VDC	2.5V	3.0V	60A	88.0%
EXB250-48S3V3J	33-75 VDC	3.3V	4.0V	50A	90.0%
EXB250-48S05J	33-75 VDC	5.0V	6.0V	33A	91.7%
EXB250-48S12J	33-75 VDC	12.0V	14.4V	13.75A	92.0%

RoHS Compliance Ordering Information

The 'J' at the end of the part number indicates that the part is Pb-free (RoHS 6/6 compliant). TSE RoHS 5/6 (non Pb-free) compliant versions may be available on special request, please contact your local sales representative for details.

EXB250-48S1V2J Model**Input Characteristics**

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Input current - maximum	I_{in} (max.)		2.45	2.6	A DC	$V_{in} = V_{in}$ (min); $I_{out} = I_{out}$ (max); $V_o = V_o$ (nom), measured at converter
Reflected ripple current	I_{in} (ripple)		9 25		mA RMS mA pk-pk	$I_{out} = I_{out}$ (max), measured with standard filter. See Application Note 119
Input Capacitor ripple current	I_{in} (cap)		125 350		mA RMS mA pk-pk	$I_{out} = I_{out}$ (max), measured without standard filter. See Application Note 119
Input capacitance - Internal	C_{input}	6	7.5	13.5	μF	Internal to converter
Input capacitance - External bypass	C_{bypass}		33		μF	Recommended customer added capacitance

EXB250-48S1V2J Model**Electrical Characteristics - O/P**

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Nominal set-point voltage	V_o (nom)	1.179	1.200	1.221	V DC	$V_{in} = V_{in}$ (nom); $I_{out} = I_{out}$ (nom)
Total regulation band	V_o	1.164		1.236	V DC	For all line, static load and temperature until end of life.
Line regulation			0.01	0.2	%	$I_{out} = I_{out}$ (nom), V_{in} (min) to V_{in} (max)
Load regulation			0.02	0.2	%	$V_{in} = V_{in}$ (nom), I_{out} (min) to I_{out} (max)
Temperature regulation			0.002	0.02	$\pm\%/\text{ }^{\circ}\text{C}$	$V_{in} = V_{in}$ (nom), $I_{out} = I_{out}$ (max)
Output current continuous	I_{out}	0		60	A DC	
Output current - short circuit	I_{sc}	65	69	77	A DC	Continuous, unit auto recovers from short, $V_o < 100mV$
Load transient response - peak deviation	$V_{dynamic}/V_o$ (nom)			6	%	Peak deviation for 50% to 75% step load, $di/dt = 100mA/\mu\text{sec}$, of V_o (nom)
Load transient response - recovery	$T_{recovery}$			150	μsec	Settling time to within 1% of output set point voltage for 50% to 75% load step
External load capacitance	C_{ext}	0		15,000	μF	Higher load capacitance values may be possible. Contact Artesyn Technologies for details
Output voltage - noise	V_{p-p} V_{rms}			60 20	mV pk-pk mV rms	Measurement bandwidth 20 MHz See Application Note 119 for test set-up

EXB250-48S1V2J Model

Protection and Control Features

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Overvoltage clamp voltage	V_{ov}	1.32	1.44	1.56	V DC	Non-latching. See Application Note 119 for details
Overcurrent limit inception	I_{oc}		69	77	A DC	$V_o = 90\% \text{ of } V_o (\text{nom})$
Output voltage trim range		90		110	%	Trim up (% of V_o nom) Trim down (% of V_o nom) See Application Note 119 for details of trim equations and trim curves
Open sense voltage		1.15	1.20	1.30	V DC	

EXB250-48S1V2J Model

Efficiency

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Efficiency	η	82.5	84.0		%	$I_{out} = 100\% I_{out} (\text{max}),$ $V_{in} = V_{in} (\text{nom})$
Efficiency	η	84.0	85.2		%	$I_{out} = 50\% I_{out} (\text{max}),$ $V_{in} = V_{in} (\text{nom})$

EXB250-48S1V5J Model

Input Characteristics

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Input current - maximum	I _{in} (max.)		3.2	3.4	A DC	V _{in} = V _{in} (min); I _{out} = I _{out} (max); V _o = V _o (nom), measured at converter
Reflected ripple current	I _{in} (ripple)		9 25		mA RMS mA pk-pk	I _{out} = I _{out} (max), measured with standard filter. See Application Note 119
Input Capacitor ripple current	I _{in} (cap)		125 350		mA RMS mA pk-pk	I _{out} = I _{out} (max), measured without standard filter. See Application Note 119
Input capacitance - Internal	C _{input}	6	7.5	13.5	μF	Internal to converter
Input capacitance - External bypass	C _{bypass}		33		μF	Recommended customer added capacitance

EXB250-48S1V5J Model

Electrical Characteristics - O/P

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Nominal set-point voltage	V _o (nom)	1.476	1.50	1.524	V DC	V _{in} = V _{in} (nom); I _{out} = I _{out} (nom)
Total regulation band	V _o	1.455		1.545	V DC	For all line, static load and temperature until end of life.
Line regulation			0.01	0.2	%	I _{out} = I _{out} (nom), V _{in} (min) to V _{in} (max)
Load regulation			0.02	0.2	%	V _{in} = V _{in} (nom), I _{out} (min) to I _{out} (max)
Temperature regulation			0.002	0.02	±%/°C	V _{in} = V _{in} (nom), I _{out} = I _{out} (max)
Output current continuous	I _{out}	0		60	A DC	
Output current - short circuit	I _{sc}	65	69	77	A DC	Continuous, unit auto recovers from short, V _o < 100mV
Load transient response - peak deviation	V _{dynamic} / V _o (nom)			6	%	Peak deviation for 50% to 75% step load, di/dt = 100mA/μsec, of V _o (nom)
Load transient response - recovery	T _{recovery}			150	μsec	Settling time to within 1% of output set point voltage for 50% to 75% load step
External load capacitance	C _{ext}	0		15,000	μF	Higher load capacitance values may be possible. Contact Artesyn Technologies for details
Output voltage - noise	V _{p-p} V _{rms}			60 20	mV pk-pk mV rms	Measurement bandwidth 20 MHz See Application Note 119 for test set-up

EXB250-48S1V5J Model

Protection and Control Features

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Overvoltage clamp voltage	V_{ov}	1.66		1.95	V DC	Non-latching. See Application Note 119 for details
Overcurrent limit inception	I_{oc}		69	77	A DC	$V_o = 90\% \text{ of } V_o (\text{nom})$
Output voltage trim range		90		110	%	Trim up (% of V_o nom) Trim down (% of V_o nom) See Application Note 119 for details of trim equations and trim curves
Open sense voltage		1.45	1.53	1.59	V DC	

EXB250-48S1V5J Model

Efficiency

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Efficiency	η	84.5	85.5		%	$I_{out} = 100\% I_{out} (\text{max}),$ $V_{in} = V_{in} (\text{nom})$
Efficiency	η	85.5	86.5		%	$I_{out} = 50\% I_{out} (\text{max}),$ $V_{in} = V_{in} (\text{nom})$

EXB250-48S1V8J Model

Input Characteristics						
Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Input current - maximum	I_{in} (max.)		3.8	3.9	A DC	$V_{in} = V_{in}$ (min); $I_{out} = I_{out}$ (max); $V_o = V_o$ (nom), measured at converter
Reflected ripple current	I_{in} (ripple)		6 25		mA RMS mA pk-pk	$I_{out} = I_{out}$ (max), measured with standard filter. See Application Note 119
Input Capacitor ripple current	I_{in} (cap)		90 225		mA RMS mA pk-pk	$I_{out} = I_{out}$ (max), measured without standard filter. See Application Note 119
Input capacitance - Internal	C_{input}	4.8	6.0	10.8	μF	Internal to converter
Input capacitance - External bypass	C_{bypass}		33		μF	Recommended customer added capacitance

EXB250-48S1V8J Model

Electrical Characteristics - O/P						
Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Nominal set-point voltage	V_o (nom)	1.77	1.80	1.83	V DC	$V_{in} = V_{in}$ (nom); $I_{out} = I_{out}$ (nom)
Total regulation band	V_o	1.74		1.85	V DC	For all line, static load and temperature until end of life.
Line regulation			0.01	0.1	%	$I_{out} = I_{out}$ (nom), V_{in} (min) to V_{in} (max)
Load regulation			0.02	0.2	%	$V_{in} = V_{in}$ (nom), I_{out} (min) to I_{out} (max)
Temperature regulation			0.002	0.02	$\pm\%/\text{ }^{\circ}\text{C}$	$V_{in} = V_{in}$ (nom), $I_{out} = I_{out}$ (max)
Output current continuous	I_{out}	0		60	A DC	
Output current - short circuit	I_{sc}	65	69	77	A DC	Continuous, unit auto recovers from short, $V_o < 100\text{mV}$
Load transient response - peak deviation	$V_{dynamic}/V_o$ (nom)			6	%	Peak deviation for 50% to 75% step load, $di/dt = 100\text{mA}/\mu\text{sec}$, of V_o (nom)
Load transient response - recovery	$T_{recovery}$			150	μsec	Settling time to within 1% of output set point voltage for 50% to 75% load step
External load capacitance	C_{ext}	0		15,000	μF	Higher load capacitance values may be possible. Contact Artesyn Technologies for details
Output voltage - noise	V_{p-p} V_{rms}			60 20	mV pk-pk mV rms	Measurement bandwidth 20 MHz See Application Note 119 for test set-up

EXB250-48S1V8J Model

Protection and Control Features

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Overvoltage clamp voltage	V_{ov}	2.0		2.34	V DC	Non-latching. See Application Note 119 for details
Overcurrent limit inception	I_{oc}		69	77	A DC	$V_o = 90\% \text{ of } V_o (\text{nom})$
Output voltage trim range		90		110	%	Trim up (% of V_o nom) Trim down (% of V_o nom) See Application Note 119 for details of trim equations and trim curves
Open sense voltage		1.75	1.83	1.89	V DC	

EXB250-48S1V8J Model

Efficiency

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Efficiency	η	85.5	87		%	$I_{out} = 100\% I_{out} (\text{max}),$ $V_{in} = V_{in} (\text{nom})$
Efficiency	η	86.5	88		%	$I_{out} = 50\% I_{out} (\text{max}),$ $V_{in} = V_{in} (\text{nom})$

EXB250-48S2V5J Model

Input Characteristics

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Input current - maximum	I_{in} (max.)		5.4	5.5	A DC	$V_{in} = V_{in}$ (min); $I_{out} = I_{out}$ (max); $V_o = V_o$ (nom) (measured at converter)
Reflected ripple current	I_{in} (ripple)		9 25		mA RMS mA pk-pk	$I_{out} = I_{out}$ (max), measured with standard filter. See Application Note 119
Input Capacitor ripple current	I_{in} (cap)		89 250		mA RMS mA pk-pk	$I_{out} = I_{out}$ (max), measured without standard filter. See Application Note 119
Input capacitance - Internal	C_{input}	6.0	7.5	13.5	μF	Internal to converter
Input capacitance - External bypass	C_{bypass}		33		μF	Recommended customer added capacitance

EXB250-48S2V5J Model

Electrical Characteristics - O/P

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Nominal set-point voltage	V_o (nom)	2.46	2.50	2.54	V DC	$V_{in} = V_{in}$ (nom); $I_{out} = I_{out}$ (nom)
Total regulation band	V_o	2.42		2.58	V DC	For all line, static load and temperature until end of life.
Line regulation			0.01	0.1	%	$I_{out} = I_{out}$ (nom), V_{in} (min) to V_{in} (max)
Load regulation			0.02	0.2	%	$V_{in} = V_{in}$ (nom), I_{out} (min) to I_{out} (max)
Temperature regulation			0.002	0.02	$\pm\%/\text{ }^{\circ}\text{C}$	$V_{in} = V_{in}$ (nom), $I_{out} = I_{out}$ (max)
Output current continuous	I_{out}	0		60	A DC	
Output current - short circuit	I_{sc}	65	69	77	A DC	Continuous, unit auto recovers from short, $V_o < 100\text{mV}$
Load transient response - peak deviation	$V_{dynamic}/V_o$ (nom)			6	%	Peak deviation for 50% to 75% step load, $di/dt = 100\text{mA}/\mu\text{sec}$, of V_o (nom)
Load transient response - recovery	$T_{recovery}$			150	μsec	Settling time to within 1% of output set point voltage for 50% to 75% load step
External load capacitance	C_{ext}	0		15,000	μF	Higher load capacitance values may be possible. Contact Artesyn Technologies for details
Output voltage - noise	V_{p-p} V_{rms}			60 20	mV pk-pk mV rms	Measurement bandwidth 20 MHz See Application Note 119 for test set-up

EXB250-48S2V5J Model

Protection and Control Features

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Overvoltage clamp voltage	V_{ov}	2.78		3.125	V DC	Non-latching. See Application Note 119 for details
Overcurrent limit inception	I_{oc}		69	77	A DC	$V_o = 90\% \text{ of } V_o (\text{nom})$
Output voltage trim range		90		110	%	Trim up (% of V_o nom) Trim down (% of V_o nom) See Application Note 119 for details of trim equations and trim curves
Open sense voltage		2.46	2.54	2.62	V DC	

EXB250-48S2V5J Model

Efficiency

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Efficiency	η	87	88		%	$I_{out} = 100\% I_{out} (\text{max})$, $V_{in} = V_{in} (\text{nom})$
Efficiency	η	89	90		%	$I_{out} = 50\% I_{out} (\text{max})$, $V_{in} = V_{in} (\text{nom})$

EXB250-48S3V3J Model

Input Characteristics

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Input current - maximum	I_{in} (max.)		5.6	5.7	A DC	$V_{in} = V_{in}$ (min); $I_{out} = I_{out}$ (max); $V_o = V_o$ (nom) (measured at converter)
Reflected ripple current	I_{in} (ripple)		9 25		mA RMS mA pk-pk	$I_{out} = I_{out}$ (max), measured with standard filter. See Application Note 119
Input Capacitor ripple current	I_{in} (cap)		125 350		mA RMS mA pk-pk	$I_{out} = I_{out}$ (max), measured without external Pi filter. See Application Note 119
Input capacitance - Internal	C_{input}	6.0	7.5	13.5	μF	Internal to converter filter
Input capacitance - External bypass	C_{bypass}		33		μF	Recommended customer added capacitance

EXB250-48S3V3J Model

Electrical Characteristics - O/P

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Nominal set-point voltage	V_o (nom)	3.25	3.30	3.35	V DC	$V_{in} = V_{in}$ (nom); $I_{out} = I_{out}$ (nom)
Total regulation band	V_o	3.20		3.40	V DC	For all line, static load and temperature until end of life.
Line regulation			0.01	0.1	%	$I_{out} = I_{out}$ (nom), V_{in} (min) to V_{in} (max)
Load regulation			0.02	0.2	%	$V_{in} = V_{in}$ (nom), I_{out} (min) to I_{out} (max)
Temperature regulation			0.002	0.02	$\pm\%/\text{ }^{\circ}\text{C}$	$V_{in} = V_{in}$ (nom), $I_{out} = I_{out}$ (max)
Output current continuous	I_{out}	0		50	A DC	
Output current - short circuit	I_{sc}	55	59	68	A DC	Continuous, unit auto recovers from short, $V_o < 100\text{mV}$
Load transient response - peak deviation	$V_{dynamic}/V_o$ (nom)			6	%	Peak deviation for 50% to 75% step load, $di/dt = 100\text{mA}/\mu\text{sec}$, of V_o (nom)
Load transient response - recovery	$T_{recovery}$			150	μsec	Settling time to within 1% of output set point voltage for 50% to 75% load step
External load capacitance	C_{ext}	0		15,000	μF	Higher load capacitance values may be possible. Contact Artesyn Technologies for details
Output voltage - noise	V_{p-p} V_{rms}			60 20	mV pk-pk mV rms	Measurement bandwidth 20 MHz See Application Note 119 for test set-up

EXB250-48S3V3J Model

Protection and Control Features

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Overvoltage clamp voltage	V_{ov}	3.65		4.125	V DC	Non-latching. See Application Note for details
Overcurrent limit inception	I_{oc}		58	68	A DC	$V_o = 90\% \text{ of } V_{o(\text{nom})}$
Output voltage trim range		90		110	%	Trim up (% of $V_{o(\text{nom})}$) Trim down (% of $V_{o(\text{nom})}$) See Application Note 118 for details of trim equations and trim curves
Open sense voltage		3.15	3.34	3.45	V DC	

EXB250-48S3V3J Model

Efficiency

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Efficiency	η	89	90		%	$I_{out} = 100\% I_{out(\text{max})}$, $V_{in} = V_{in(\text{nom})}$
Efficiency	η	89.5	90.5		%	$I_{out} = 50\% I_{out(\text{max})}$, $V_{in} = V_{in(\text{nom})}$

EXB250-48S05J Model

Input Characteristics

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Input current - maximum	I_{in} (max.)		5.6	5.7	A DC	$V_{in} = V_{in}$ (min); $I_{out} = I_{out}$ (max); $V_o = V_o$ (nom), measured at converter
Reflected ripple current	I_{in} (ripple)		9 25		mA RMS mA pk-pk	$I_{out} = I_{out}$ (max), measured with standard filter. See Application Note 119
Input Capacitor ripple current	I_{in} (cap)		125 350		mA RMS mA pk-pk	$I_{out} = I_{out}$ (max), measured without standard filter. See Application Note 119
Input capacitance - Internal	C_{input}	6	7.5	13.5	μF	Internal to converter
Input capacitance - External bypass	C_{bypass}		33		μF	Recommended customer added capacitance

EXB250-48S05J Model

Electrical Characteristics - O/P

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Nominal set-point voltage	V_o (nom)	4.92	5.00	5.08	V DC	$V_{in} = V_{in}$ (nom); $I_{out} = I_{out}$ (nom)
Total regulation band	V_o	4.85		5.15	V DC	For all line, static load and temperature until end of life.
Line regulation			0.01	0.1	%	$I_{out} = I_{out}$ (nom), V_{in} (min) to V_{in} (max)
Load regulation			0.02	0.2	%	$V_{in} = V_{in}$ (nom), I_{out} (min) to I_{out} (max)
Temperature regulation			0.002	0.02	$\pm\%/\text{ }^{\circ}\text{C}$	$V_{in} = V_{in}$ (nom), $I_{out} = I_{out}$ (max)
Output current continuous	I_{out}	0		33	A DC	
Output current - short circuit	I_{sc}	35	37.7	43.5	A DC	Continuous, unit auto recovers from short, $V_o < 100\text{mV}$
Load transient response - peak deviation	$V_{dynamic}/V_o$ (nom)			6	%	Peak deviation for 50% to 75% step load, $di/dt = 100\text{mA}/\mu\text{sec}$, of V_o (nom)
Load transient response - recovery	$T_{recovery}$			150	μsec	Settling time to within 1% of output set point voltage for 50% to 75% load step
External load capacitance	C_{ext}	0		5,000	μF	Higher load capacitance values may be possible. Contact Artesyn Technologies for details
Output voltage - noise	V_{p-p} V_{rms}			60 20	mV pk-pk mV rms	Measurement bandwidth 20 MHz See Application Note 119 for test set-up

EXB250-48S05J Model

Protection and Control Features

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Overvoltage clamp voltage	V_{ov}	5.5	6.0	6.5	V DC	Non-latching. See Application Note 119 for details
Overcurrent limit inception	I_{oc}		35	43.5	A DC	$V_o = 90\% \text{ of } V_{o(\text{nom})}$
Output voltage trim range		90		110	%	Trim up (% of $V_{o(\text{nom})}$) Trim down (% of $V_{o(\text{nom})}$) See Application Note 119 for details of trim equations and trim curves
Open sense voltage		4.85	5.00	5.25	V DC	

EXB250-48S05J Model

Efficiency

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Efficiency	η	90.5	91.7		%	$I_{out} = 100\% I_{out(\text{max})}$, $V_{in} = V_{in(\text{nom})}$
Efficiency	η	91.0	92		%	$I_{out} = 50\% I_{out(\text{max})}$, $V_{in} = V_{in(\text{nom})}$

EXB250-48S12J Model

Input Characteristics

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Input current - maximum	I _{in} (max.)		5.6	5.7	A DC	V _{in} = V _{in} (min); I _{out} = I _{out} (max); V _o = V _o (nom), measured at converter
Reflected ripple current	I _{in} (refl)		9 25		mA RMS mA pk-pk	I _{out} = I _{out} (max), measured with standard filter. See Application Note 119
Input Capacitor ripple current	I _{in} (ripple)		125 350		mA RMS mA pk-pk	I _{out} = I _{out} (max), measured without standard filter. See Application Note 119
Input capacitance - Internal	C _{input}	6	7.5	13.5	μF	Internal to converter
Input capacitance - External bypass	C _{bypass}		33		μF	Recommended customer added capacitance

EXB250-48S12J Model

Electrical Characteristics - O/P

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Nominal set-point voltage	V _o (nom)	11.82	12.00	12.18	V DC	V _{in} = V _{in} (nom); I _{out} = I _{out} (nom)
Total regulation band	V _o	11.64		12.36	V DC	For all line, static load and temperature until end of life.
Line regulation			0.01	0.1	%	I _{out} = I _{out} (nom), V _{in} (min) to V _{in} (max)
Load regulation			0.02	0.2	%	V _{in} = V _{in} (nom), I _{out} (min) to I _{out} (max)
Temperature regulation			0.002	0.02	±%/°C	V _{in} = V _{in} (nom), I _{out} = I _{out} (max)
Output current continuous	I _{out}	0		13.75	A DC	
Output current - short circuit	I _{sc}	10	15.8	19	A DC	Continuous, unit auto recovers from short, V _o < 100mV
Load transient response - peak deviation	V _{dynamic} / V _o (nom)			6	%	Peak deviation for 50% to 75% step load, dI/dt = 100mA/μsec, of V _o (nom)
Load transient response - recovery	T _{recovery}			150	μsec	Settling time to within 1% of output set point voltage for 50% to 75% load step
External load capacitance	C _{ext}	0		1,000	μF	Higher load capacitance values may be possible. Contact Artesyn Technologies for details
Output voltage - noise	V _{p-p} V _{rms}			60 20	mV pk-pk mV rms	Measurement bandwidth 20 MHz See Application Note 119 for test set-up

EXB250-48S12J Model

Protection and Control Features

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Overvoltage clamp voltage	V_{ov}	13.5		15.6	V DC	Non-latching. See Application Note 119 for details
Overcurrent limit inception	I_{oc}	15	16.6	19	A DC	$V_o = 90\% \text{ of } V_o (\text{nom})$
Output voltage trim range		90		110	%	Trim up (% of V_o nom) Trim down (% of V_o nom) See Application Note 119 for details of trim equations and trim curves
Open sense voltage		11.6	12.0	12.5	V DC	

EXQ125-48S12J Model

Efficiency

Characteristic	Symbol	Min	Typ	Max	Units	Notes and Conditions
Efficiency	η	91	92.1		%	$I_{out} = 100\% I_{out} (\text{max})$, $V_{in} = V_{in} (\text{nom})$
Efficiency	η	91	92.0		%	$I_{out} = 50\% I_{out} (\text{max})$, $V_{in} = V_{in} (\text{nom})$

EXB250-48S1V2J Model

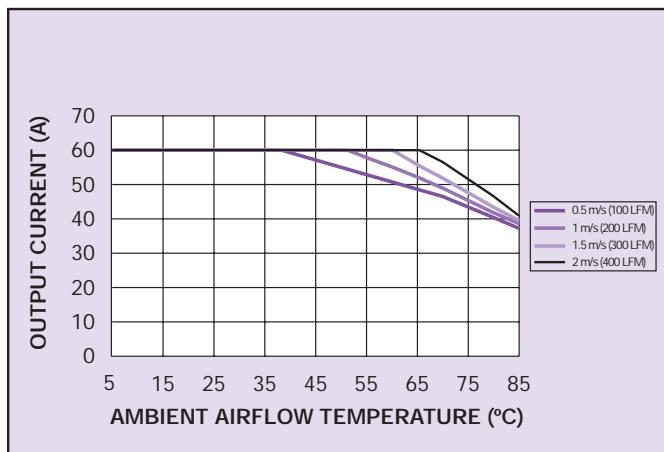


Figure 1: Derating Curve Output Current vs. Temperature

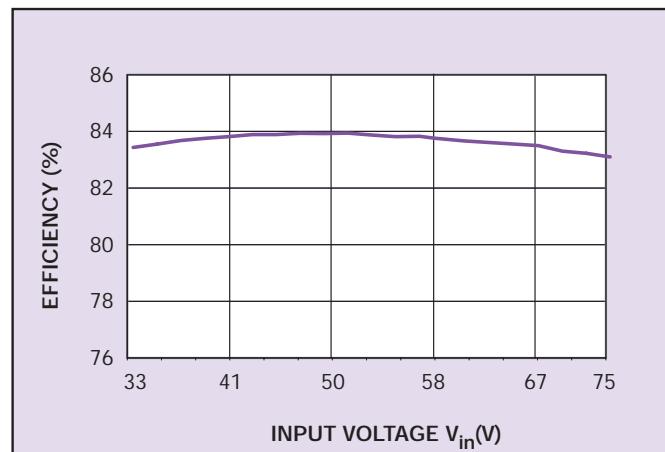


Figure 2: Efficiency vs. Line

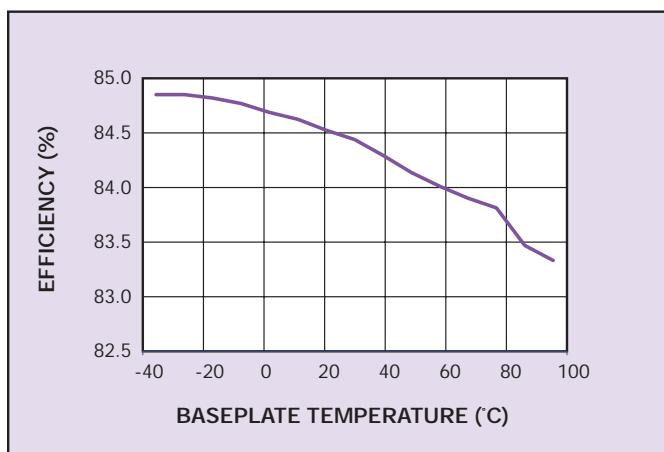


Figure 3: Typical Efficiency vs. Baseplate Temperature

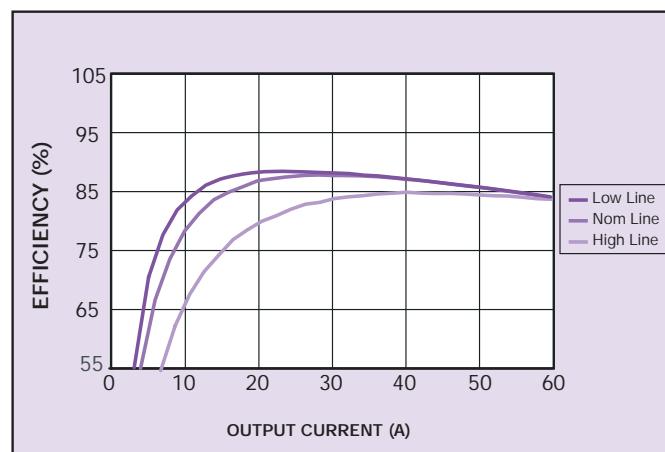


Figure 4: Efficiency vs. Load

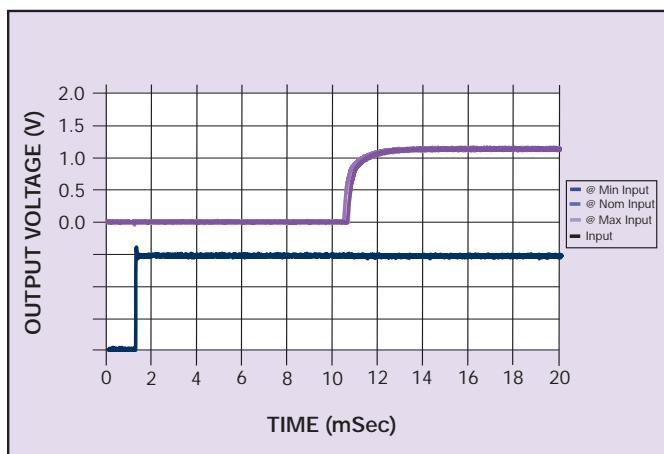


Figure 5: Turn-on Characteristic

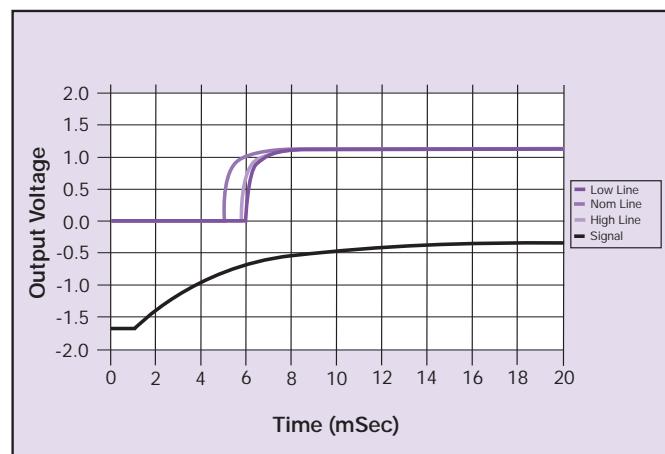


Figure 6: Control On/Off Characteristic

EXB250-48S1V2J Model

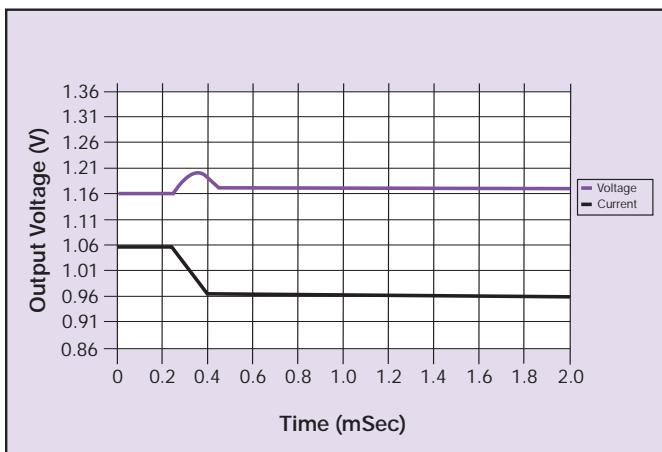


Figure 7: Typical Transient Response 75-50%
Step Load Change

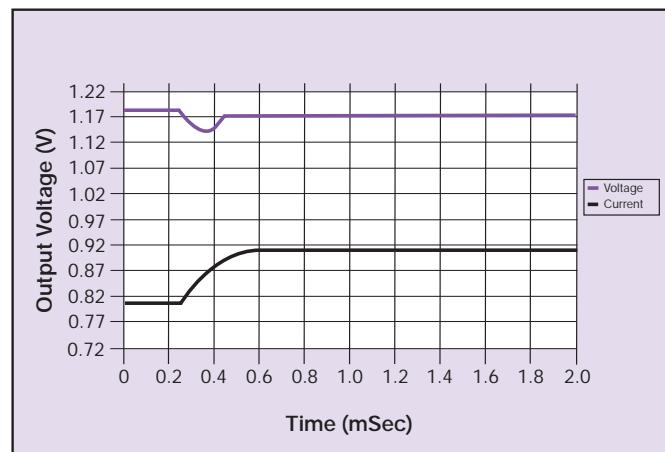


Figure 8: Typical Transient Response 50-75%
Step Load Change

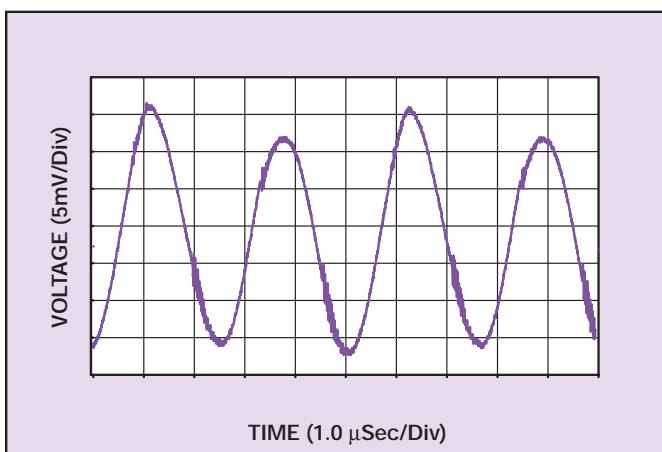


Figure 9: Output Ripple and Noise Measurement

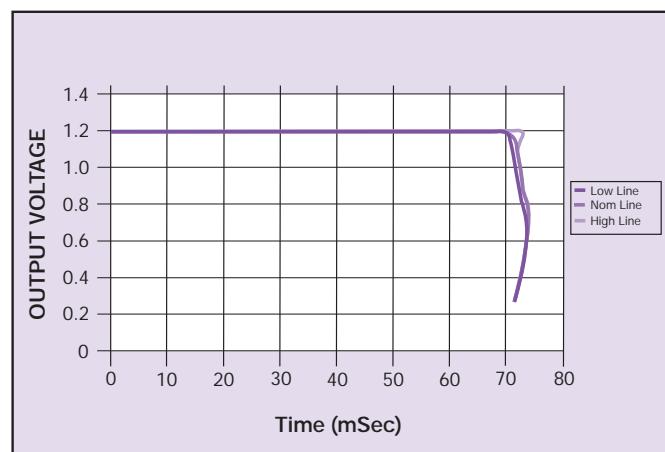


Figure 10: Output Voltage vs. Output Current

EXB250-48S1V5J Model

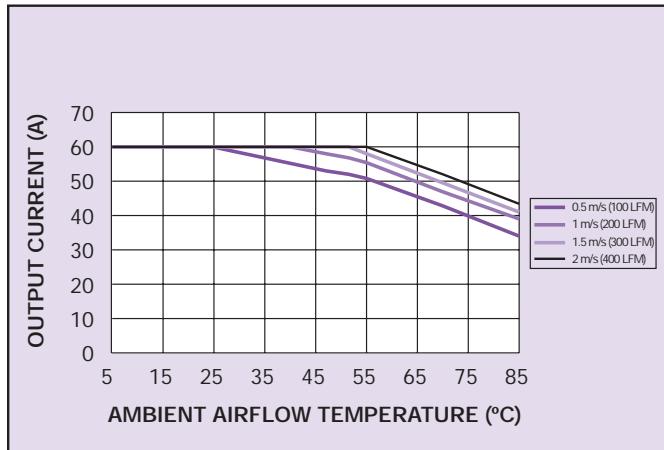


Figure 11: Derating Curve Output Current vs. Temperature

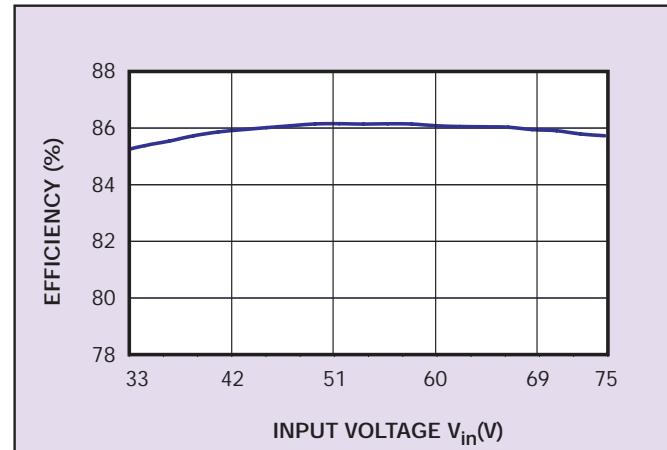


Figure 12: Efficiency vs. Line

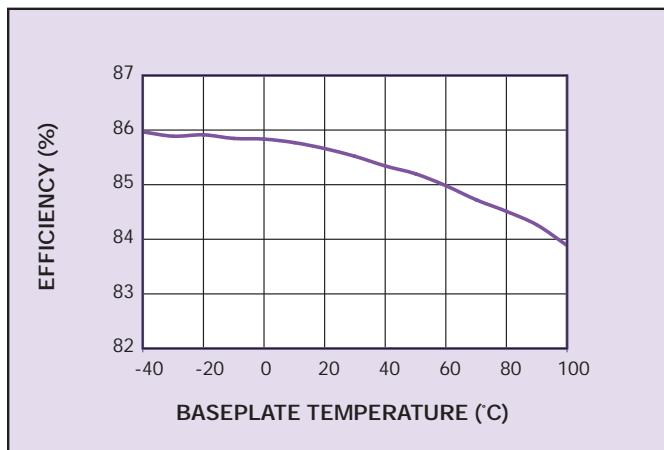


Figure 13: Typical Efficiency vs. Baseplate Temperature

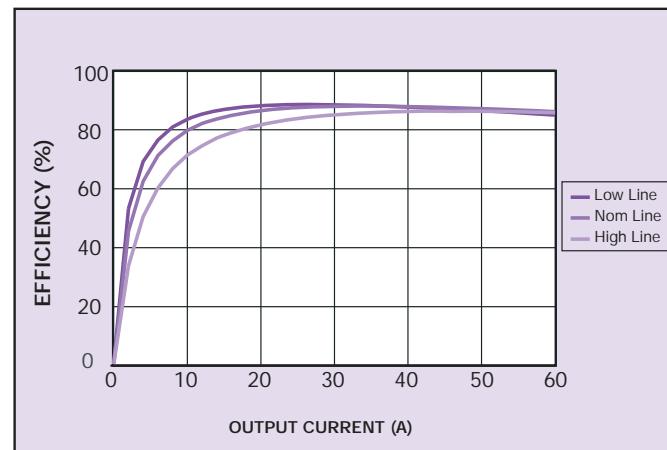


Figure 14: Efficiency vs. Load

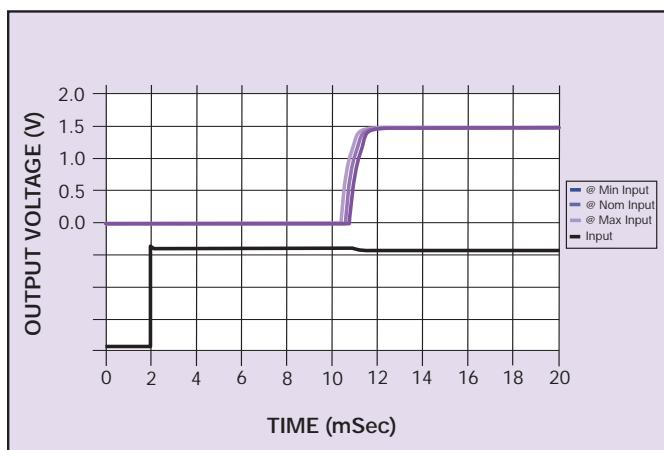


Figure 15: Turn-on Characteristic

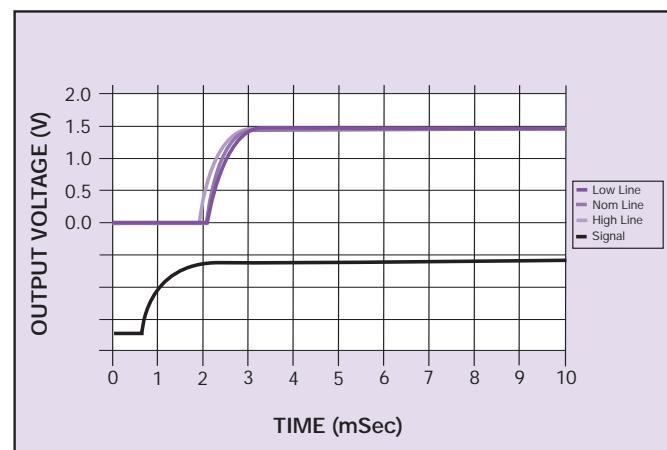


Figure 16: Control On/Off Characteristic

EXB250-48S1V5J Model



Figure 17: Typical Transient Response 75-50%
Step Load Change

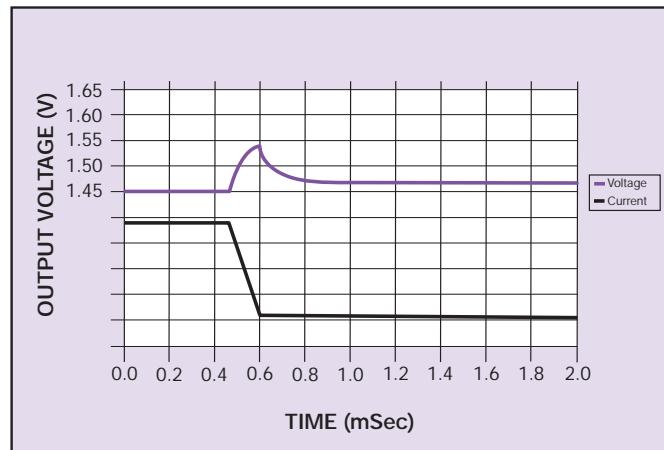


Figure 18: Typical Transient Response 50-75%
Step Load Change

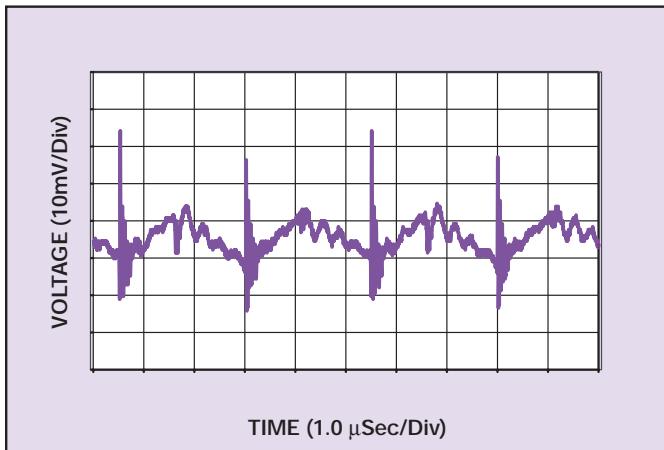


Figure 19: Output Ripple and Noise Measurement

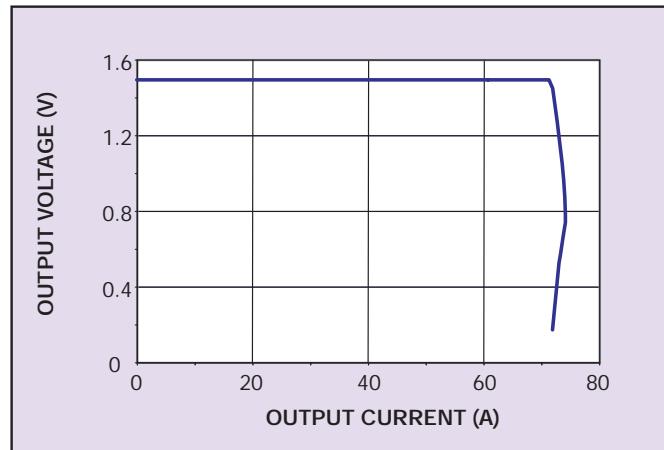


Figure 20: Output Voltage vs. Output Current

EXB250-48S1V8J Model

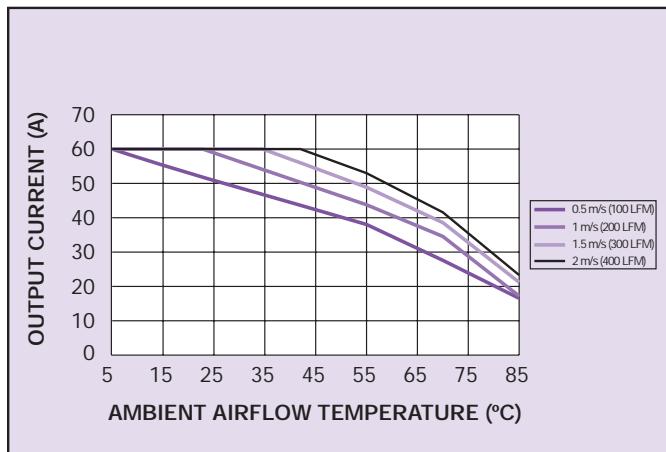


Figure 21: Derating Curve Output Current vs. Temperature

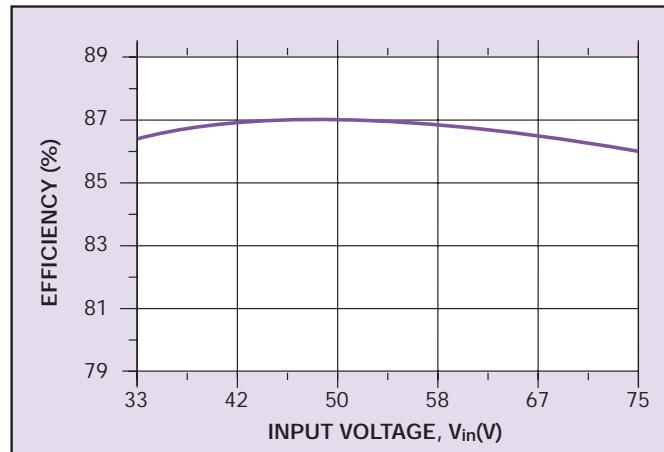


Figure 22: Efficiency vs. Line

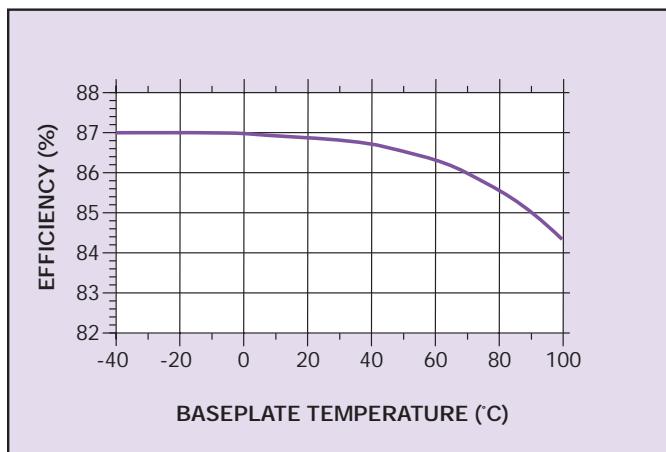


Figure 23: Typical Efficiency vs. Baseplate Temperature

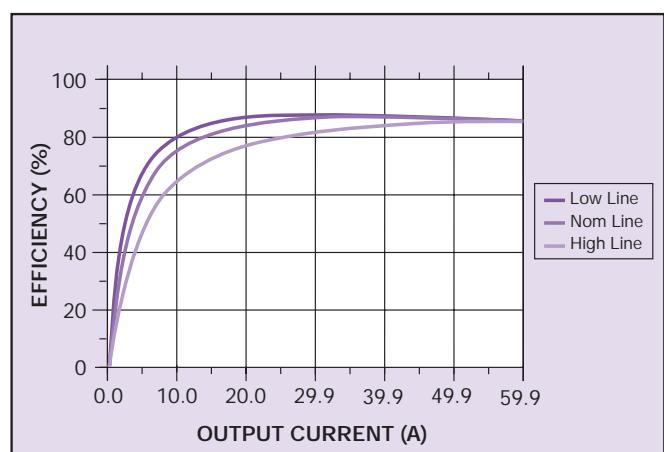


Figure 24: Efficiency vs. Load

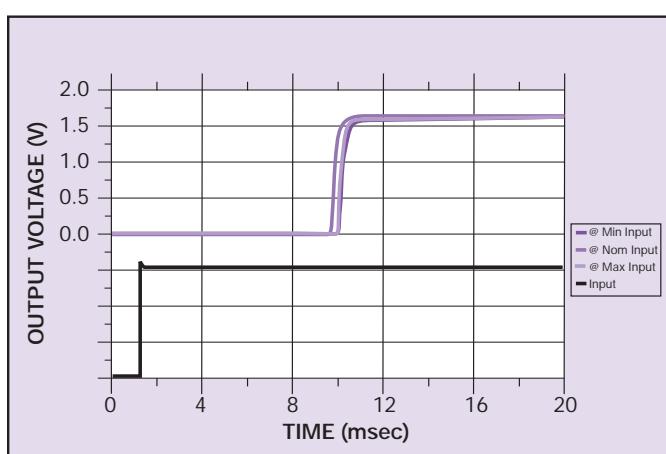


Figure 25: Turn-on Characteristic

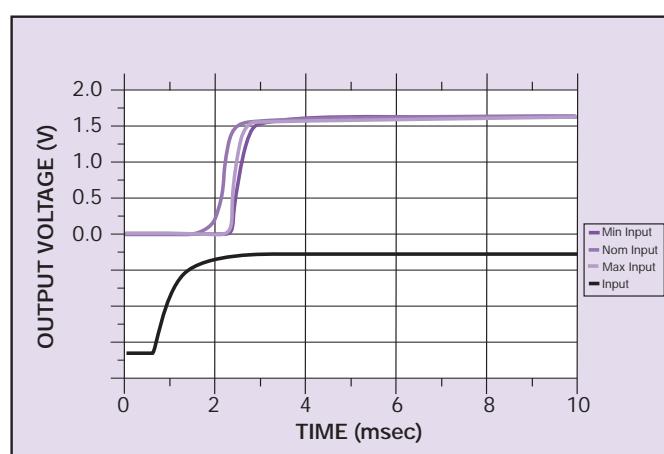


Figure 26: Control On/Off Characteristic

EXB250-48S1V8J Model

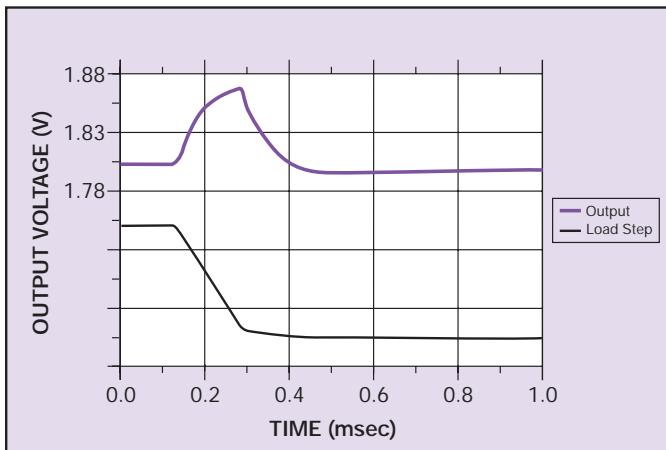


Figure 27: Typical Transient Response 75-50%
Step Load Change

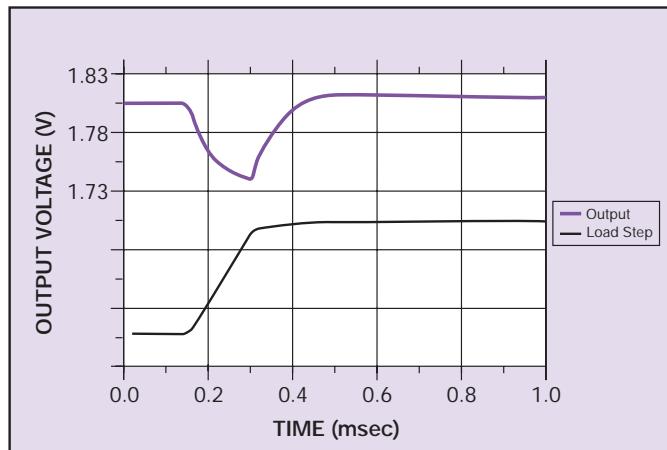


Figure 28: Typical Transient Response 50-75%
Step Load Change

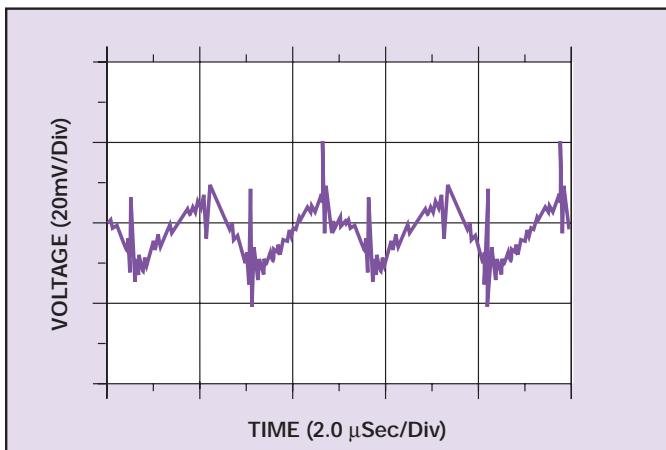


Figure 29: Output Ripple and Noise Measurement

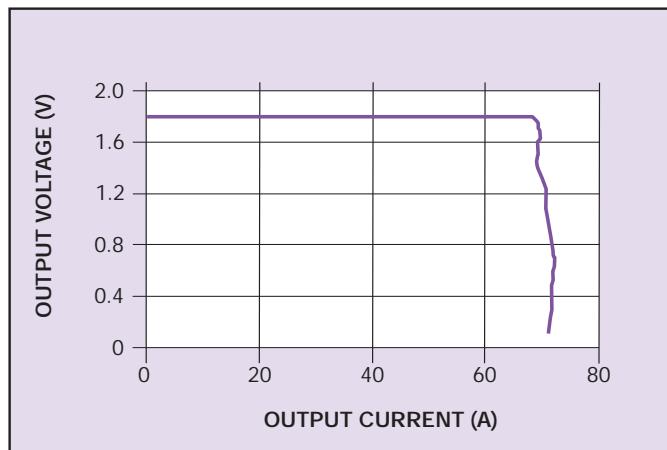


Figure 30: Output Voltage vs. Output Current

EXB250-48S2V5J Model

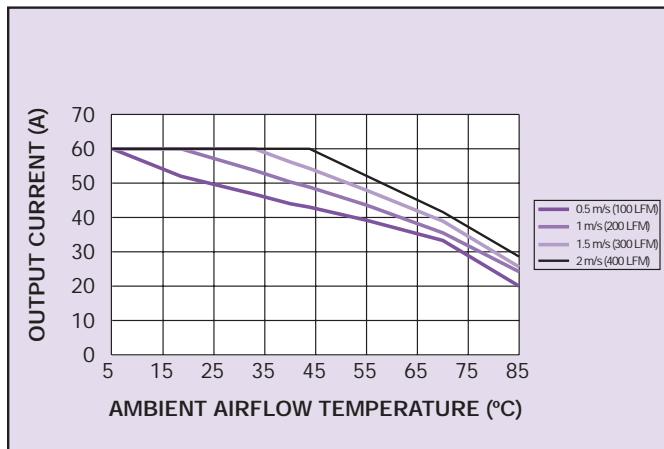


Figure 31: Derating Curve Output Current vs. Temperature

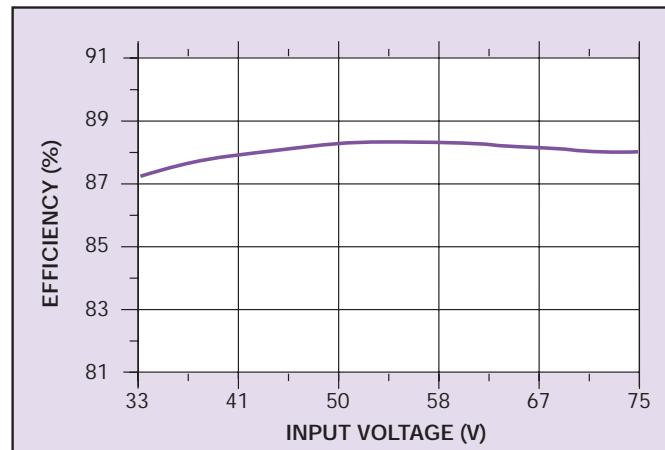


Figure 32: Efficiency vs. Line

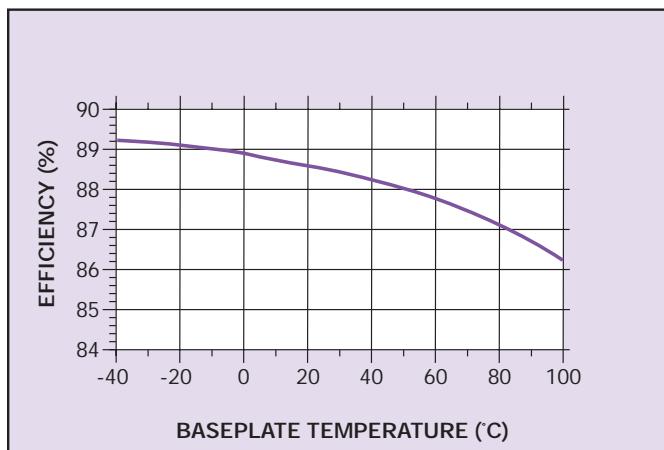


Figure 33: Typical Efficiency vs. Baseplate Temperature

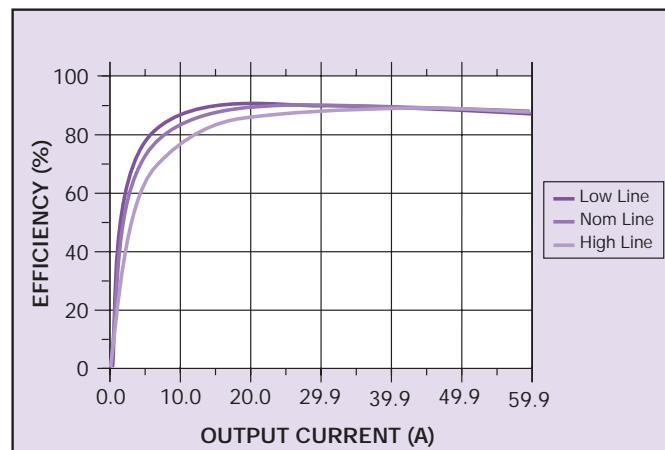


Figure 34: Efficiency vs. Load

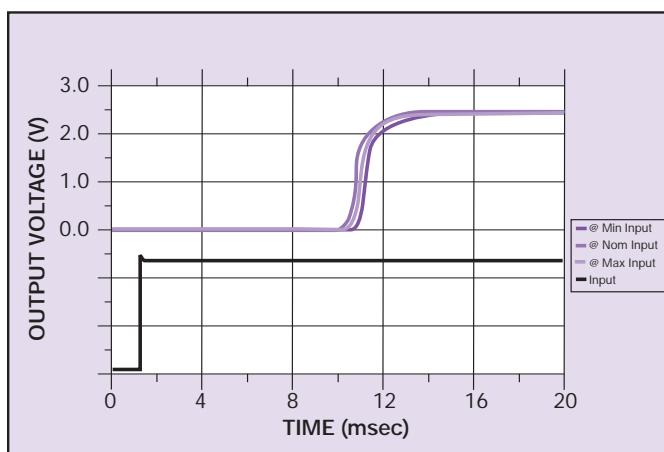


Figure 35: Turn-on Characteristic

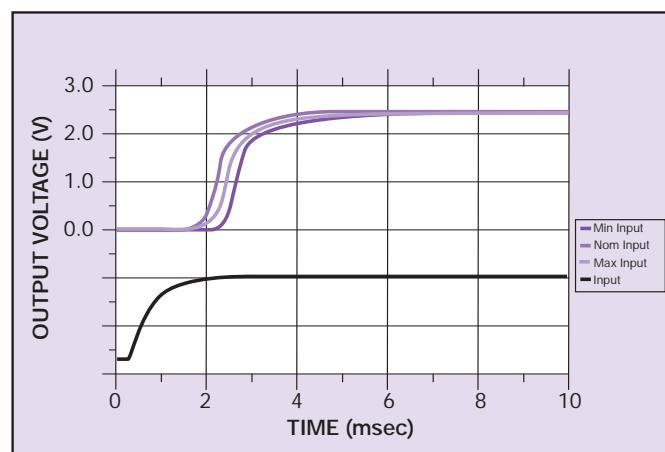


Figure 36: Control On/Off Characteristic

EXB250-48S2V5J Model

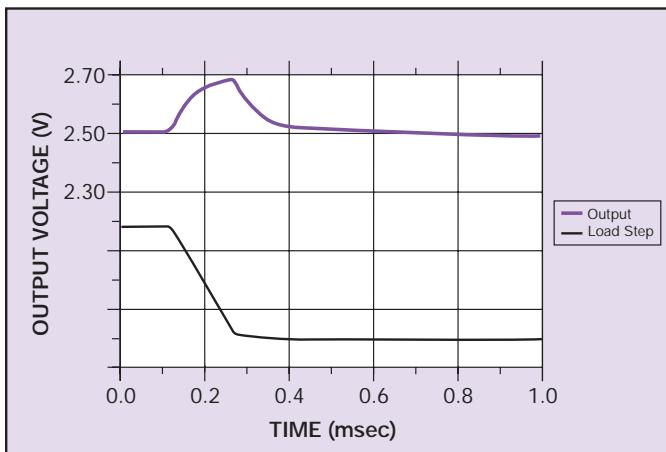


Figure 37: Typical Transient Response 75-50%
Step Load Change

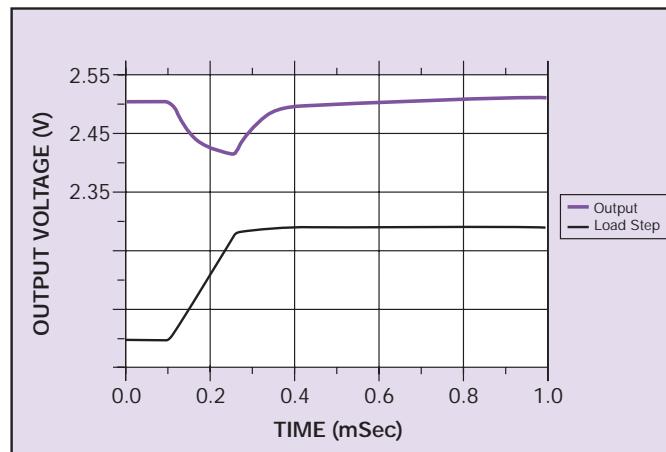


Figure 38: Typical Transient Response 50-75%
Step Load Change

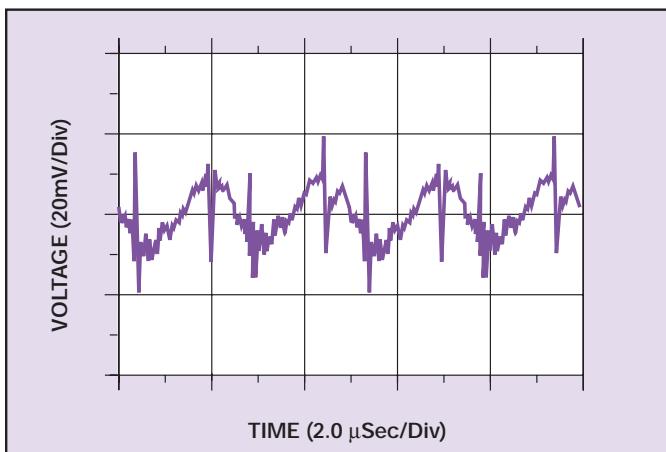


Figure 39: Output Ripple and Noise Measurement

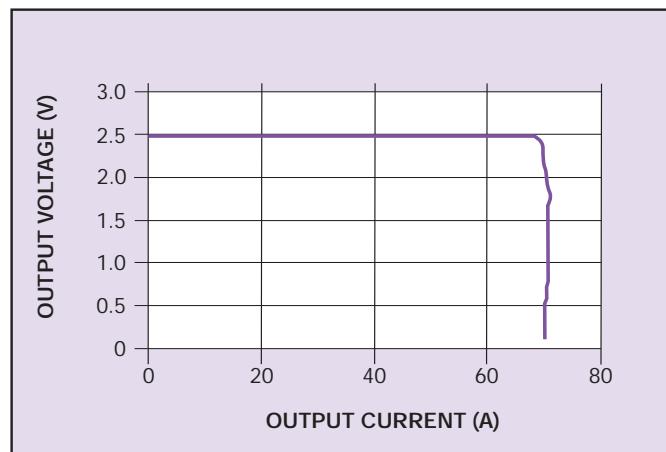


Figure 40: Output Voltage vs. Output Current

EXB250-48S3V3J Model

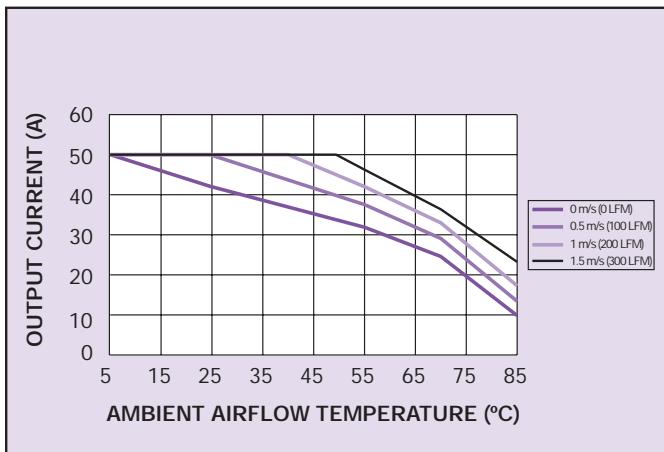


Figure 41: Derating Curve Output Current vs. Temperature

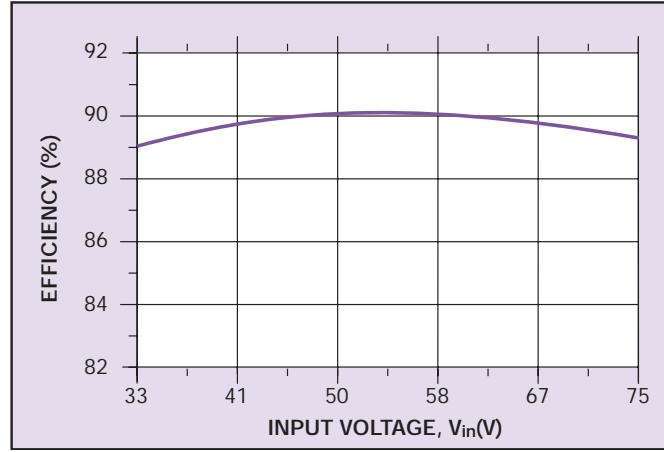


Figure 42: Efficiency vs. Line

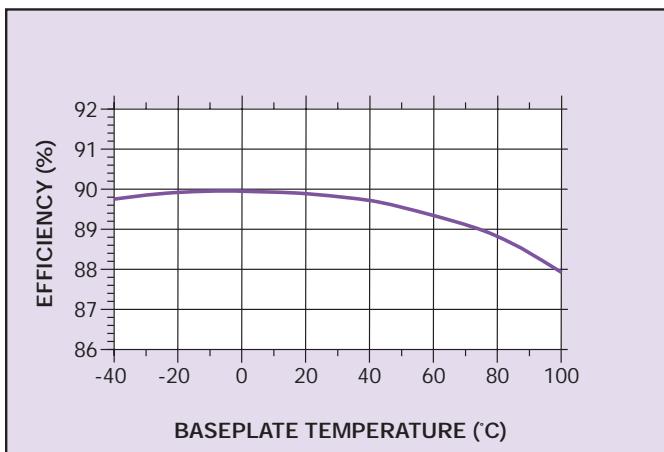


Figure 43: Typical Efficiency vs. Baseplate Temperature

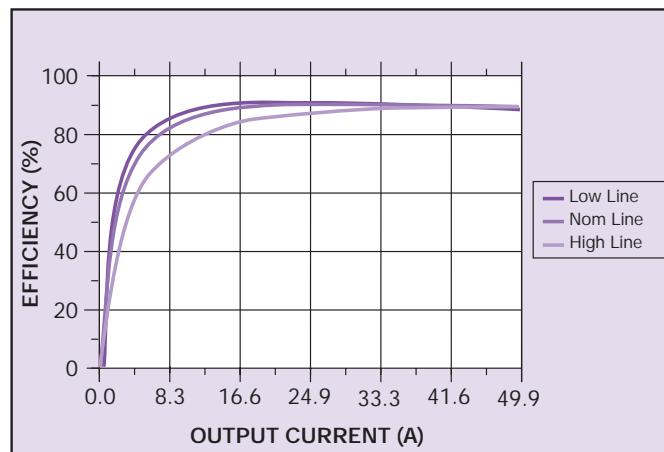


Figure 44: Efficiency vs. Load

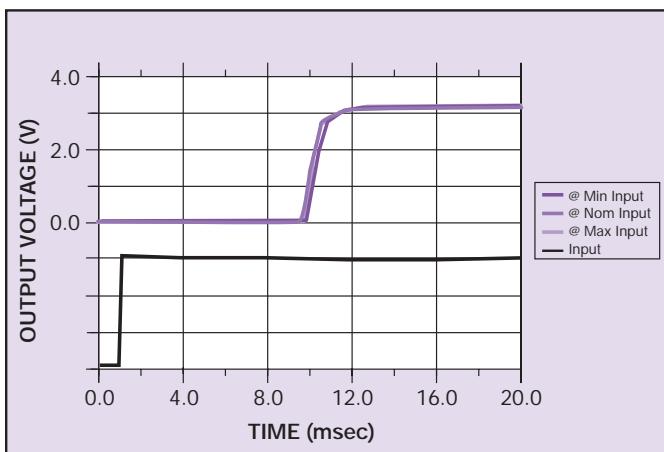


Figure 45: Turn-on Characteristic

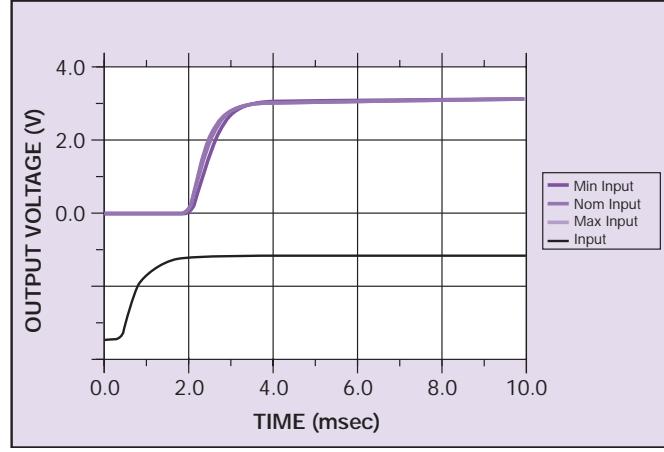


Figure 46: Control On/Off Characteristic

EXB250-48S3V3J Model

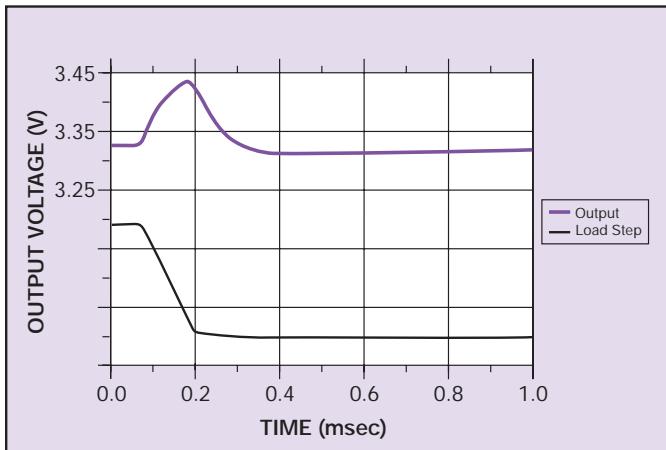


Figure 47: Typical Transient Response 75-50%
Step Load Change

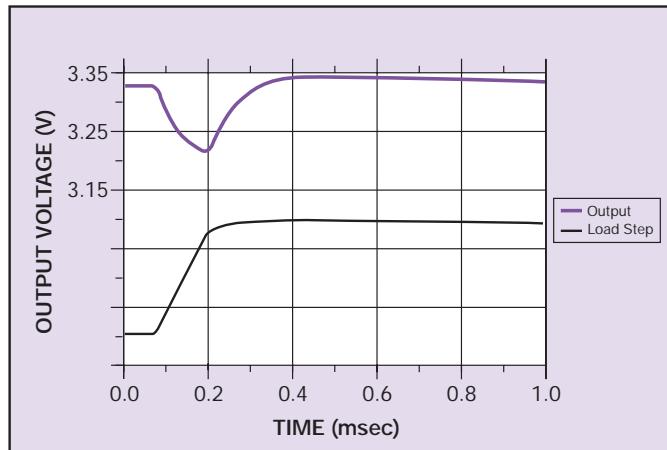


Figure 48: Typical Transient Response 50-75%
Step Load Change

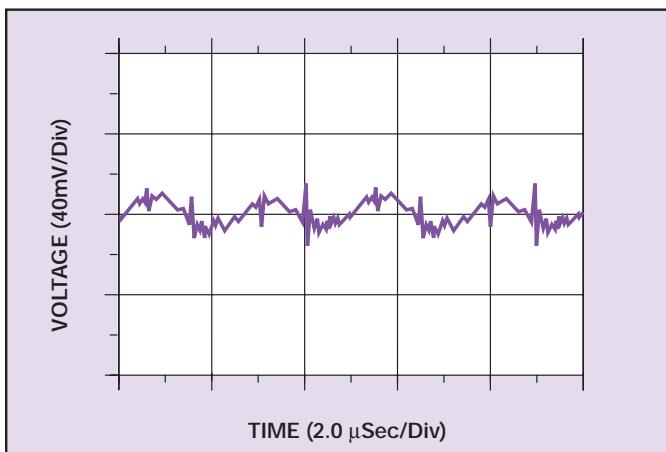


Figure 49: Output Ripple and Noise Measurement

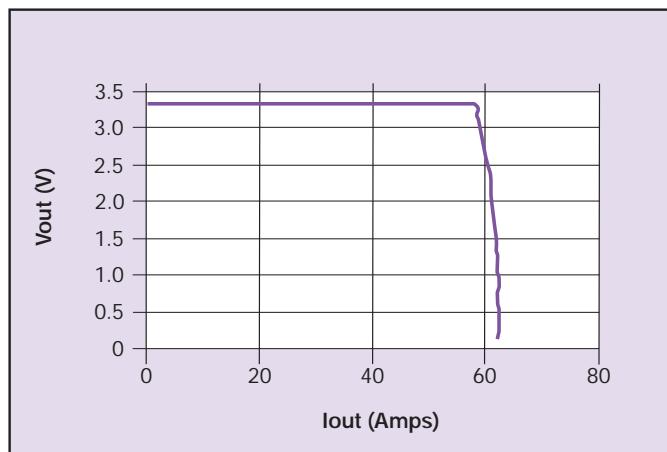


Figure 50: Output Voltage vs. Output Current

EXB250-48S05J Model

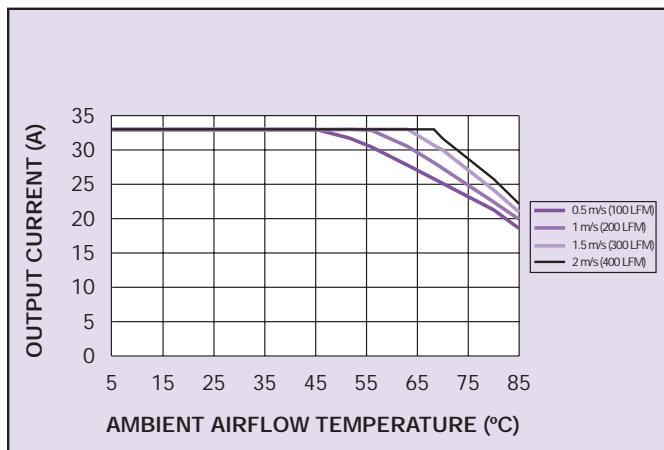


Figure 51: Derating Curve Output Current vs. Temperature

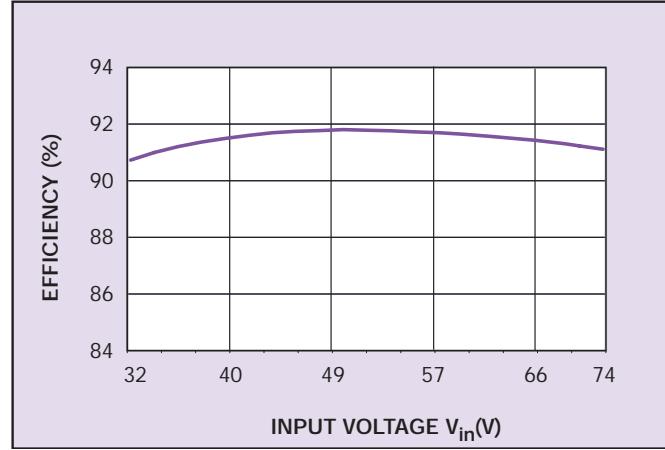


Figure 52: Efficiency vs. Line

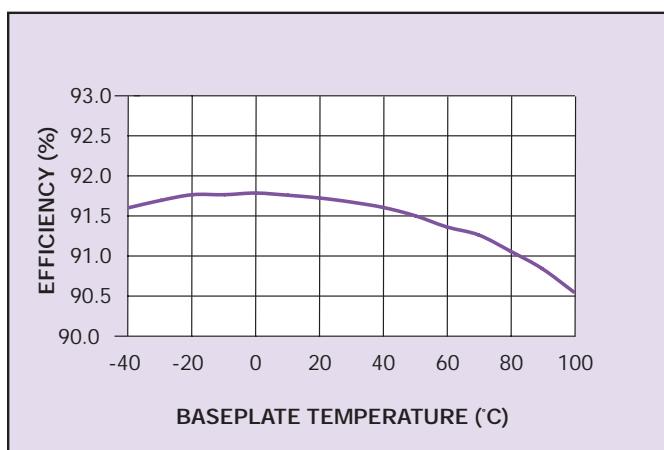


Figure 53: Typical Efficiency vs. Baseplate Temperature

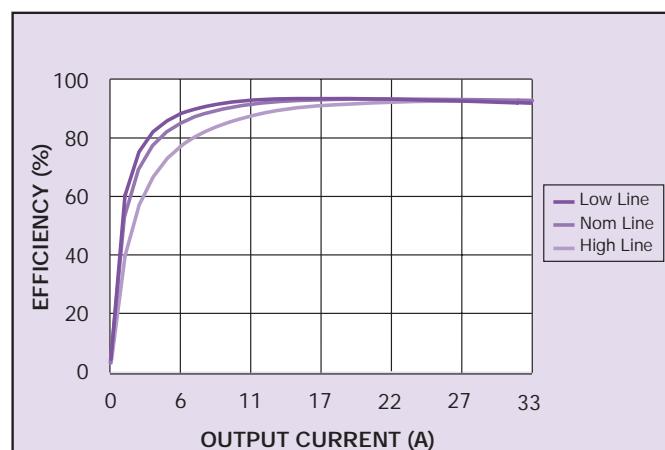


Figure 54: Efficiency vs. Load

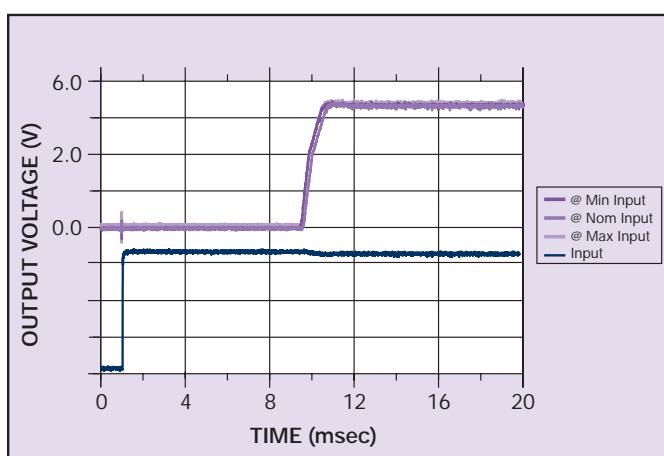


Figure 55: Turn-on Characteristic

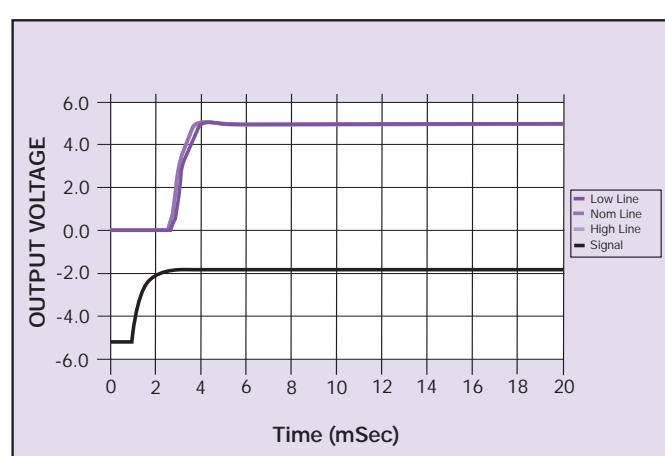


Figure 56: Control On/Off Characteristic

EXB250-48S05J Model

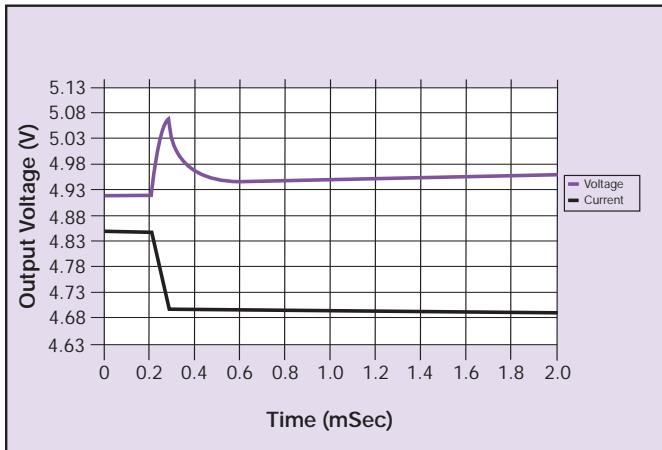


Figure 57: Typical Transient Response 75-50%
Step Load Change

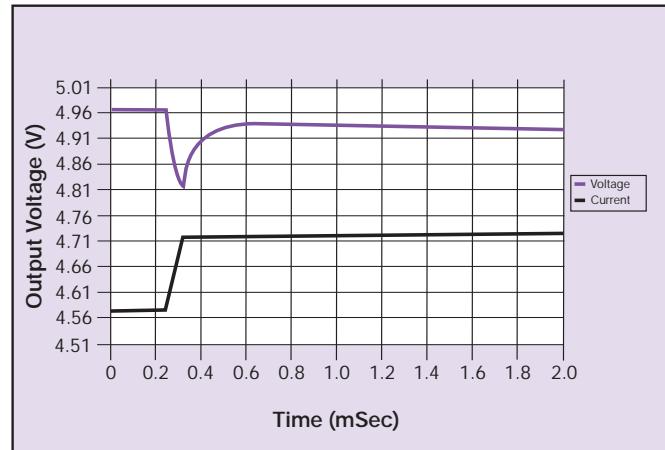


Figure 58: Typical Transient Response 50-75%
Step Load Change

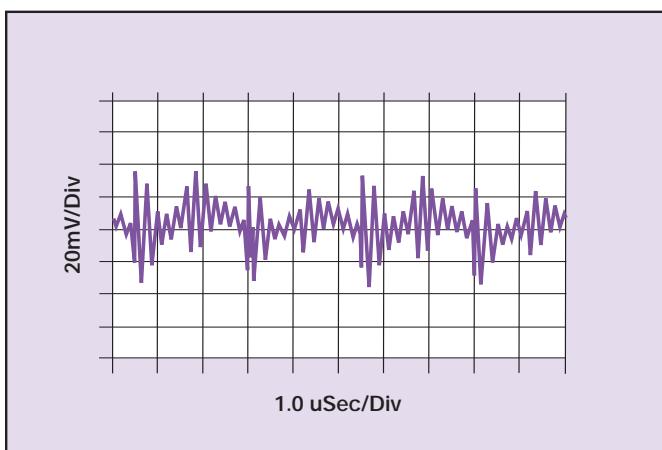


Figure 59: Output Ripple and Noise Measurement

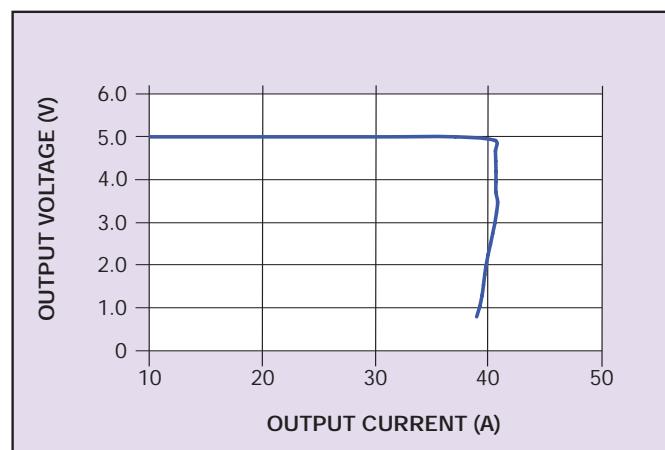


Figure 60: Output Voltage vs. Output Current

EXB250-48S12J Model

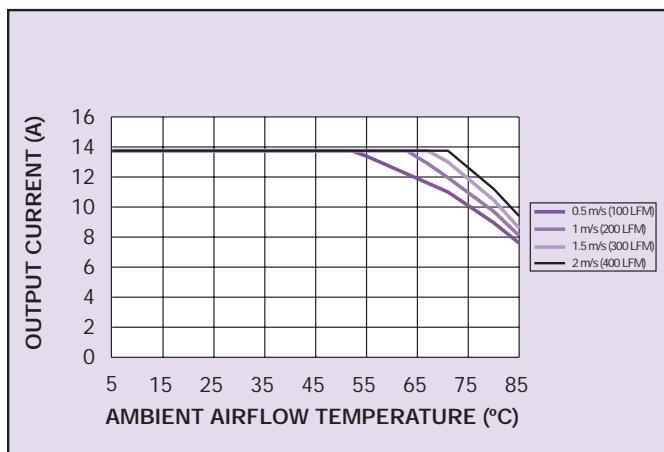


Figure 61: Derating Curve Output Current vs. Temperature

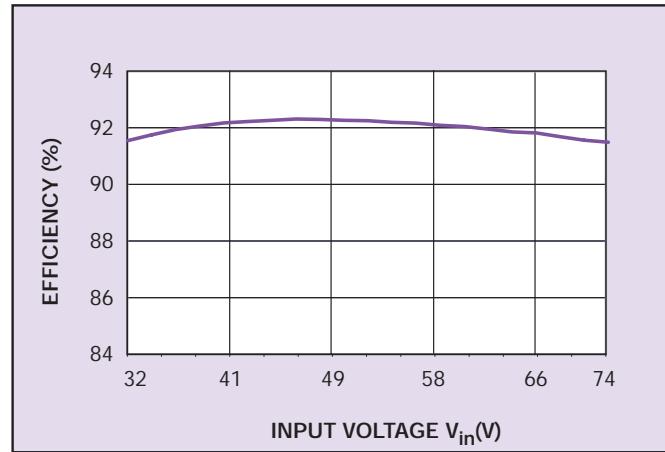


Figure 62: Efficiency vs. Line

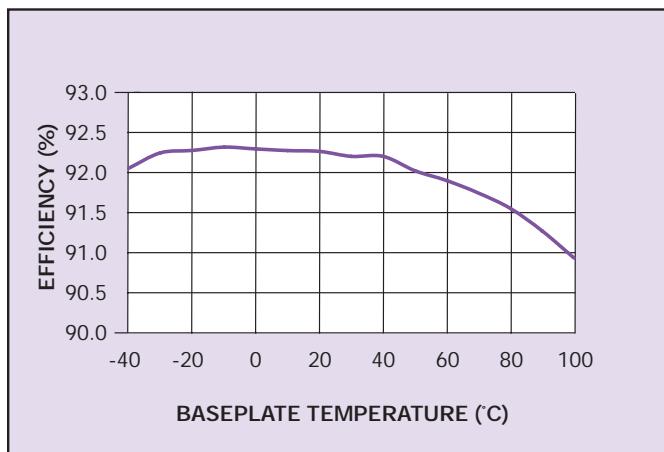


Figure 63: Typical Efficiency vs. Baseplate Temperature

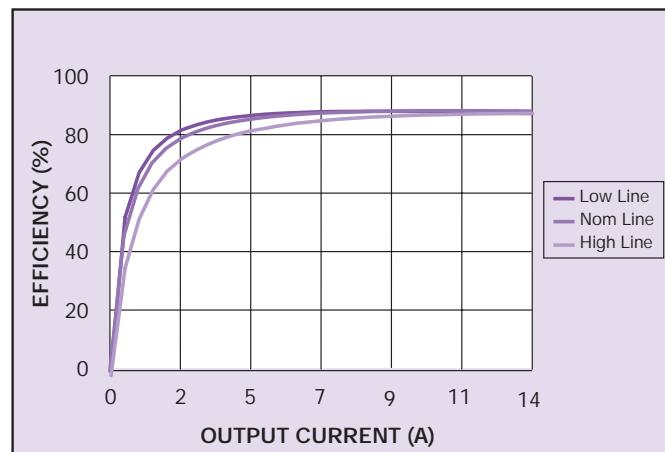


Figure 64: Efficiency vs. Load

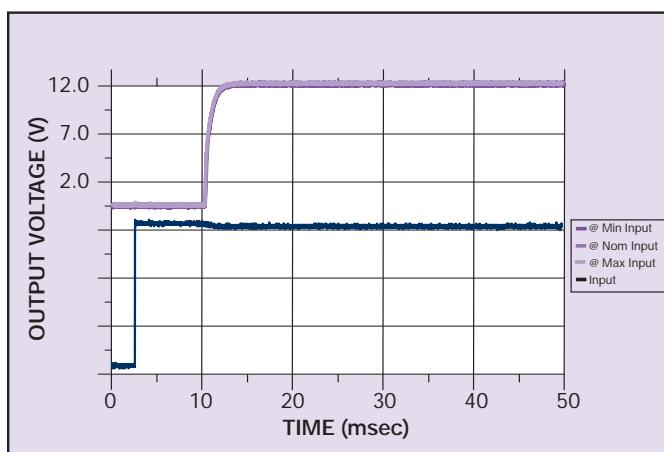


Figure 65: Turn-on Characteristic

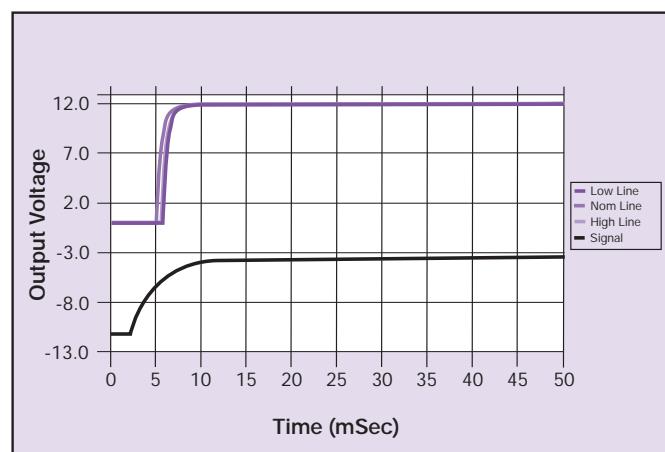


Figure 66: Control On/Off Characteristic

EXB250-48S12J Model

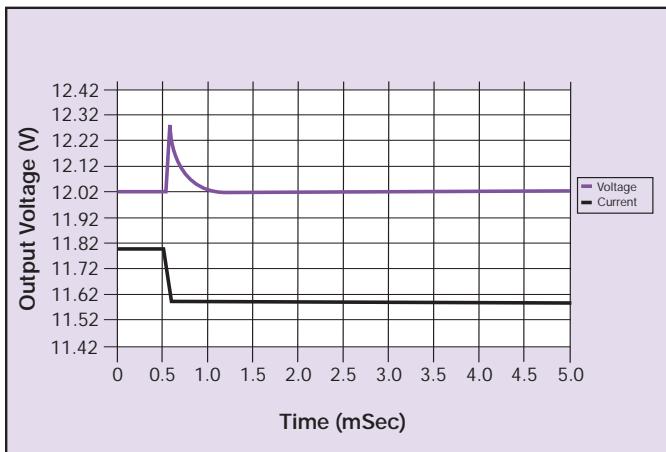


Figure 67: Typical Transient Response 75-50%
Step Load Change

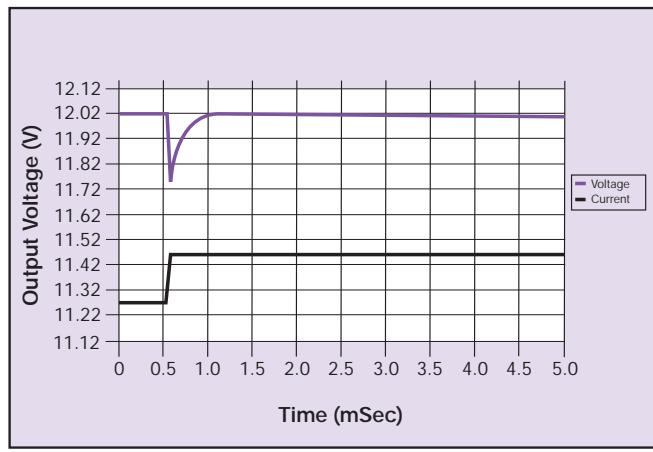


Figure 68: Typical Transient Response 50-75%
Step Load Change

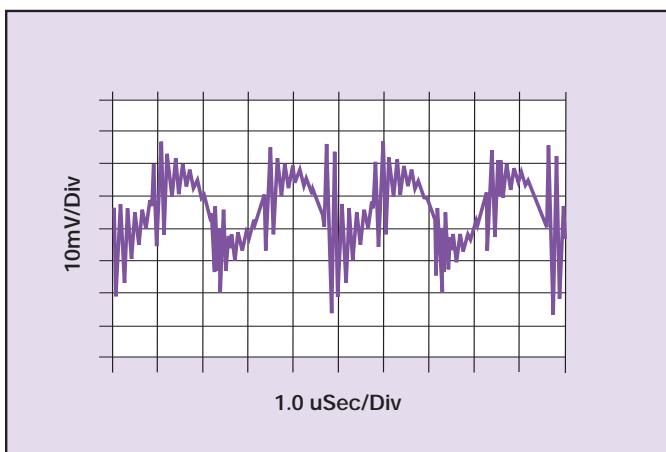


Figure 69: Typical Output Ripple and Noise Measurement

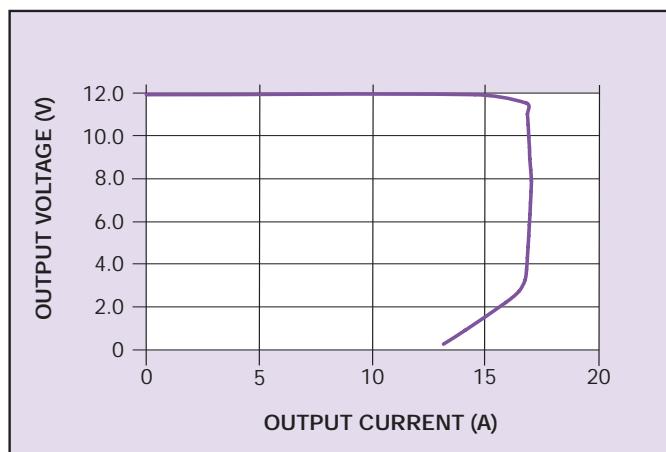
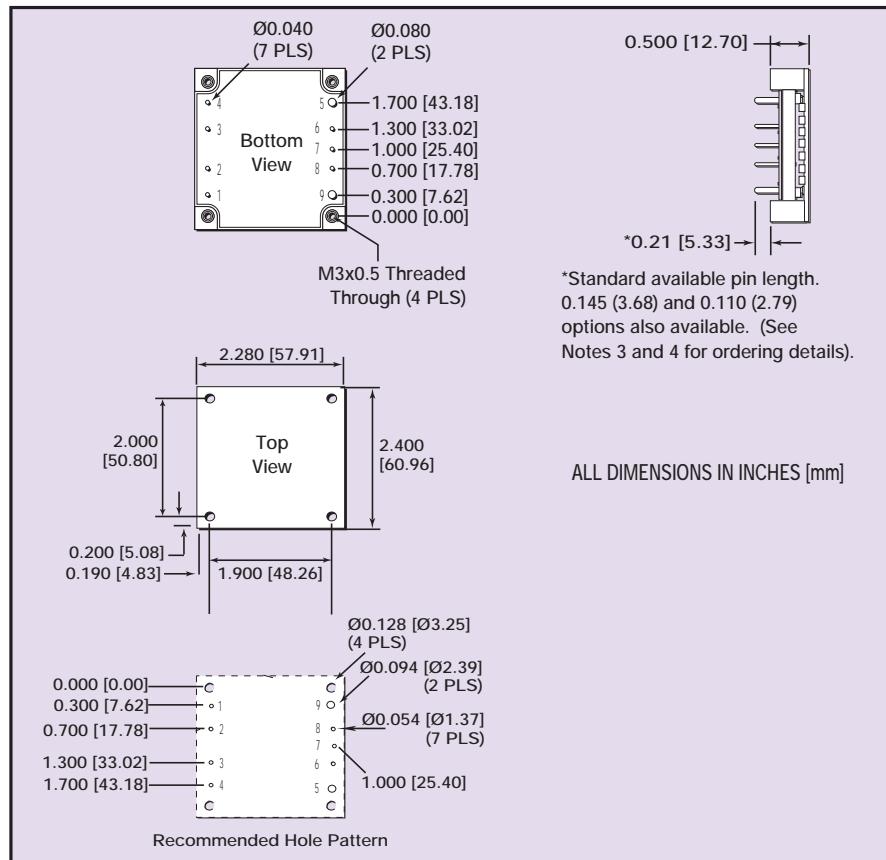


Figure 70: Current Limit Characteristic



Pin Connections

Pin No.	Function
1	+ Vin
2	Remote ON/OFF
3	No Function
4	- Vin
5	- Vout
6	- Sense
7	Trim
8	+ Sense
9	+ Vout

Figure 71: Dimensions and Pinout

EXB250 48V SERIES

Single Output

Embedded Power for
Business-Critical Continuity

Rev.10.11.07
exb250 48v series
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Notes

- 1 The control pin is referenced to Vin-.
- 2 Active low Remote ON/OFF is available. Standard product is active high. When ordering active low parts, designate with the Suffix '-R' e.g. EXB250-48S1V2J-RJ. See Application Note 119 for detailed information regarding ON/OFF control implementation.
- 3 When ordering 0.145" pin lengths designate with the Suffix '-N',
if the product is already a '-R' suffix product then the suffix will be '-RNJ'.
- 4 When ordering 0.110" pin lengths designate with the Suffix '-K',
if the product is already a '-R' suffix product then the suffix will be '-RKJ'.

CAUTION: Hazardous internal voltages and high temperatures. Ensure that unit is accessible only to trained personnel. The user must provide the recommended fusing in order to comply with safety approvals.

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