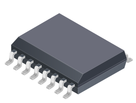
High Accuracy, Dual Fault, Galvanically Isolated Current Sensor in SOIC16 Wide-Body Package

FEATURES AND BENEFITS

- Differential current sensing cancels common mode fields, simplifying PCB layout
- Two user-settable faults for fast short-circuit protection and slower overcurrent detection
- Industry-leading noise performance with greatly improved bandwidth through proprietary amplifier and filter design techniques
- Patented integrated digital temperature compensation circuitry allows high accuracy over temperature in an open loop sensor
- 1.0 mΩ primary conductor resistance for low power loss and high inrush current-withstanding capability
- Small footprint, low-profile SOIC16 package suitable for space-constrained applications
- Integrated shield virtually eliminates capacitive coupling from current conductor to die due to high dV/dt voltage transients
- 5 V single supply operation with 0-3 V output swing
- Output voltage proportional to AC or DC current
- Factory-trimmed sensitivity and quiescent output voltage for improved accuracy
- 3600 Vrms Dielectric Strength certified under UL60950-1
- High PSRR for noisy environments

PACKAGE:

16-Pin SOICW (suffix LA)



Not to scale

CB Certificate Number: US-23711-A2-UL

DESCRIPTION

The ACS720 is a high accuracy Hall-effect-based current sensor IC with multiple programmable fault levels intended for industrial and consumer applications with a focus on motor control and power inverter stage applications.

One of the key benefits of the ACS720 is to provide high isolation with a reduced bill of materials made possible by the proprietary IC SOIC16W package. The ACS720 works off of a single 5 V supply while maintaining an output voltage swing from 0 to 3 V, with a stable zero current output of 1.5 V. This allows the ACS720 to operate off of a 5 V supply while having an output which is compatible with typical 3.3 V ADCs found on many MCUs. Furthermore, the ACS720's high PSRR rejects the noise often found on the supplies in the power section of the PCB or system, maintaining high accuracy in noisy environments.

The device has dual fault functions that are user configurable. Fast and slow fault output allow for short-circuit and overcurrent fault detection. A user-created resistor divider from the power supply of the ACS720 is used to set the fault level. The fault outputs are open drain, allowing the user to pull them up to a compatible voltage for the MCU. The open-drain outputs also allow for implementing a simple logical OR of multiple sensor fault outputs.

The ACS720 also integrates differential current sensing, which rejects external magnetic fields, greatly simplifying board layout in 3-phase motor applications.

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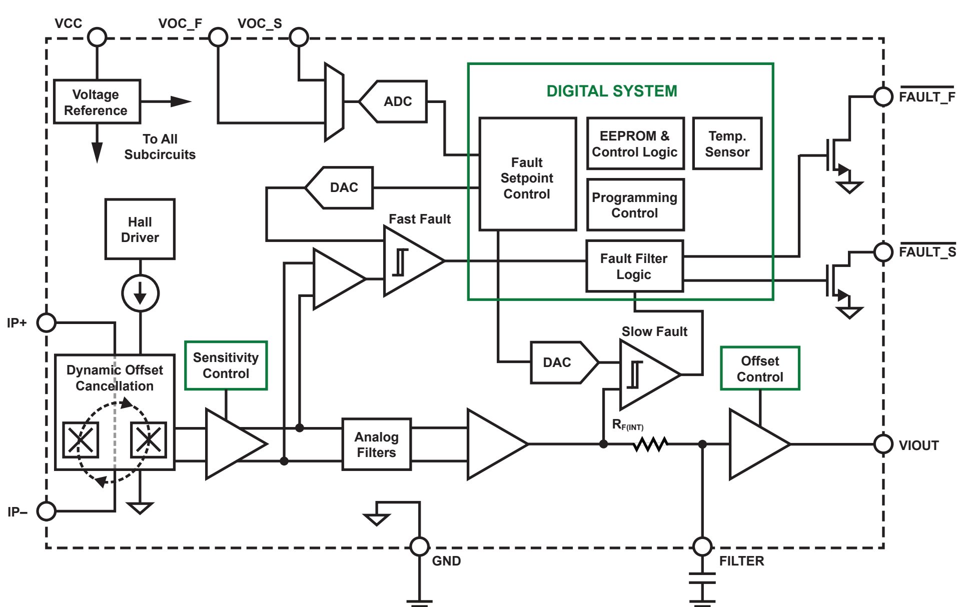

Figure 1: Functional Block Diagram

DESCRIPTION (continued)

Near closed-loop accuracy is achieved in this open-loop sensor due to Allegro's patented, digital temperature compensation, ultimately offering a smaller and more economical solution for many current sensing applications that traditionally rely on closed-loop core based sensors.

The ACS720 is provided in a small surface-mount SOIC16 package. The leadframe is plated with 100% matte tin, which is compatible with standard lead (Pb) free printed circuit board assembly processes. Internally, the device is Pb-free, except for flip-chip high-temperature Pb-based solder balls, currently exempt from RoHS. The device is fully calibrated prior to shipment from the factory.

SELECTION GUIDE

Part Number	Sensing Range, I_{PR} (A)	Sensitivity, Sens (Typ) (mV/A)	Fast Fault Mask Time (Typ) (µs)	T_A (°C)	Packing*
ACS720KLATR-15AB-T	±15	90	0	–40 to 125	Tape and Reel, 1000 pieces per reel
ACS720KLATR-15AB-T-4	±15	90	1.5		
ACS720KLATR-35AB-T	±35	38.5	0		
ACS720KLATR-35AB-T-4	±35	38.5	1.5		
ACS720KLATR-65AB-T	±65	20.5	0		
ACS720KLATR-65AB-T-4	±65	20.5	1.5		
ACS720KLATR-80AB-T	±80	16	0		
ACS720KLATR-80AB-T-4	±80	16	1.5		

*Contact Allegro for packing options.

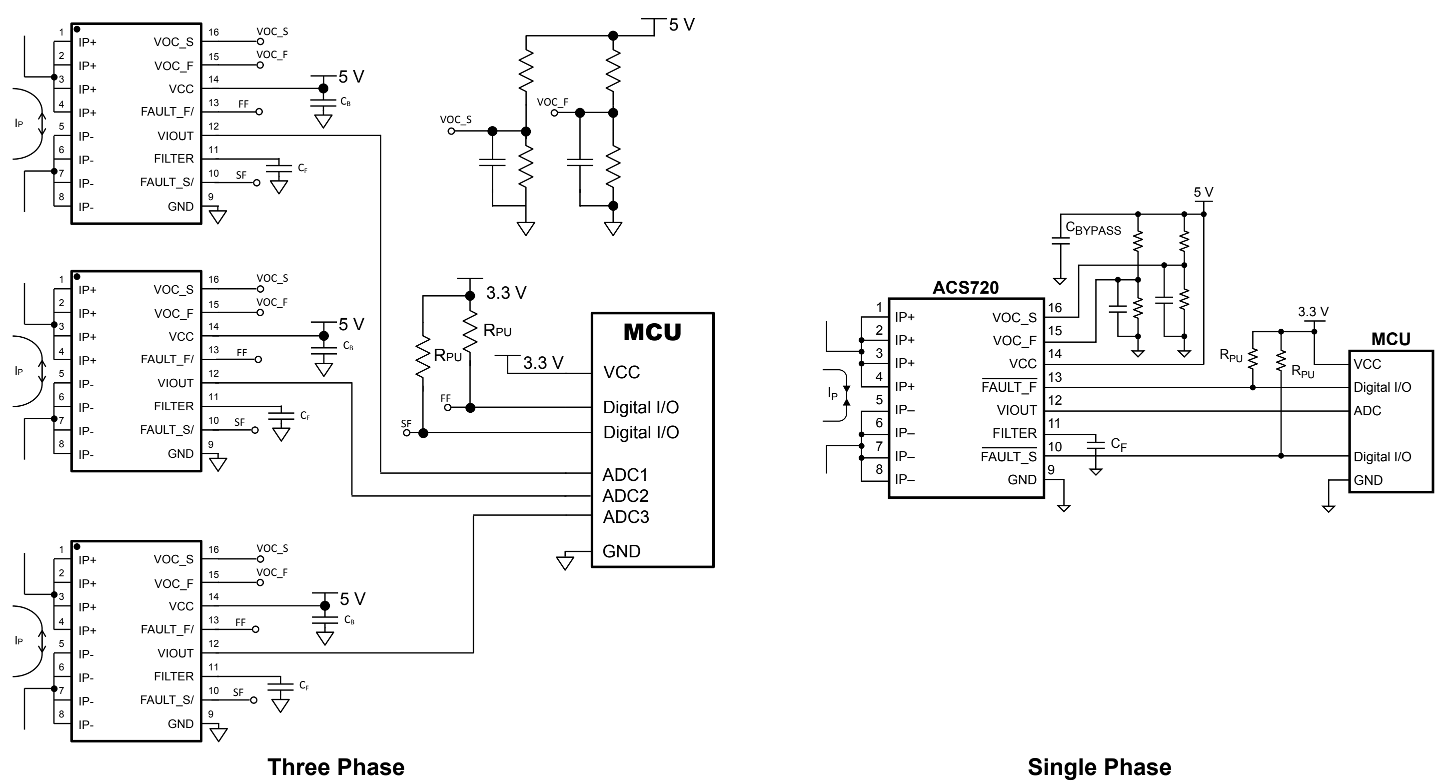

Figure 2: Typical Applications

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ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	Notes	Rating	Units
Supply Voltage	V_{CC}		6	V
Reverse Supply Voltage	V_{RCC}		–0.5	V
Filter Voltage	V_{FILTER}		25	V
Reverse Filter Voltage	$V_{RFILTER}$		–0.5	V
Output Voltages	V_{IOUT}, V_{FAULT_S}, V_{FAULT_F}		V_{CC} + 0.7	V
Reverse Output Voltage	V_{RIOUT}, V_{RFAULT_S}, V_{RFAULT_F}		–0.5	V
Input Pin Voltages	V_{OC_S}, V_{OC_F}		V_{CC} + 0.7	V
Reverse Input Pin Voltages	V_{ROC_S}, V_{ROC_F}		–0.5	V
Maximum Continuous Current	I_{CMAX}	T_A = 25°C	55	A
Operating Ambient Temperature	T_A	Range K	–40 to 125	°C
Junction Temperature	$T_{J(max)}$		165	°C
Storage Temperature	T_{stg}		–65 to 170	°C

ESD RATINGS

Characteristic	Symbol	Test Conditions	Value	Unit
Human Body Model	V_{HBM}	Per AEC-Q100	±7	kV
Charged Device Model	V_{CDM}	Per AEC-Q100	±1	kV

ISOLATION CHARACTERISTICS

Characteristic	Symbol	Notes	Value	Units
Dielectric Surge Strength Test Voltage	V_{SURGE}	Tested ±5 pulses at 2/minute in compliance to IEC 61000-4-5 1.2 µs (rise) / 50 µs (width).	10000	V
Dielectric Strength Test Voltage	V_{ISO}	Agency type-tested for 60 seconds per UL 60950-1 (edition 2). Production tested at 2250 V_{RMS} for 1 second in accordance with UL 60950-1.	3600	V_{RMS}
Working Voltage for Basic Isolation	V_{WVBI}	Maximum approved working voltage for basic (single) isolation according to UL 60950-1 (edition 2).	870	V_{PK} or V_{DC}
			616	V_{RMS}
Clearance	D_{cl}	Minimum distance through air from IP leads to signal leads.	7.5	mm
Creepage	D_{cr}	Minimum distance along package body from IP leads to signal leads.	7.5	mm

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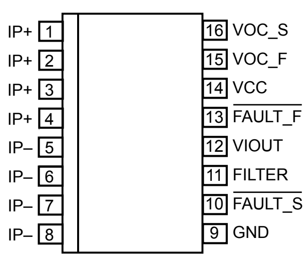
THERMAL CHARACTERISTICS [1]

Characteristic	Symbol	Test Conditions	Value	Unit
Junction-to-Ambient Thermal Resistance	$R_{\theta JA}$	Mounted on the Allegro ASEK732/3 evaluation board. Performance values include the power consumed by the PCB. [2]	17	°C/W
Junction-to-Lead Thermal Resistance	$R_{\theta JL}$	Mounted on the Allegro ASEK732/3 evaluation board. [2]	5	°C/W

[1] Refer to the die temperature curves versus DC current plot (p. 29). Additional thermal information is available on the Allegro website.
[2] The Allegro evaluation board has 1500 mm^2 of 2 oz. copper on each side, connected to pins 1 through 4 and pins 5 through 8, with thermal vias connecting the layers. Performance values include the power consumed by the PCB. Further details on the board are available from the Frequently Asked Questions document on our website. Further information about board design and thermal performance also can be found in the Applications Information section of this datasheet.

PINOUT DIAGRAM AND TERMINAL LIST TABLE



Package LA, 16-Pin SOICW Pinout Diagram

TERMINAL LIST TABLE

Number	Name	Description
1 through 4	IP+	Terminals for current being sensed; fused internally
5 through 8	IP–	Terminals for current being sensed; fused internally
9	GND	Signal ground terminal
10	$\overline{\text{FAULT\_S}}$	Open drain slow fault output (low true)
11	FILTER	Add capacitor to set output filter pole location
12	VIOUT	Analog output signal
13	$\overline{\text{FAULT\_F}}$	Open drain fast fault output (low true)
14	VCC	Device power supply terminal
15	VOC_F	Sets the trip current level for the fast fault
16	VOC_S	Sets the trip current level for the slow fault

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COMMON OPERATING CHARACTERISTICS [1]: Over full range of T_A, and V_{CC} = 5 V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ.	Max.	Units
ELECTRICAL CHARACTERISTICS						
Supply Voltage	V_{CC}		4.5	5.0	5.5	V
Supply Current	I_{CC}	V_{CC} = 5.0 V, output open	–	13	16	mA
Filter Resistance	$R_{F(INT)}$	T_A = 25°C	–	1.7	–	kΩ
Primary Conductor Resistance	R_{IP}	T_A = 25°C	–	1.0	–	mΩ
Power-On Time	t_{PO}	Time from when $V_{CC} > V_{CC(min)}$ to when the output reaches 90% of its steady-state level; T_A = 25°C	–	70	–	µs
Fault Power-On Time [2]	$t_{PO(FAULT)}$	Time from when $V_{CC} > V_{CC(min)}$ to when FAULT_S and FAULT_F will react to an overcurrent event	–	270	–	µs
OUTPUT SIGNAL CHARACTERISTICS						
Rise Time	t_R	T_A = 25°C, C_L = 1 nF, 1 V step on output	–	3	–	µs
Response Time	$t_{RESPONSE}$	T_A = 25°C, C_L = 1 nF, 1 V step on output	–	4	–	µs
Propagation Delay	t_{PD}	T_A = 25°C, C_L = 1 nF, 1 V step on output	–	1	–	µs
Internal Bandwidth	BW	Small signal –3 dB; C_L = 1 nF	–	120	–	kHz
Output Capacitance Load	C_L	VIOUT to GND	–	–	10	nF
Output Resistive Load	R_L	VIOUT to GND, VIOUT to VCC	10	–	–	kΩ
Output Source Current	$I_{OUT(SRC)}$	VIOUT shorted to GND	–	3	–	mA
Output Sink Current	$I_{OUT(SNK)}$	VIOUT shorted to VCC	–	30	–	mA
Saturation Voltage	V_{OL}	R_L = 10 kΩ (VIOUT to VCC)	–	–	150	mV
Clamp Voltage [4]	V_{CLAMP}		3.0	3.25	3.5	V
Noise Density	I_{ND}	Input-referenced noise density; T_A = 25°C, C_L = 4.7 nF	–	220	–	µA /√(Hz)
Noise	I_N	Input referenced noise at 120 kHz bandwidth; T_A = 25°C; C_L = 1 nF; C_F = 0 nF	–	100	–	mA_{rms}
		Input referenced noise at 20 kHz bandwidth; T_A = 25°C; C_L = 1 nF; C_F = 4.7 nF	–	31	–	mA_{rms}
Nonlinearity	E_{LIN}		–	±0.75	–	%
Power Supply Rejection Ratio	PSRR	DC to 1 kHz	–	40	–	dB
		1 kHz to 20 kHz	–	30	–	dB
Common Mode Field Rejection Ratio	CMFR	Magnetic field perpendicular to Hall plates	–	–45	–	dB

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COMMON OPERATING CHARACTERISTICS [1] (continued): Over full range of T_A, and V_{CC} = 5 V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ.	Max.	Units
FAULT CHARACTERISTICS						
Fault Clear Time	$t_{C(F)}$	Time from I_P falling below $I_{FAULT} – I_{HYS}$ to when V_{FAULT} is pulled above V_{FAULTL}; R_{PU} = 10 kΩ, 100 pF from FAULT to ground	–	6	–	µs
Fast Fault Hysteresis [3]	$I_{HYS(FF)}$		–	0.06 × I_{PR}	–	A
Slow Fault Hysteresis [3]	$I_{HYS(SF)}$		–	0.05 × I_{PR}	–	A
Fault Output Low Voltage	V_{FAULTL}	R_{PU} = 10 kΩ, under fault condition, FAULT_S and FAULT_F pins	–	–	0.4	V
Fault Pull-Up Resistance	R_{PU}		4.7	–	500	kΩ
Fast Fault Range	$I_{FAULT(F)}$	Absolute value of I_P	1.0 × I_{PR}	–	2.25 × I_{PR}	A
Slow Fault Range	$I_{FAULT(S)}$	Absolute value of I_P	0.5 × I_{PR}	–	1.25 × I_{PR}	A
VOC Input Range	V_{VOC}	VOC_S, VOC_F	0.3 × V_{CC}	–	0.7 × V_{CC}	V
High Impedance Pin Input Current	I_{IN}	VOC_S, VOC_F	–	100	–	nA
VOC Sample Rate	$f_{s(VOC)}$	VOC_S, VOC_F	–	62.5	–	kHz
VOC Update Rate	$f_{update(VOC)}$	8 samples averaged per update	–	7.8	–	kHz

[1] Device may be operated at higher primary current levels, I_P, ambient T_A, and internal leadframe temperature, provided that the Maximum Junction Temperature, T_J(max), is not exceeded.
[2] When V_{CC} < V_{CC} (min), the faults remain in the no-fault state.
[3] After the absolute value of I_P goes above $I_{FAULT(F)}$ or $I_{FAULT(S)}$, tripping the internal fault comparator, I_P must go below $I_{FAULT(F)} – I_{HYS(FF)}$ or $I_{FAULT(S)} – I_{HYS(SF)}$, before the internal fault comparator will reset.
[4] Clamp Voltage applies only to VIOUT pin.

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x15AB PERFORMANCE CHARACTERISTICS: Valid at T_A = –40°C to 125°C, V_{CC} = 5 V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. [1]	Max.	Unit
NOMINAL PERFORMANCE						
Optimized Accuracy Range	I_{PR}		–15	–	15	A
Sensitivity	Sens		–	90	–	mV/A
Zero-Current Output Voltage	$V_{IOUT(Q)}$	I_P = 0 A	–	1.5	–	V
Fast Fault Trip Level	$I_{FF(HIGH)}$	VOC_F = 0.7 × V_{CC}	–	33.8	–	A
	$I_{FF(LOW)}$	VOC_F = 0.54 × V_{CC}	–	26.3	–	A
Slow Fault Trip Level	$I_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}	–	18.8	–	A
	$I_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}	–	7.5	–	A
TOTAL OUTPUT ERROR COMPONENTS [2] $E_{TOT}(I_P) = \{[V_{IOUT\_ideal}(I_P) - V_{IOUT}(I_P)] / [Sens_{ideal}(I_P) \times I_P]\} \times 100$ (%)						
Total Output Error [3]	E_{TOT}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.8	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.6	4	%
Sensitivity Error	E_{SENS}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.6	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.6	4	%
Offset Voltage	V_{OE}	I_P = 0 A, T_A = 25°C to 125°C	–10	±4	10	mV
		I_P = 0 A, T_A = –40°C to 25°C	–30	±9	30	mV
OVERCURRENT FAULT PERFORMANCE						
Fast Fault Error	$E_{FF(HIGH)+}$	VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–15	±5	15	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±25	–	%
	$E_{FF(HIGH)-}$	VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C	–12	±6	12	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–30	±20	30	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±35	–	%
	$E_{FF(LOW)+}$	VOC_F = 0.54 × V_{CC}, Positive I_P, T_A = 25°C	–10	±7	10	%
		VOC_F = 0.54 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–15	±10	15	%
		VOC_F = 0.54 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±25	–	%
	$E_{FF(LOW)-}$	VOC_F = 0.54 × V_{CC}, Negative I_P, T_A = 25°C	–15	±7	15	%
		VOC_F = 0.54 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–40	±25	40	%
		VOC_F = 0.54 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±45	–	%
Slow Fault Error	$E_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}, I_P rising	–6	±3	6	%
	$E_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}, I_P rising	–12	±5	12	%
Fast Fault Delay Code	–			0		–
Slow Fault Delay Code	–			4		–
FAULT CHARACTERISTICS						
Fast Fault Response Time	$t_{R(FF)}$	Time from I_P rising above I_{FF} until $V_{FAULT\_F} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(FAST)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_F to ground	–	1.5	2	µs
Slow Fault Response Time	$t_{R(SF)}$	Time from I_P rising above I_{SF} until $V_{FAULT\_S} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(SLOW)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_S to ground	–	13	–	µs

[1] Typical values with +/- are 3 sigma values.
[2] A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage, as that would violate the maximum/minimum total output error specification. Also, 3 sigma distribution values are combined by taking the square root of the sum of the squares.
[3] Percentage of I_P, with $I_P = I_{PR(max)}$.

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x15AB-4 PERFORMANCE CHARACTERISTICS: Valid at T_A = –40°C to 125°C, V_{CC} = 5 V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. [1]	Max.	Unit
NOMINAL PERFORMANCE						
Optimized Accuracy Range	I_{PR}		–15	–	15	A
Sensitivity	Sens		–	90	–	mV/A
Zero-Current Output Voltage	$V_{IOUT(Q)}$	I_P = 0 A	–	1.5	–	V
Fast Fault Trip Level	$I_{FF(HIGH)}$	VOC_F = 0.7 × V_{CC}	–	33.8	–	A
	$I_{FF(LOW)}$	VOC_F = 0.54 × V_{CC}	–	26.3	–	A
Slow Fault Trip Level	$I_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}	–	18.8	–	A
	$I_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}	–	7.5	–	A
TOTAL OUTPUT ERROR COMPONENTS [2] $E_{TOT}(I_P) = \{[V_{IOUT\_ideal}(I_P) – V_{IOUT}(I_P)] / [Sens_{ideal}(I_P) \times I_P]\} \times 100$ (%)						
Total Output Error [3]	E_{TOT}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.8	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.6	4	%
Sensitivity Error	E_{SENS}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.6	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.6	4	%
Offset Voltage	V_{OE}	I_P = 0 A, T_A = 25°C to 125°C	–10	±4	10	mV
		I_P = 0 A, T_A = –40°C to 25°C	–30	±9	30	mV
OVERCURRENT FAULT PERFORMANCE						
Fast Fault Error	$E_{FF(HIGH)+}$	VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–15	±5	15	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±25	–	%
	$E_{FF(HIGH)–}$	VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C	–12	±6	12	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–30	±20	30	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±35	–	%
	$E_{FF(LOW)+}$	VOC_F = 0.54 × V_{CC}, Positive I_P, T_A = 25°C	–10	±7	10	%
		VOC_F = 0.54 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–15	±10	15	%
		VOC_F = 0.54 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±25	–	%
	$E_{FF(LOW)-}$	VOC_F = 0.54 × V_{CC}, Negative I_P, T_A = 25°C	–15	±7	15	%
		VOC_F = 0.54 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–40	±25	40	%
		VOC_F = 0.54 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±45	–	%
Slow Fault Error	$E_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}, I_P rising	–6	±3	6	%
	$E_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}, I_P rising	–12	±5	12	%
Fast Fault Delay Code	–			4		–
Fast Fault Mask Time	$t_{m(ff)}$		–	1.5	–	µs
Slow Fault Delay Code	–			4		–
FAULT CHARACTERISTICS						
Fast Fault Response Time	$t_{R(FF)}$	Time from I_P rising above I_{FF} until $V_{FAULT\_F} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(FAST)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_F to ground	–	3.5	–	µs
Slow Fault Response Time	$t_{R(SF)}$	Time from I_P rising above I_{SF} until $V_{FAULT\_S} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(SLOW)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_S to ground	–	13	–	µs

[1] Typical values with +/- are 3 sigma values.
[2] A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage, as that would violate the maximum/minimum total output error specification. Also, 3 sigma distribution values are combined by taking the square root of the sum of the squares.
[3] Percentage of I_P, with $I_P = I_{PR(max)}$.

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Characteristic	Symbol	Test Conditions	Min.	Typ. [1]	Max.	Unit
NOMINAL PERFORMANCE						
Optimized Accuracy Range	I_{PR}		–35	–	35	A
Sensitivity	Sens		–	38.5	–	mV/A
Zero-Current Output Voltage	$V_{IOUT(Q)}$	I_P = 0 A	–	1.5	–	V
Fast Fault Trip Level	$I_{FF(HIGH)}$	VOC_F = 0.7 × V_{CC}	–	78.8	–	A
	$I_{FF(LOW)}$	VOC_F = 0.38 × V_{CC}	–	43.8	–	A
Slow Fault Trip Level	$I_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}	–	43.8	–	A
	$I_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}	–	17.5	–	A
TOTAL OUTPUT ERROR COMPONENTS [2] $E_{TOT}(I_P) = \{[V_{IOUT\_ideal}(I_P) – V_{IOUT}(I_P)] / [Sens_{ideal}(I_P) \times I_P]\} \times 100$ (%)						
Total Output Error [3]	E_{TOT}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.6	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.5	4	%
Sensitivity Error	E_{SENS}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.6	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.5	4	%
Offset Voltage	V_{OE}	I_P = 0 A, T_A = 25°C to 125°C	–10	±3.5	10	mV
		I_P = 0 A, T_A = –40°C to 25°C	–30	±9	30	mV
OVERCURRENT FAULT PERFORMANCE						
Fast Fault Error	$E_{FF(HIGH)+}$	VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±15	–	%
	$E_{FF(HIGH)–}$	VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–15	±10	15	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±20	–	%
	$E_{FF(LOW)+}$	VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = 25°C	–20	±12	20	%
		VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–20	±12	20	%
		VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±25	–	%
	$E_{FF(LOW)-}$	VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = 25°C	–20	±12	20	%
		VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–30	±18	30	%
		VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±32	–	%
Slow Fault Error	$E_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}, I_P rising	–6	±3	6	%
	$E_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}, I_P rising	–10	±5	10	%
Fast Fault Delay Code	–			0		–
Slow Fault Delay Code	–			4		–
FAULT CHARACTERISTICS						
Fast Fault Response Time	$t_{R(FF)}$	Time from I_P rising above I_{FF} until $V_{FAULT\_F} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(FAST)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_F to ground	–	1.5	2	µs
Slow Fault Response Time	$t_{R(SF)}$	Time from I_P rising above I_{SF} until $V_{FAULT\_S} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(SLOW)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_S to ground	–	13	–	µs

[1] Typical values with +/- are 3 sigma values.
[2] A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage, as that would violate the maximum/minimum total output error specification. Also, 3 sigma distribution values are combined by taking the square root of the sum of the squares.
[3] Percentage of I_P, with $I_P = I_{PR(max)}$.

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x35AB-4 PERFORMANCE CHARACTERISTICS: Valid at T_A = –40°C to 125°C, V_{CC} = 5 V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. [1]	Max.	Unit
NOMINAL PERFORMANCE						
Optimized Accuracy Range	I_{PR}		–35	–	35	A
Sensitivity	Sens		–	38.5	–	mV/A
Zero-Current Output Voltage	$V_{IOUT(Q)}$	I_P = 0 A	–	1.5	–	V
Fast Fault Trip Level	$I_{FF(HIGH)}$	VOC_F = 0.7 × V_{CC}	–	78.8	–	A
	$I_{FF(LOW)}$	VOC_F = 0.38 × V_{CC}	–	43.8	–	A
Slow Fault Trip Level	$I_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}	–	43.8	–	A
	$I_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}	–	17.5	–	A
TOTAL OUTPUT ERROR COMPONENTS [2] $E_{TOT}(I_P) = \{[V_{IOUT\_ideal}(I_P) - V_{IOUT}(I_P)] / [Sens_{ideal}(I_P) \times I_P]\} \times 100$ (%)						
Total Output Error [3]	E_{TOT}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.6	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.5	4	%
Sensitivity Error	E_{SENS}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.6	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.5	4	%
Offset Voltage	V_{OE}	I_P = 0 A, T_A = 25°C to 125°C	–10	±3.5	10	mV
		I_P = 0 A, T_A = –40°C to 25°C	–30	±9	30	mV
OVERCURRENT FAULT PERFORMANCE						
Fast Fault Error	$E_{FF(HIGH)+}$	VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±15	–	%
	$E_{FF(HIGH)–}$	VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–15	±10	15	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±20	–	%
	$E_{FF(LOW)+}$	VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = 25°C	–20	±12	20	%
		VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–20	±12	20	%
		VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±25	–	%
	$E_{FF(LOW)-}$	VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = 25°C	–20	±12	20	%
		VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–30	±18	30	%
		VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±32	–	%
Slow Fault Error	$E_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}, I_P rising	–6	±3	6	%
	$E_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}, I_P rising	–10	±5	10	%
Fast Fault Delay Code	–			4		–
Fast Fault Mask Time	$t_{m(ff)}$		–	1.5	–	μs
Slow Fault Delay Code	–			4		–
FAULT CHARACTERISTICS						
Fast Fault Response Time	$t_{R(FF)}$	Time from I_P rising above I_{FF} until $V_{FAULT\_F} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(FAST)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_F to ground	–	3.5	–	μs
Slow Fault Response Time	$t_{R(SF)}$	Time from I_P rising above I_{SF} until $V_{FAULT\_S} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(SLOW)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_S to ground	–	13	–	μs

[1] Typical values with +/- are 3 sigma values.
[2] A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage, as that would violate the maximum/minimum total output error specification. Also, 3 sigma distribution values are combined by taking the square root of the sum of the squares.
[3] Percentage of I_P, with $I_P = I_{PR(max)}$.

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Characteristic	Symbol	Test Conditions	Min.	Typ. [1]	Max.	Unit
NOMINAL PERFORMANCE						
Optimized Accuracy Range	I_{PR}		–65	–	65	A
Sensitivity	Sens		–	20.5	–	mV/A
Zero-Current Output Voltage	$V_{IOUT(Q)}$	I_P = 0 A	–	1.5	–	V
Fast Fault Trip Level	$I_{FF(HIGH)}$	VOC_F = 0.7 × V_{CC}	–	146.3	–	A
	$I_{FF(LOW)}$	VOC_F = 0.38 × V_{CC}	–	81.3	–	A
Slow Fault Trip Level	$I_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}	–	81.3	–	A
	$I_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}	–	32.5	–	A
TOTAL OUTPUT ERROR COMPONENTS [2] $E_{TOT}(I_P) = \{[V_{IOUT\_ideal}(I_P) - V_{IOUT}(I_P)] / [Sens_{ideal}(I_P) \times I_P]\} \times 100$ (%)						
Total Output Error [3]	E_{TOT}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.6	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.5	4	%
Sensitivity Error	E_{SENS}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.5	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.5	4	%
Offset Voltage	V_{OE}	I_P = 0 A, T_A = 25°C to 125°C	–10	±3.5	10	mV
		I_P = 0 A, T_A = –40°C to 25°C	–30	±9	30	mV
OVERCURRENT FAULT PERFORMANCE						
Fast Fault Error	$E_{FF(HIGH)+}$	VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±15	–	%
	$E_{FF(HIGH)–}$	VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±15	–	%
	$E_{FF(LOW)+}$	VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = 25°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±25	–	%
	$E_{FF(LOW)-}$	VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = 25°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±30	–	%
Slow Fault Error	$E_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}, I_P rising	–6	±3	6	%
	$E_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}, I_P rising	–10	±5	10	%
Fast Fault Delay Code	–			0		–
Slow Fault Delay Code	–			4		–
FAULT CHARACTERISTICS						
Fast Fault Response Time	$t_{R(FF)}$	Time from I_P rising above I_{FF} until $V_{FAULT\_F} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(FAST)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_F to ground	–	1.5	2	µs
Slow Fault Response Time	$t_{R(SF)}$	Time from I_P rising above I_{SF} until $V_{FAULT\_S} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(SLOW)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_S to ground	–	13	–	µs

[1] Typical values with +/- are 3 sigma values.
[2] A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage, as that would violate the maximum/minimum total output error specification. Also, 3 sigma distribution values are combined by taking the square root of the sum of the squares.
[3] Percentage of I_P, with $I_P = I_{PR(max)}$.

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Characteristic	Symbol	Test Conditions	Min.	Typ. [1]	Max.	Unit
NOMINAL PERFORMANCE						
Optimized Accuracy Range	I_{PR}		–65	–	65	A
Sensitivity	Sens		–	20.5	–	mV/A
Zero-Current Output Voltage	$V_{IOUT(Q)}$	I_P = 0 A	–	1.5	–	V
Fast Fault Trip Level	$I_{FF(HIGH)}$	VOC_F = 0.7 × V_{CC}	–	146.3	–	A
	$I_{FF(LOW)}$	VOC_F = 0.38 × V_{CC}	–	81.3	–	A
Slow Fault Trip Level	$I_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}	–	81.3	–	A
	$I_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}	–	32.5	–	A
TOTAL OUTPUT ERROR COMPONENTS [2] $E_{TOT}(I_P) = \{[V_{IOUT\_ideal}(I_P) – V_{IOUT}(I_P)] / [Sens_{ideal}(I_P) × I_P]\} × 100$ (%)						
Total Output Error [3]	E_{TOT}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.6	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.5	4	%
Sensitivity Error	E_{SENS}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.5	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.5	4	%
Offset Voltage	V_{OE}	I_P = 0 A, T_A = 25°C to 125°C	–10	±3.5	10	mV
		I_P = 0 A, T_A = –40°C to 25°C	–30	±9	30	mV
OVERCURRENT FAULT PERFORMANCE						
Fast Fault Error	$E_{FF(HIGH)+}$	VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±15	–	%
	$E_{FF(HIGH)–}$	VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±15	–	%
	$E_{FF(LOW)+}$	VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = 25°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±25	–	%
	$E_{FF(LOW)-}$	VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = 25°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±30	–	%
Slow Fault Error	$E_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}, I_P rising	–6	±3	6	%
	$E_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}, I_P rising	–10	±5	10	%
Fast Fault Delay Code	–			4		–
Fast Fault Mask Time	$t_{m(ff)}$		–	1.5	–	μs
Slow Fault Delay Code	–			4		–
FAULT CHARACTERISTICS						
Fast Fault Response Time	$t_{R(FF)}$	Time from I_P rising above I_{FF} until $V_{FAULT\_F} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(FAST)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_F to ground	–	3.5	–	μs
Slow Fault Response Time	$t_{R(SF)}$	Time from I_P rising above I_{SF} until $V_{FAULT\_S} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(SLOW)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_S to ground	–	13	–	μs

[1] Typical values with +/- are 3 sigma values.
[2] A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage, as that would violate the maximum/minimum total output error specification. Also, 3 sigma distribution values are combined by taking the square root of the sum of the squares.
[3] Percentage of I_P, with $I_P = I_{PR(max)}$.

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Characteristic	Symbol	Test Conditions	Min.	Typ. [1]	Max.	Unit
NOMINAL PERFORMANCE						
Optimized Accuracy Range	I_{PR}		–80	–	80	A
Sensitivity	Sens		–	16	–	mV/A
Zero-Current Output Voltage	$V_{IOUT(Q)}$	I_P = 0 A	–	1.5	–	V
Fast Fault Trip Level	$I_{FF(HIGH)}$	VOC_F = 0.7 × V_{CC}	–	180	–	A
	$I_{FF(LOW)}$	VOC_F = 0.38 × V_{CC}	–	100	–	A
Slow Fault Trip Level	$I_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}	–	100	–	A
	$I_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}	–	40	–	A
TOTAL OUTPUT ERROR COMPONENTS [2] $E_{TOT}(I_P) = \{[V_{IOUT\_ideal}(I_P) – V_{IOUT}(I_P)] / [Sens_{ideal}(I_P) \times I_P]\} \times 100$ (%)						
Total Output Error [3]	E_{TOT}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.6	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.5	4	%
Sensitivity Error	E_{SENS}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.5	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.5	4	%
Offset Voltage	V_{OE}	I_P = 0 A, T_A = 25°C to 125°C	–10	±3.5	10	mV
		I_P = 0 A, T_A = –40°C to 25°C	–30	±9	30	mV
OVERCURRENT FAULT PERFORMANCE						
Fast Fault Error	$E_{FF(HIGH)+}$	VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±15	–	%
	$E_{FF(HIGH)–}$	VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±15	–	%
	$E_{FF(LOW)+}$	VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = 25°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±25	–	%
	$E_{FF(LOW)-}$	VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = 25°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±30	–	%
Slow Fault Error	$E_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}, I_P rising	–8	±4	8	%
	$E_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}, I_P rising	–10	±5	10	%
Fast Fault Delay Code	–			0		–
Slow Fault Delay Code	–			4		–
FAULT CHARACTERISTICS						
Fast Fault Response Time	$t_{R(FF)}$	Time from I_P rising above I_{FF} until $V_{FAULT\_F} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(FAST)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_F to ground	–	1.5	2	µs
Slow Fault Response Time	$t_{R(SF)}$	Time from I_P rising above I_{SF} until $V_{FAULT\_S} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(SLOW)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_S to ground	–	13	–	µs

[1] Typical values with +/- are 3 sigma values.
[2] A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage, as that would violate the maximum/minimum total output error specification. Also, 3 sigma distribution values are combined by taking the square root of the sum of the squares.
[3] Percentage of I_P, with $I_P = I_{PR(max)}$.

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Characteristic	Symbol	Test Conditions	Min.	Typ. [1]	Max.	Unit
NOMINAL PERFORMANCE						
Optimized Accuracy Range	I_{PR}		–80	–	80	A
Sensitivity	Sens		–	16	–	mV/A
Zero-Current Output Voltage	$V_{IOUT(Q)}$	I_P = 0 A	–	1.5	–	V
Fast Fault Trip Level	$I_{FF(HIGH)}$	VOC_F = 0.7 × V_{CC}	–	180	–	A
	$I_{FF(LOW)}$	VOC_F = 0.38 × V_{CC}	–	100	–	A
Slow Fault Trip Level	$I_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}	–	100	–	A
	$I_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}	–	40	–	A
TOTAL OUTPUT ERROR COMPONENTS [2] $E_{TOT}(I_P) = \{[V_{IOUT\_ideal}(I_P) - V_{IOUT}(I_P)] / [Sens_{ideal}(I_P) \times I_P]\} \times 100$ (%)						
Total Output Error [3]	E_{TOT}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.6	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.5	4	%
Sensitivity Error	E_{SENS}	$I_P = I_{PR(max)}$, T_A = 25°C to 125°C	–1.5	±0.5	1.5	%
		$I_P = I_{PR(max)}$, T_A = –40°C to 25°C	–4	±1.5	4	%
Offset Voltage	V_{OE}	I_P = 0 A, T_A = 25°C to 125°C	–10	±3.5	10	mV
		I_P = 0 A, T_A = –40°C to 25°C	–30	±9	30	mV
OVERCURRENT FAULT PERFORMANCE						
Fast Fault Error	$E_{FF(HIGH)+}$	VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±15	–	%
	$E_{FF(HIGH)–}$	VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–10	±5	10	%
		VOC_F = 0.7 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±15	–	%
	$E_{FF(LOW)+}$	VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = 25°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = 25°C to 125°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Positive I_P, T_A = –40°C to 25°C	–	±25	–	%
	$E_{FF(LOW)-}$	VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = 25°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = 25°C to 125°C	–20	±15	20	%
		VOC_F = 0.38 × V_{CC}, Negative I_P, T_A = –40°C to 25°C	–	±30	–	%
Slow Fault Error	$E_{SF(HIGH)}$	VOC_S = 0.7 × V_{CC}, I_P rising	–8	±4	8	%
	$E_{SF(LOW)}$	VOC_S = 0.3 × V_{CC}, I_P rising	–10	±5	10	%
Fast Fault Delay Code	–			4		–
Fast Fault Mask Time	$t_{m(ff)}$		–	1.5	–	μs
Slow Fault Delay Code	–			4		–
FAULT CHARACTERISTICS						
Fast Fault Response Time	$t_{R(FF)}$	Time from I_P rising above I_{FF} until $V_{FAULT\_F} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(FAST)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_F to ground	–	3.5	–	μs
Slow Fault Response Time	$t_{R(SF)}$	Time from I_P rising above I_{SF} until $V_{FAULT\_S} < V_{FAULTL}$ for a current step from 0 to 1.2 × $I_{FAULT(SLOW)}$; R_{PU} = 10 kΩ, 100 pF from FAULT_S to ground	–	13	–	μs

[1] Typical values with +/- are 3 sigma values.
[2] A single part will not have both the maximum/minimum sensitivity error and maximum/minimum offset voltage, as that would violate the maximum/minimum total output error specification. Also, 3 sigma distribution values are combined by taking the square root of the sum of the squares.
[3] Percentage of I_P, with $I_P = I_{PR(max)}$.

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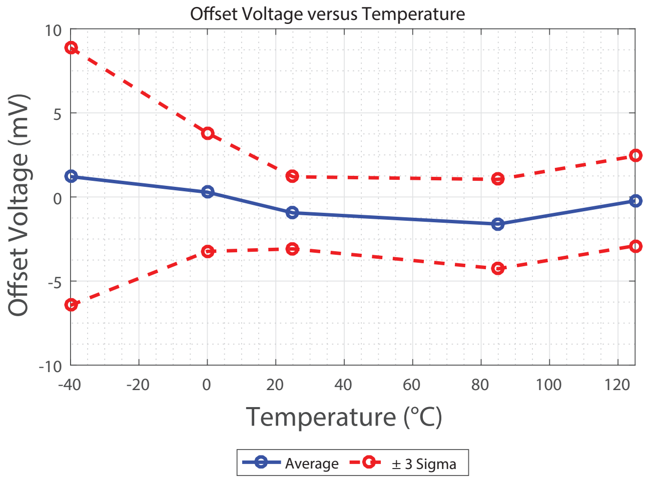

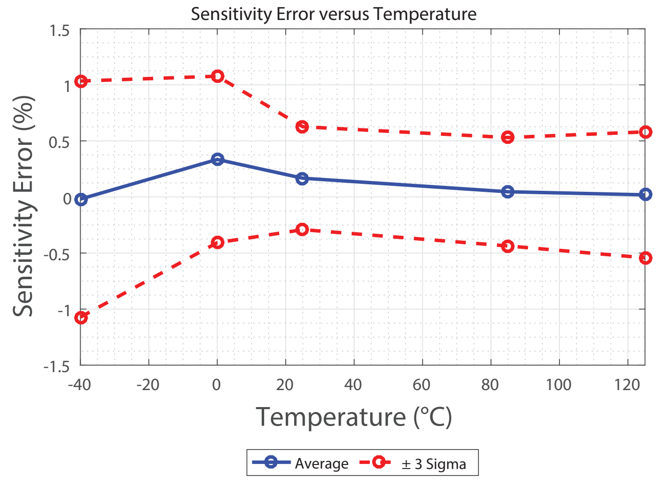

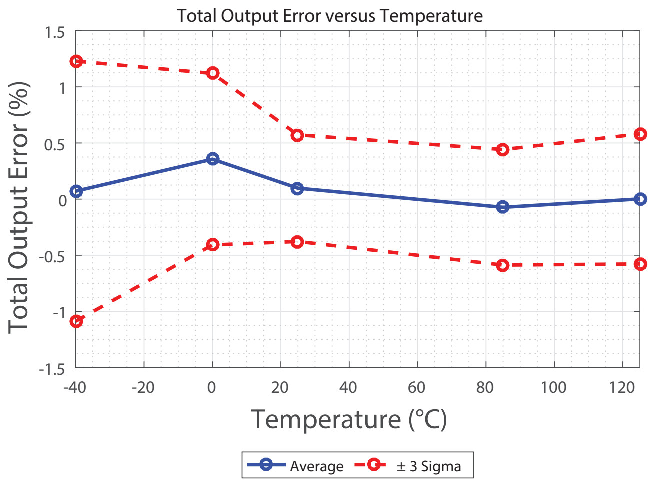

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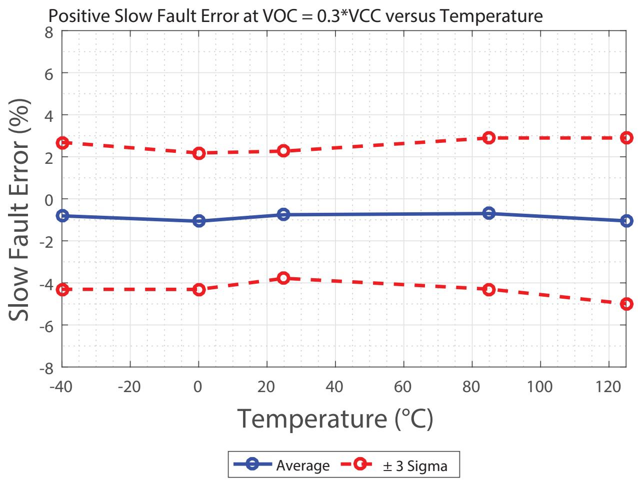

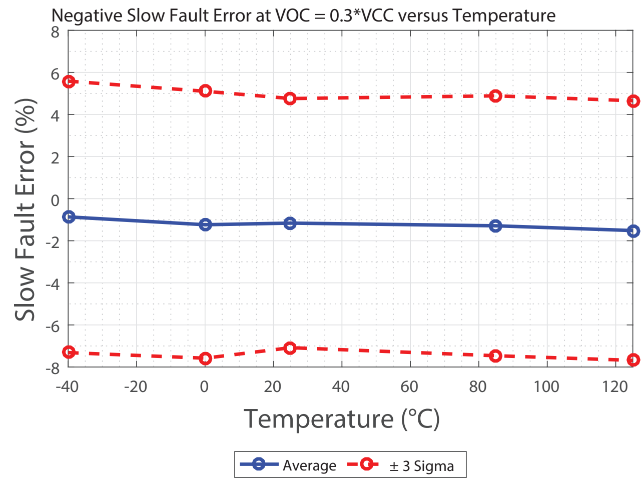

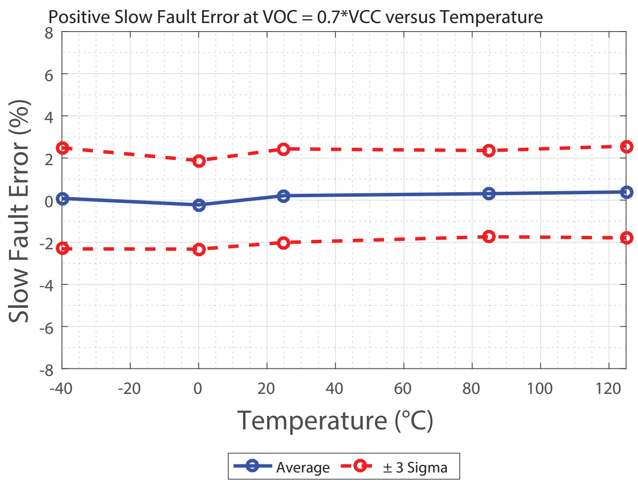

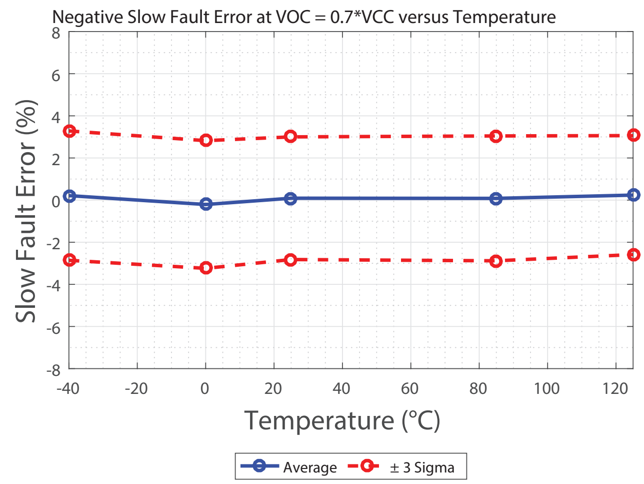

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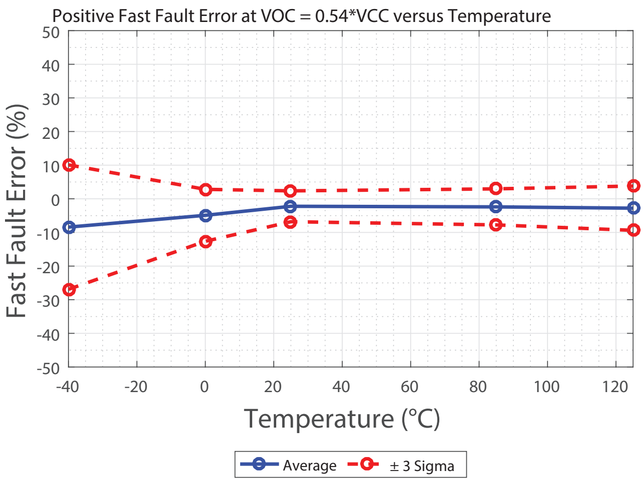

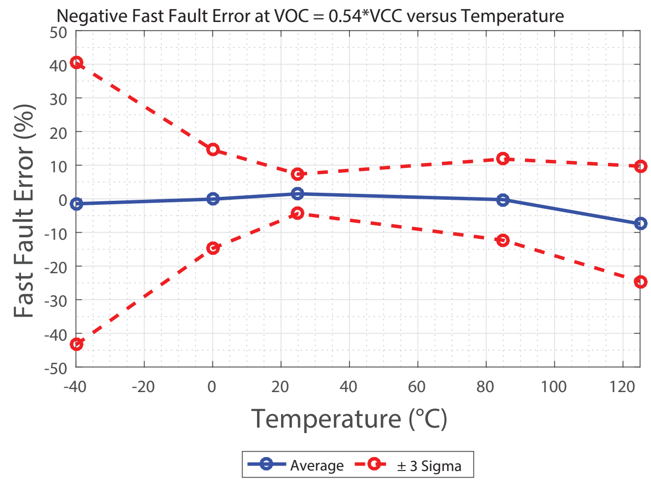

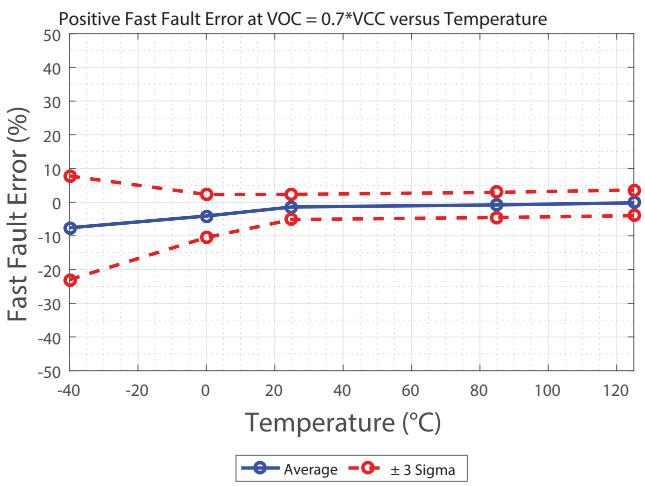

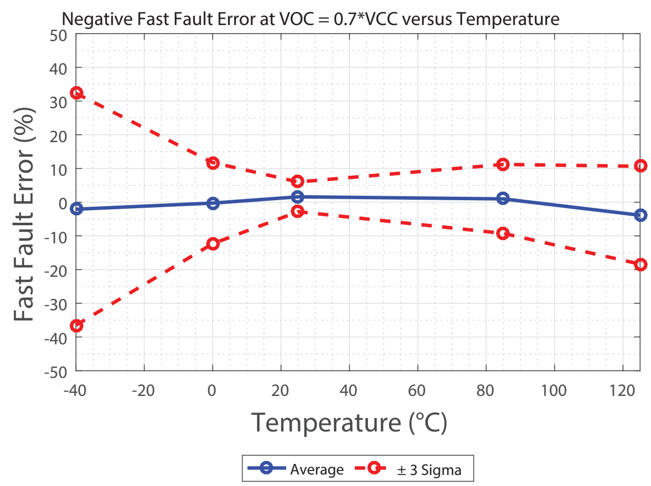

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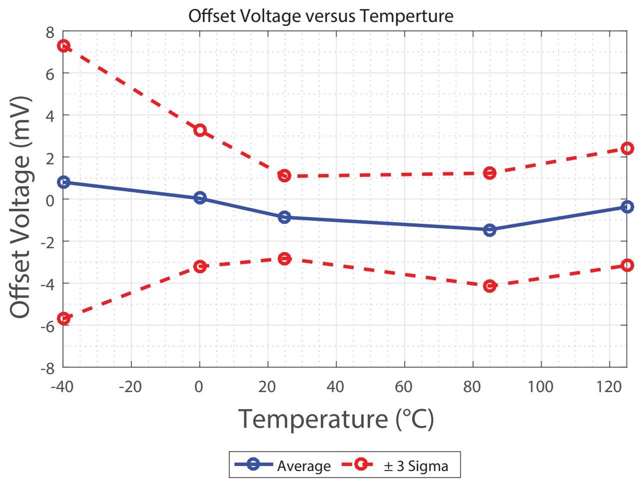

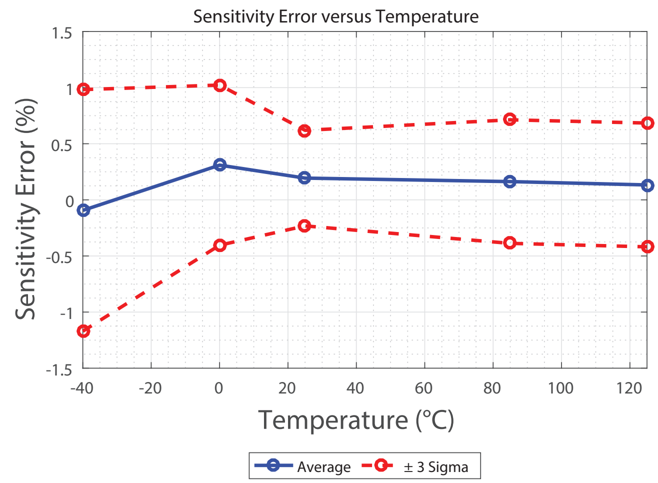

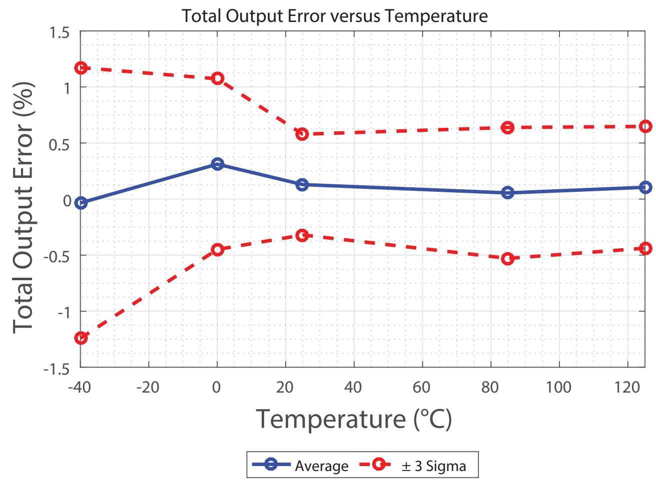

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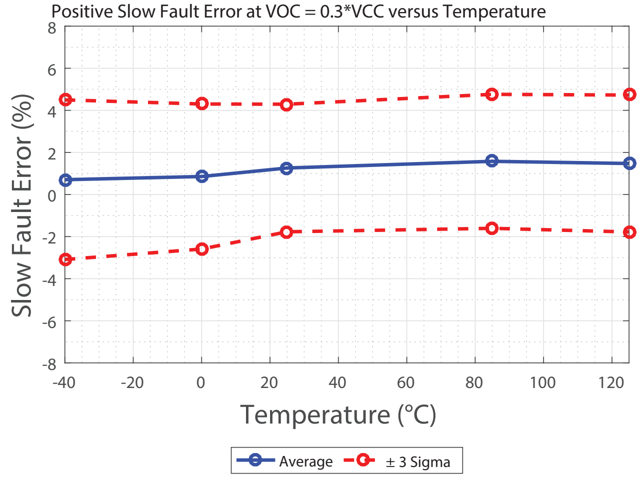
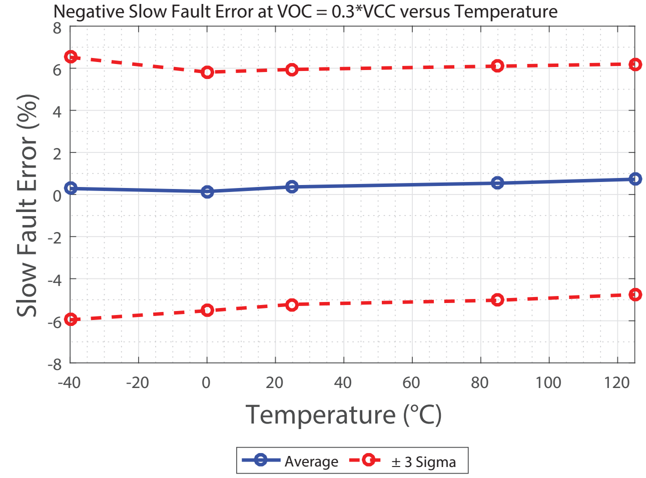
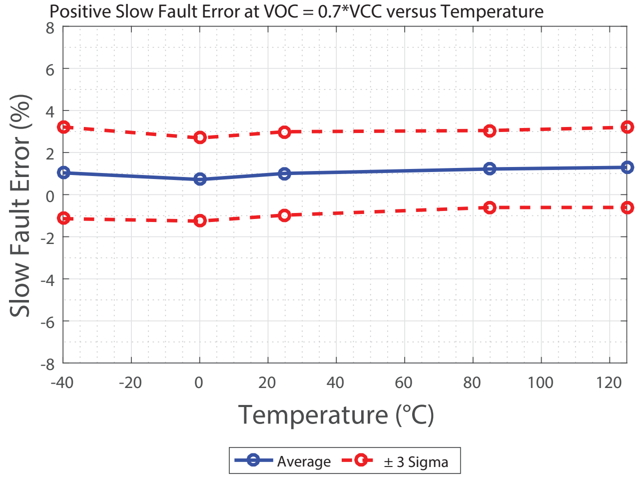
Positive Slow Fault Error at VOC = 0.3*VCC versus Temperature



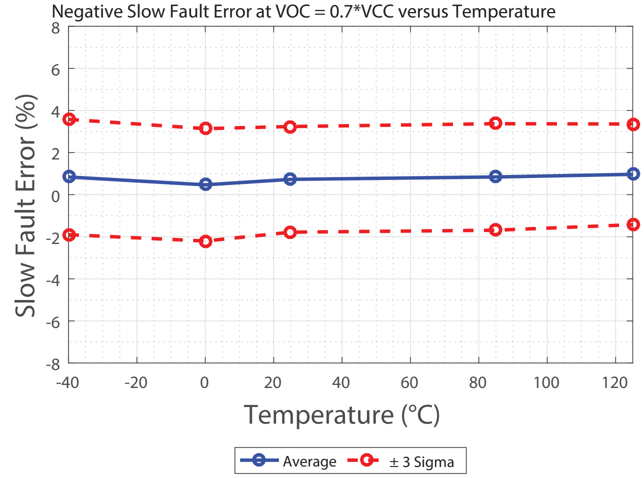
Negative Slow Fault Error at VOC = 0.3*VCC versus Temperature



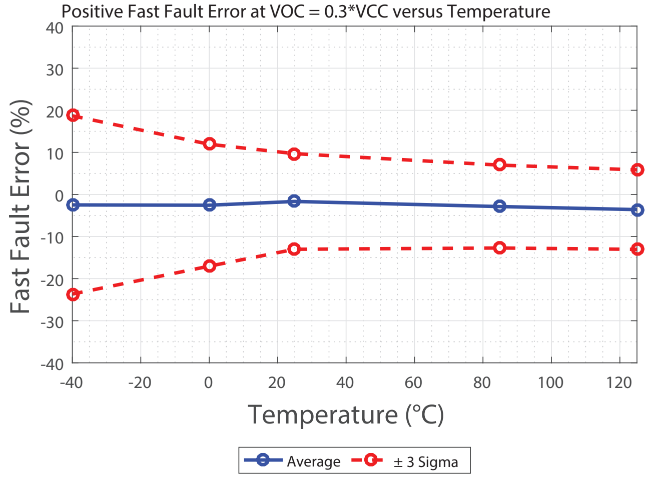
Positive Slow Fault Error at VOC = 0.7*VCC versus Temperature



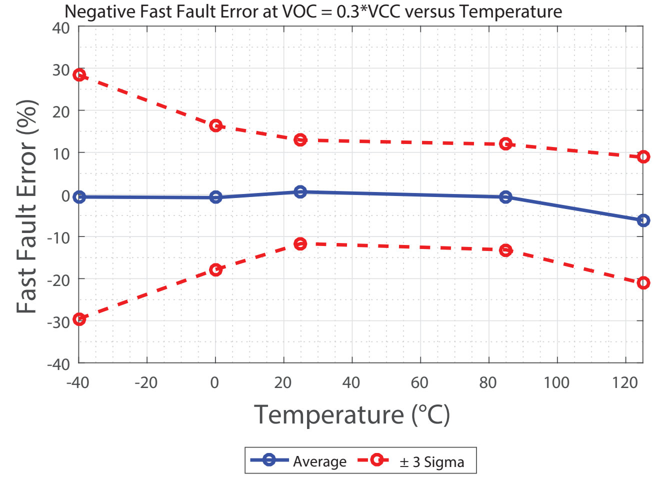
Negative Slow Fault Error at VOC = 0.7*VCC versus Temperature



Positive Fast Fault Error at VOC = 0.3*VCC versus Temperature



Negative Fast Fault Error at VOC = 0.3*VCC versus Temperature



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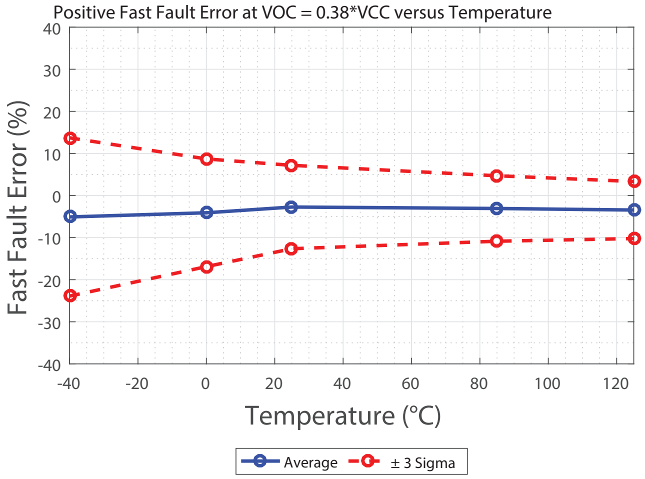

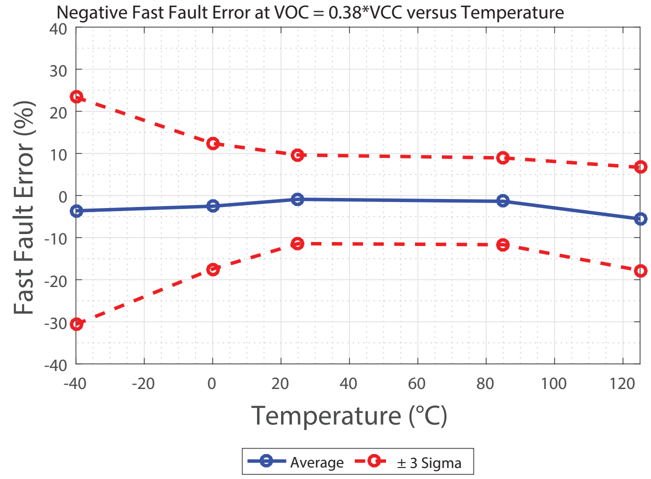

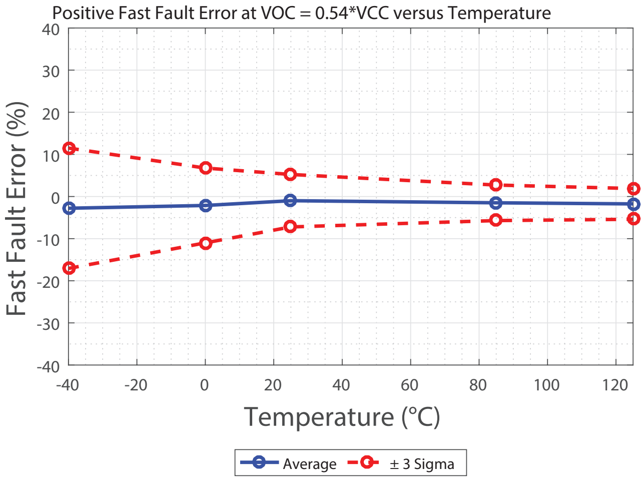

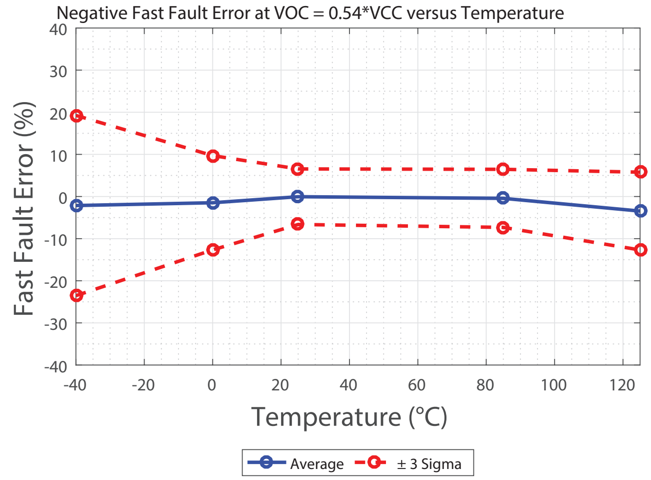

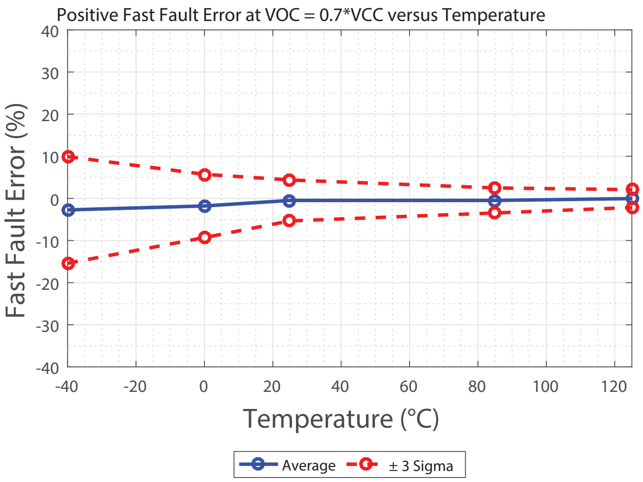

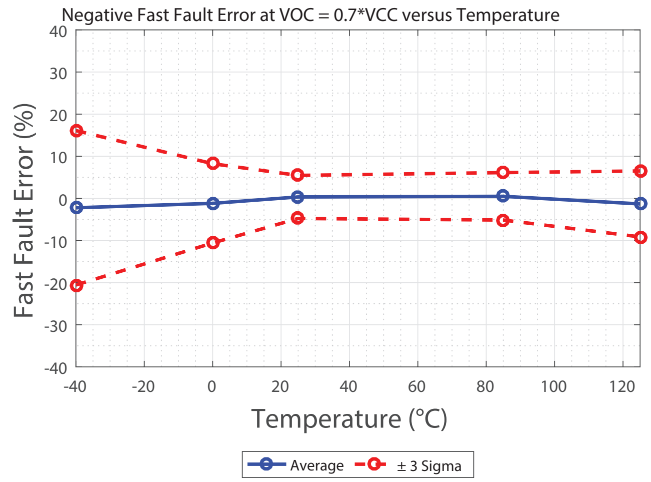

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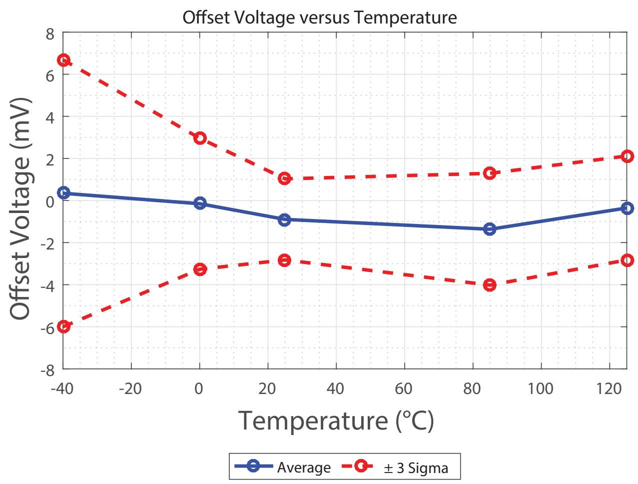

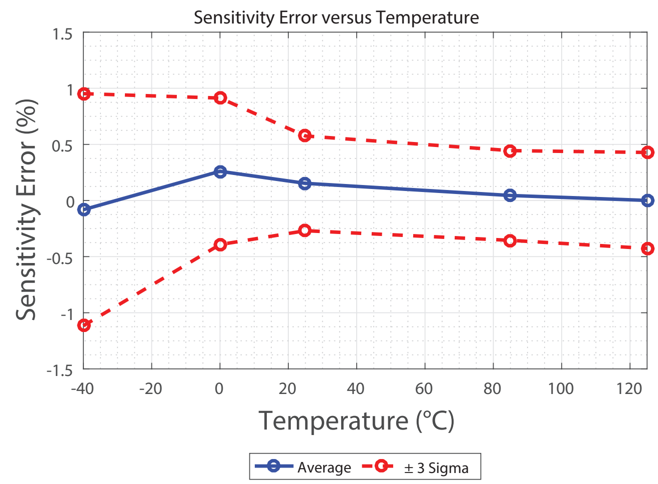

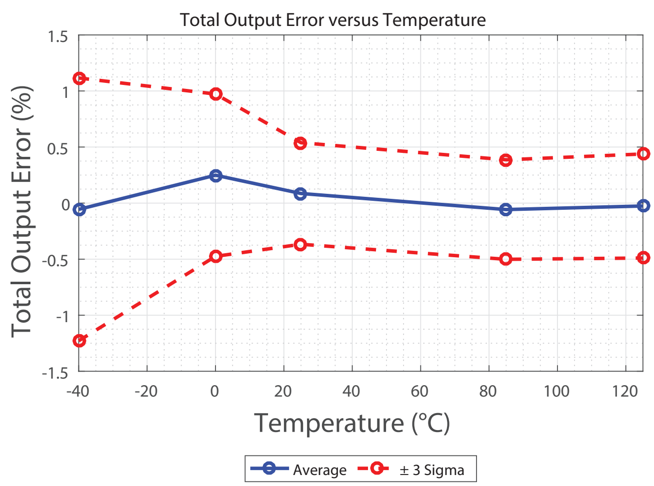

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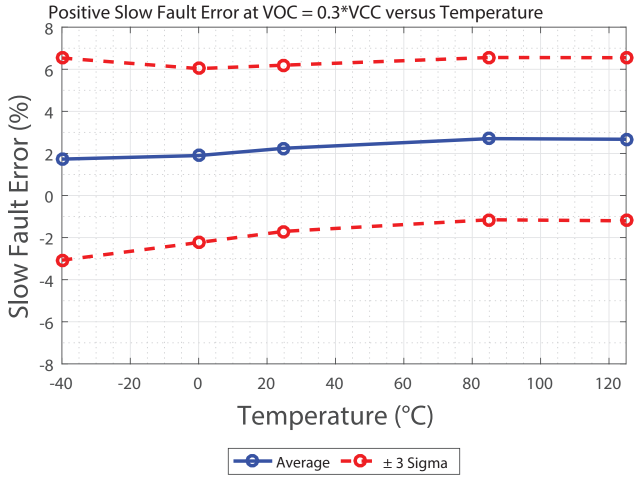

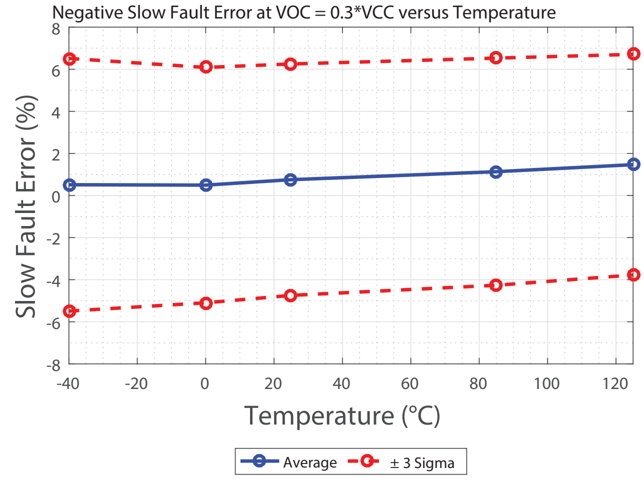

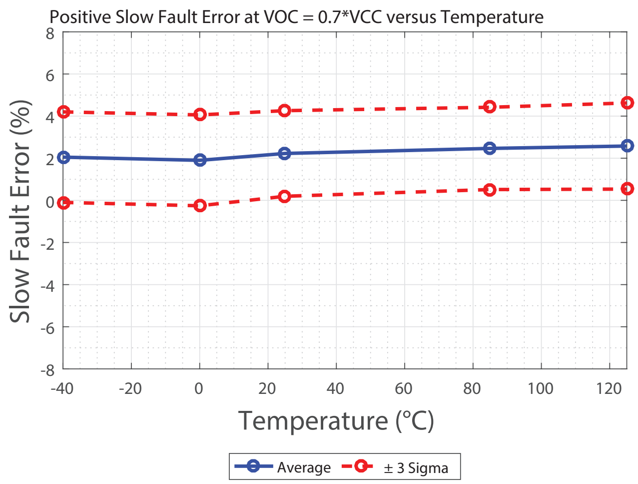

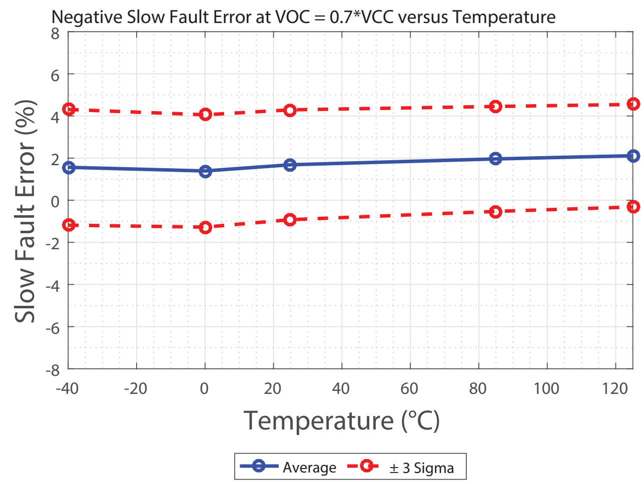

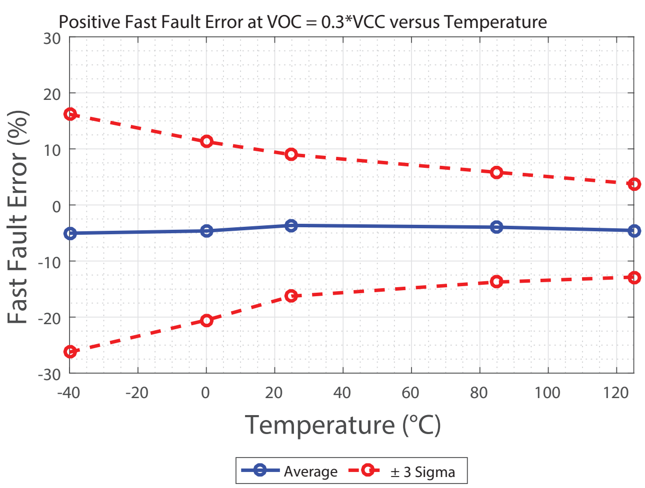

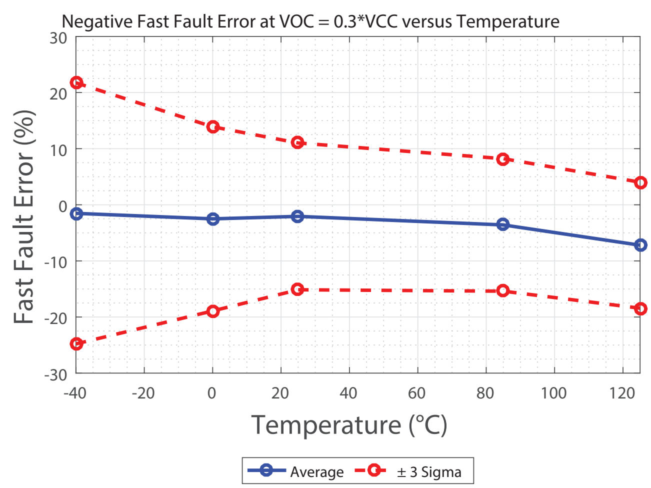

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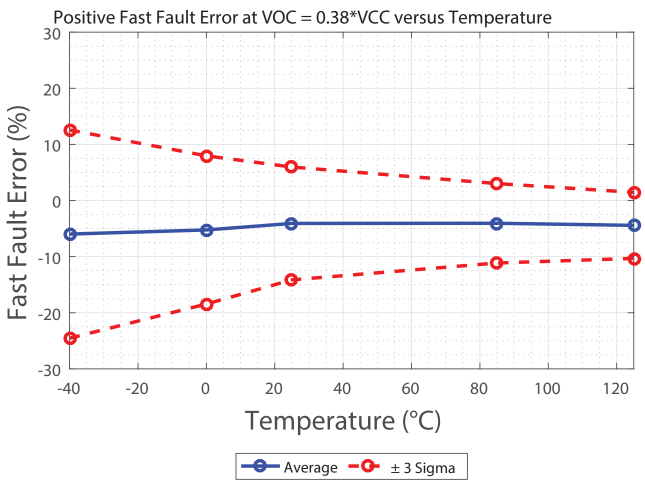

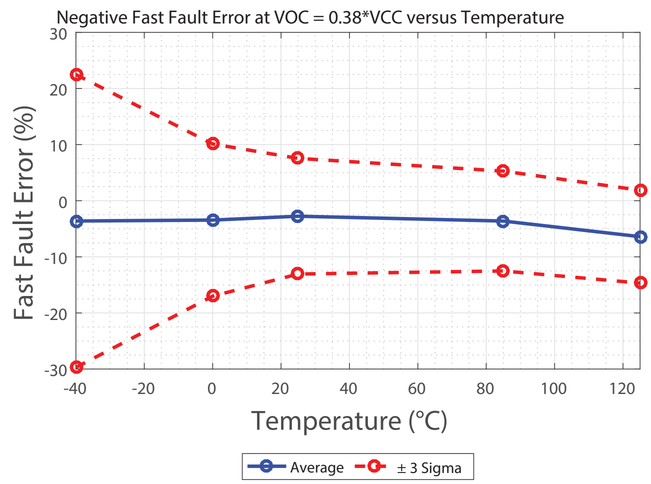

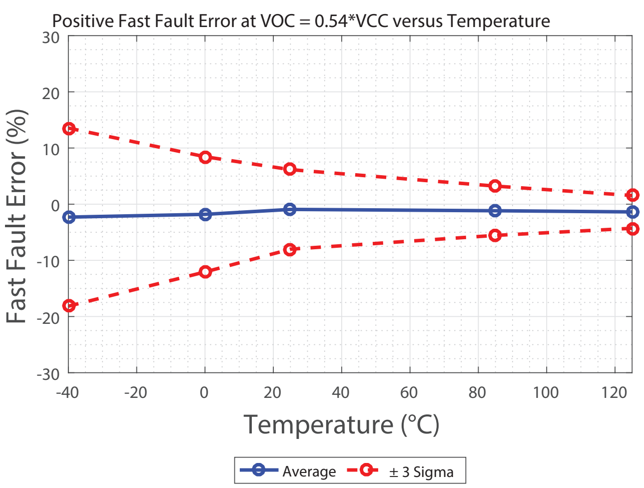

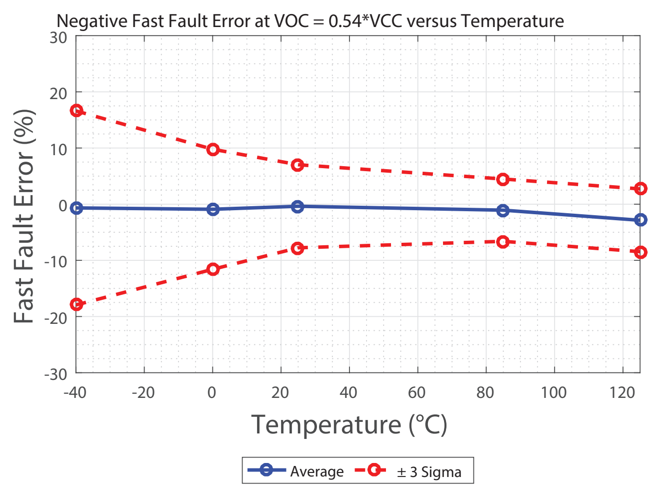

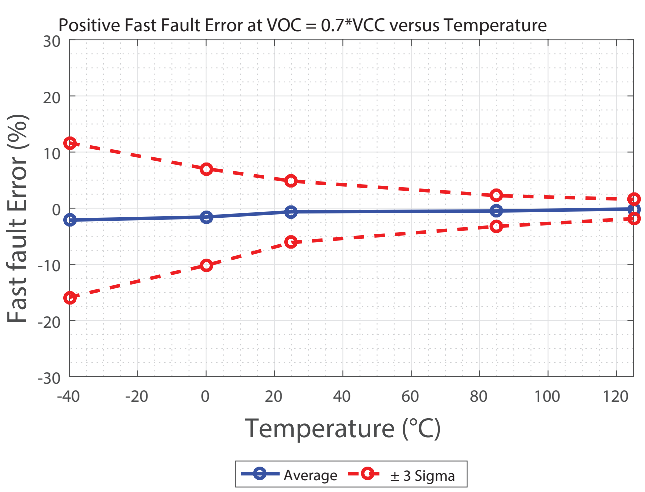

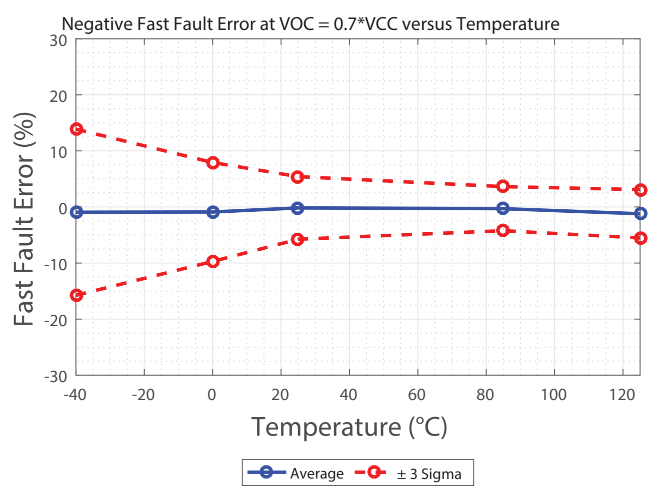

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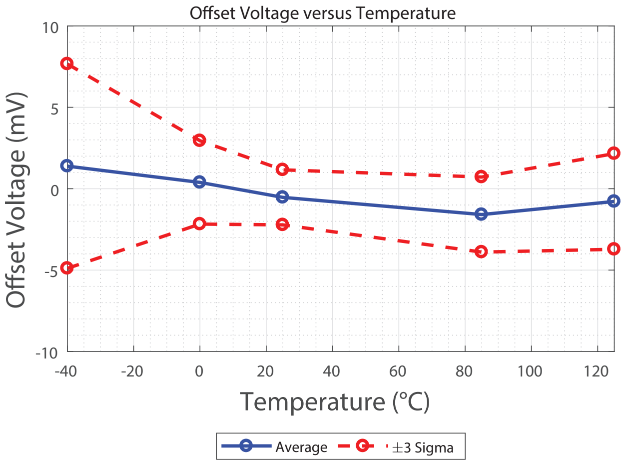

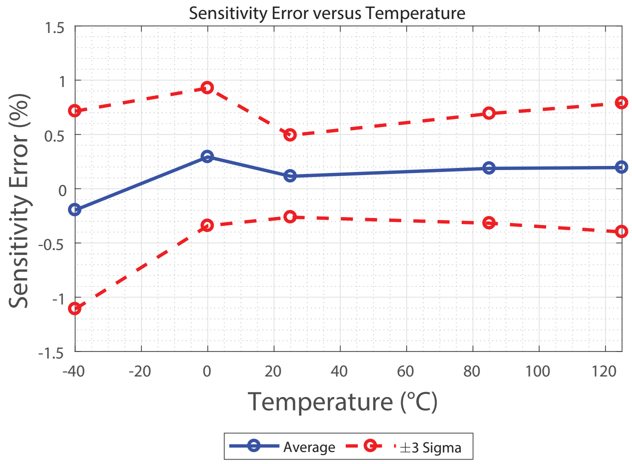

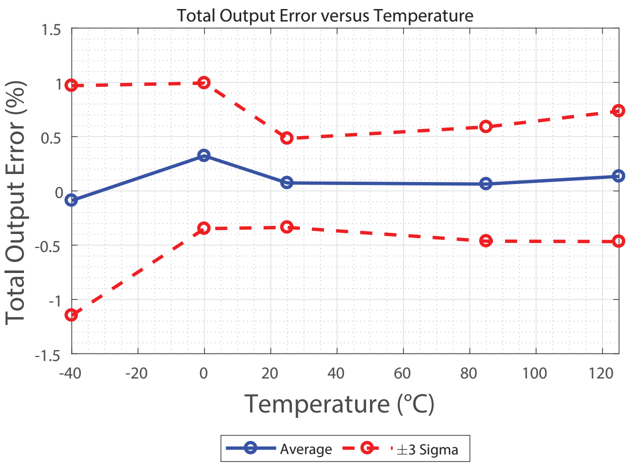

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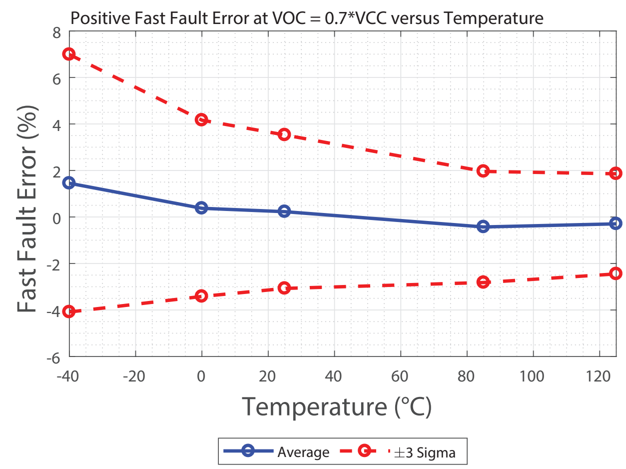

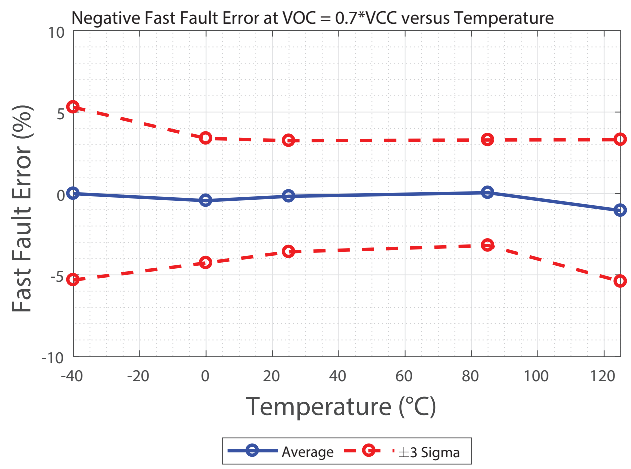

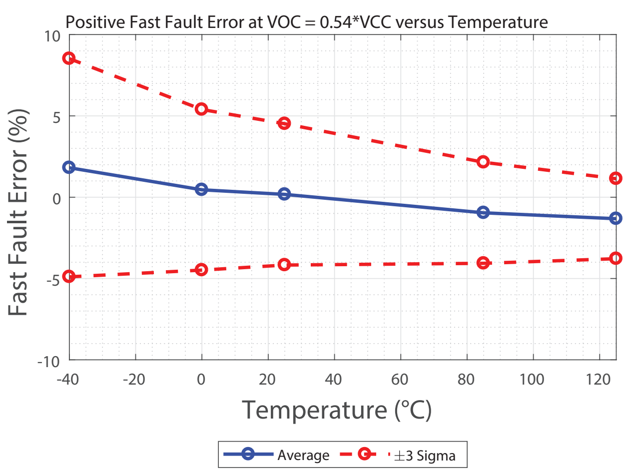

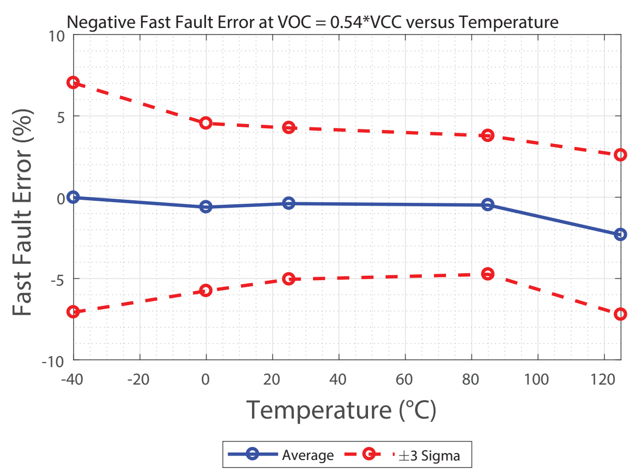

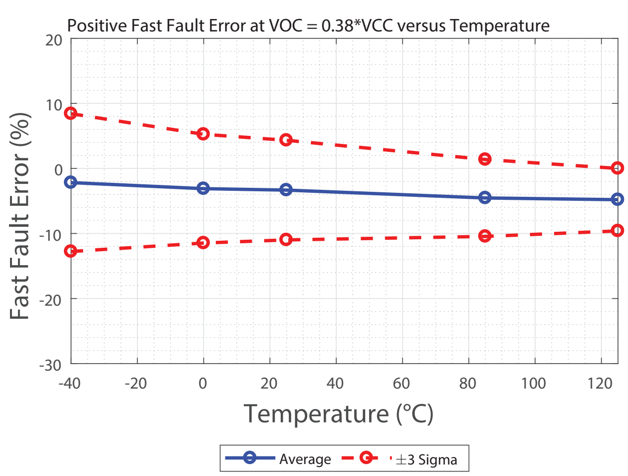

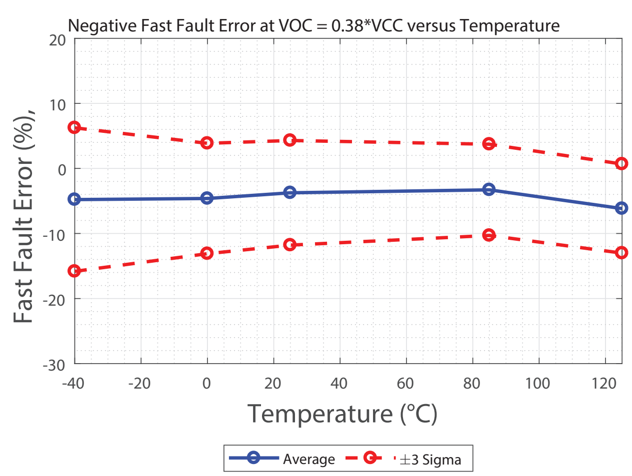

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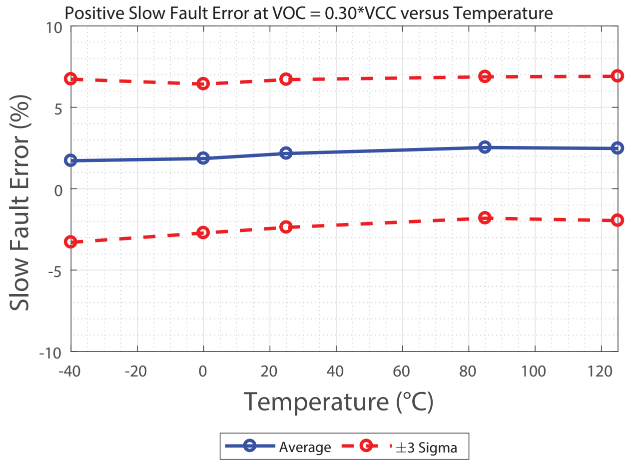

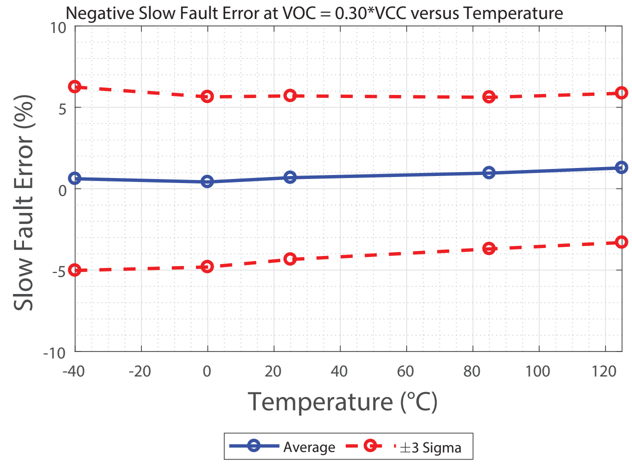

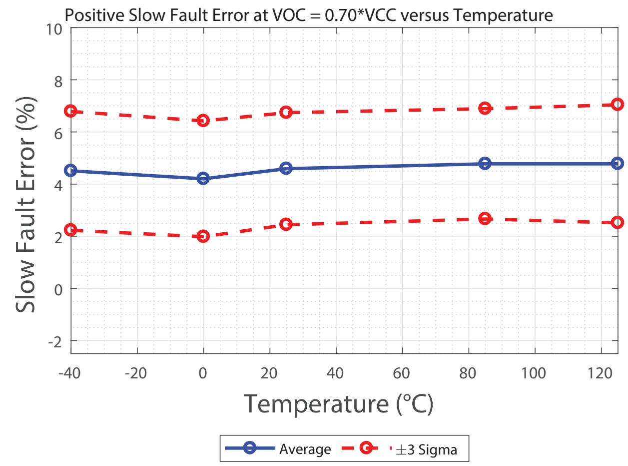

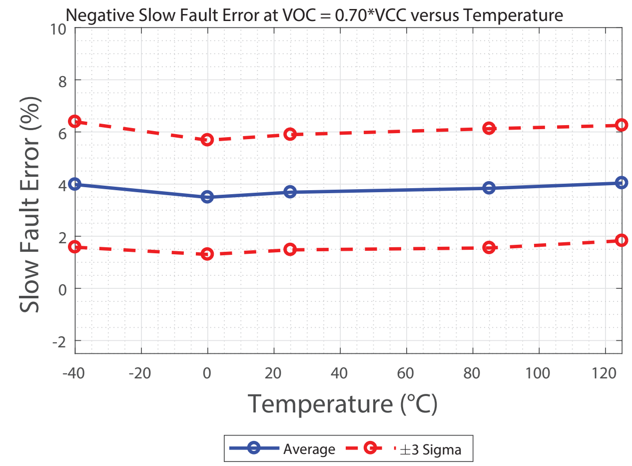

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APPLICATION INFORMATION

Fault Overview

The ACS720 has two customer-settable overcurrent fault comparators which trip when the absolute value of the input current, I_P, goes above the set threshold. The fast fault and slow fault are both active low outputs.

The Fast Fault, FAULT_F, operates early in the signal path, allowing for ultrafast response times with reduced accuracy. The Slow Fault, FAULT_S, operates later in the conditioned section of the signal path, resulting in higher accuracy. The Fast Fault feature is well suited for detecting gross short-circuit events, while the slow fault may be used to detect overload conditions, such as those found in motor applications.

The accuracy and response times for FAULT_F and FAULT_S may be found in the device performance tables of this datasheet.

Setting Fast and Slow Fault Thresholds

The fault thresholds are user-settable, using the VOC_F and VOC_S pins for the fast and slow fault trip points, respectively. The fault thresholds may be set using a resistor divider on the VOC_F and VOC_S pins. The VOC_F and VOC_S pins are ratiometric to V_{CC} and have an acceptable input range of $0.3 \times V_{CC}$ to $0.7 \times V_{CC}$. Figure 3 illustrates the linear relationship between I_{FAULT} and the V_{OC} voltages. Refer to the performance characteristics tables for factory-tested fault trip points.

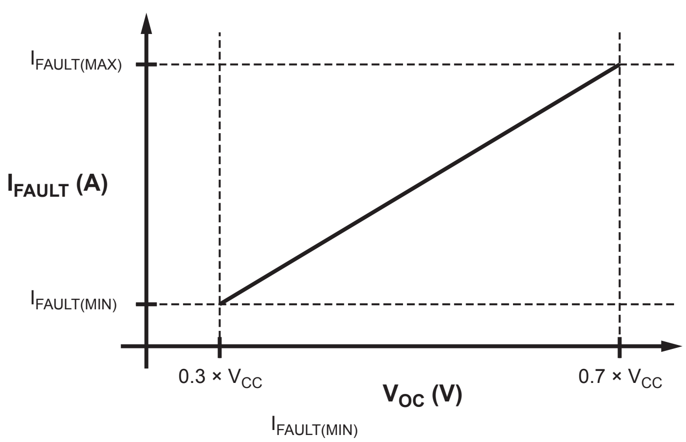

Figure 3: I_{FAULT} versus V_{OC}

The resulting equation for the slow fault threshold is:

$$I_{FAULT(S)} = \frac{V_{OC(S)} - 0.3 \times V_{CC}}{0.4 \times V_{CC}} \times (0.75 \times I_{PR}) + 0.5 \times I_{PR} \tag{1}$$

This may be inverted to solve for the VOC_S voltage relating to the desired fault threshold:

$$V_{OC(S)} = \frac{(I_{FAULT(S)} - 0.5 \times I_{PR}) \times 0.4 \times V_{CC}}{0.75 \times I_{PR}} + (0.3 \times V_{CC}) \tag{2}$$

The resulting equation for the fast fault threshold is:

$$I_{FAULT(F)} = \frac{V_{OC(F)} - 0.3 \times V_{CC}}{0.4 \times V_{CC}} \times (1.25 \times I_{PR}) + 1.0 \times I_{PR} \tag{3}$$

This may be inverted to solve for the VOC_F voltage relating to the desired fault threshold:

$$V_{OC(F)} = \frac{(I_{FAULT(F)} - 1.0 \times I_{PR}) \times 0.4 \times V_{CC}}{1.25 \times I_{PR}} + (0.3 \times V_{CC}) \tag{4}$$

The V_{CC} voltage serves as a reference to VOC pins making the adjustable fault threshold immune to changes in V_{CC}. The VOC pins are sampled at 62.5 kHz; therefore, it is best practice to filter the input to VOC pins below 31 kHz to avoid aliasing. The application schematic for the VOC pins and anti-aliasing capacitor is shown in Figure 4.

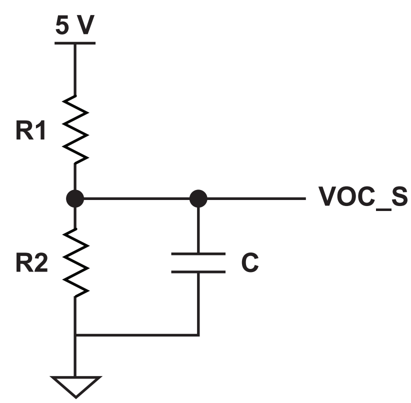

Figure 4: Resistor Divider

The capacitor, C, may be sized using the following equation:

$$f = \frac{1}{2\pi \times (R1 \| R2) \times C} = \frac{1}{2\pi \times \left(\frac{R1 \times R2}{R1 + R2}\right) \times C} \tag{5}$$

The VOC update rate is 7.8 kHz, allowing for eight samples to be averaged each update.

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Fault Response Time and Hysteresis

The Fault Response Time, $t_{R(F)}$, is defined from I_P rising above the fault threshold, I_{FAULT}, until the fault pin voltage falls below V_{FAULTL}, and is based on an input current step from 0 A to 1.2 × I_{FAULT}. This definition is applicable to both fast and slow fault circuits. When the current through I_P crosses the I_{FAULT} threshold, the fault comparator will trip, and after $t_{R(F)}$, the fault pin will assert. When the input current level drops below $I_{FAULT} – I_{HYS}$, the fault comparator will clear, and after $t_{C(F)}$, the fault pin will clear, as indicated in Figure 5.

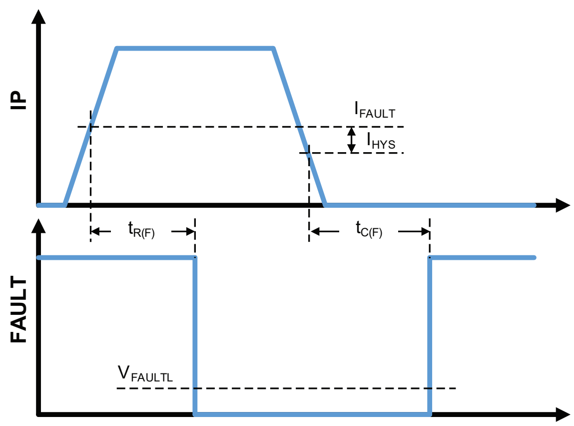

Figure 5: Fault Response Timing Diagram

Fault Masking and Nuisance Pulses

Certain ACS720 part numbers are programmed to report overcurrent events immediately and are factory-programmed with an ultrafast response time. This behavior is illustrated in the timing diagram in Figure 6. Note that fault response and fault clear times, $t_{R(F)}$ and $t_{C(F)}$, still apply.

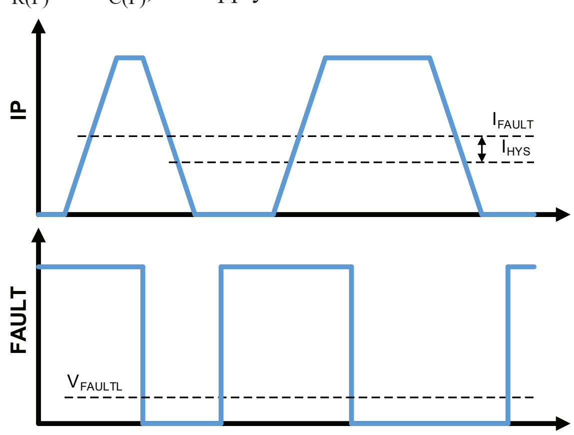

Figure 6: Non-Masked Nuisance Timing Diagram

Conversely, other ACS720 part numbers are factory-programmed with a mask time, t_{MASK}, which enables the device to ignore nuisance current pulses in application. This behavior is illustrated in Figure 7, where the width of the first pulse is less than t_{MASK} and the fault is not reported. Note that response and clear times, $t_{R(F)}$ and $t_{C(F)}$, still apply.

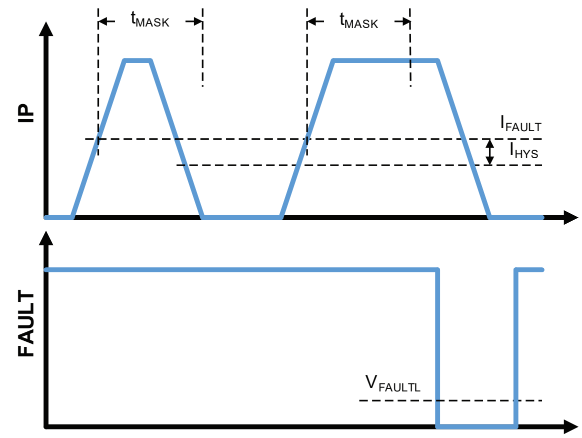

Figure 7: Masked Nuisance Timing Diagram

Due to the chopped and sampled nature of the ACS720 system, it is possible for repetitive high-frequency nuisance pulses to be interpreted as a single continuous overcurrent event. If the blank time, t_B, between pulses is < 4 µs, this may occur.

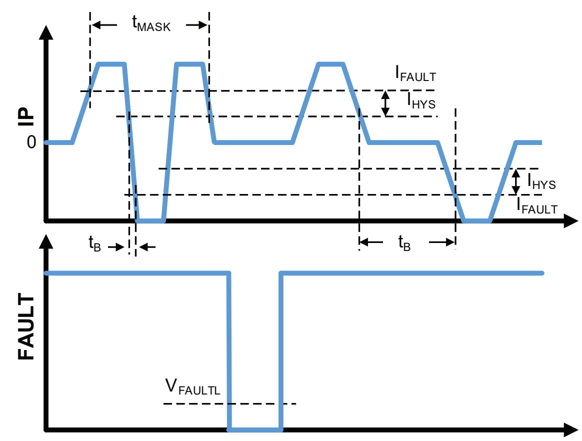

Figure 8: Nuisance Pulse Train Resulting in Fault Assertion

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Thermal Rise vs. Primary Current

Self-heating due to the flow of current should be considered during the design of any current sensing system. The sensor, printed circuit board (PCB), and contacts to the PCB will generate heat as current moves through the system.

The thermal response is highly dependent on PCB layout, copper thickness, cooling techniques, and the profile of the injected current. The current profile includes peak current, current "on-time", and duty cycle. While the data presented in this section was collected with direct current (DC), these numbers may be used to approximate thermal response for both AC signals and current pulses.

The plot in Figure 9 shows the measured rise in steady-state die temperature of the ACS720 versus continuous current at an ambient temperature, T_A, of 25 °C. The thermal offset curves may be directly applied to other values of T_A. Conversely, Figure 10 shows the maximum continuous current at a given T_A. Surges beyond the maximum current listed in Figure 10 are allowed given the maximum junction temperature, $T_{J(MAX)}$ (165°C), is not exceeded.

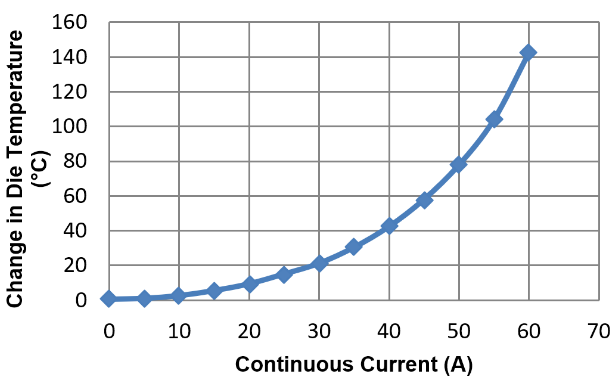

Figure 9: Self Heating in the LA Package Due to Current Flow

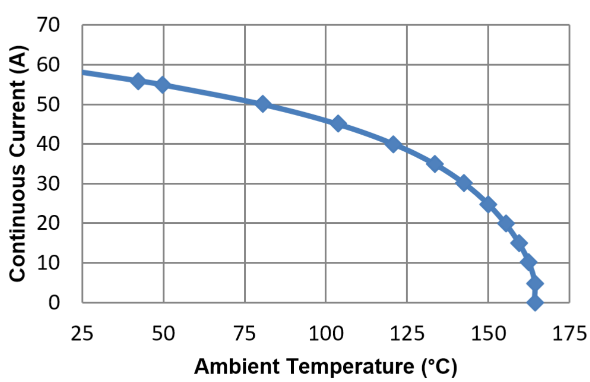

Figure 10: Maximum Continuous Current at a Given T_A

The thermal capacity of the ACS720 should be verified by the end user in the application's specific conditions. The maximum junction temperature, $T_{J(MAX)}$ (165°C), should not be exceeded. Further information on this application testing is available in the DC and Transient Current Capability application note on the Allegro website.

ASEK720 Evaluation Board Layout

Thermal data shown in Figure 9 was collected using the ASEK720 Evaluation Board (TED-85-0702-002). This board includes 1500 mm^2 of 2 oz. copper (0.0694 mm) connected to pins 1 through 4, and to pins 5 through 8, on 4 layers with thermal vias connecting the layers. Top and bottom layers of the PCB are shown below in Figure 11.

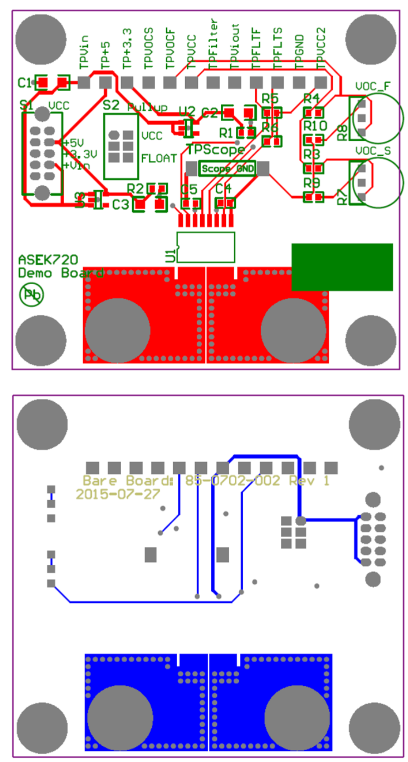

Figure 11: Top and Bottom Layers for ASEK720 Evaluation Board

Gerber files for the ASEK720 evaluation board are available for download from the Allegro website. See the technical documents section of the ACS720 device webpage.

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DEFINITIONS OF ACCURACY CHARACTERISTICS

Sensitivity (Sens). The change in sensor IC output in response to a 1 A change through the primary conductor. The sensitivity is the product of the magnetic circuit sensitivity (G/A) (1 G = 0.1 mT) and the linear IC amplifier gain (mV/G). The linear IC amplifier gain is programmed at the factory to optimize the sensitivity (mV/A) for the full-scale current of the device.

Nonlinearity (E_{LIN}). The nonlinearity is a measure of how linear the output of the sensor IC is over the full current measurement range. The nonlinearity is calculated as:

$$E_{LIN} = \left\{1 - \left[\frac{V_{IOUT}(I_{PR(max)}) - V_{IOUT(Q)}}{2 \times V_{IOUT}(I_{PR(max)}/2) - V_{IOUT(Q)}}\right]\right\} \times 100(\%) \qquad (6)$$

where $V_{IOUT}(I_{PR(max)})$ is the output of the sensor IC with the maximum measurement current flowing through it and $V_{IOUT}(I_{PR(max)}/2)$ is the output of the sensor IC with half of the maximum measurement current flowing through it.

Zero-Current Output Voltage ($V_{IOUT(Q)}$). The output of the sensor when the primary current is zero. $V_{IOUT(Q)}$ is nominally 1.5 V. Variation in $V_{IOUT(Q)}$ can be attributed to the resolution of the Allegro linear IC quiescent voltage trim and thermal drift.

Offset Voltage (V_{OE}). The deviation of the device output from its ideal quiescent value of 1.5 V due to nonmagnetic causes. To convert this voltage to amperes, divide by the device sensitivity, Sens.

Total Output Error (E_{TOT}). The difference between the current measurement from the sensor IC and the actual current (I_P), relative to the actual current. This is equivalent to the difference between the ideal output voltage and the actual output voltage, divided by the ideal sensitivity, relative to the current flowing through the primary conduction path:

$$E_{TOT}(I_P) = \frac{V_{IOUT\_ideal}(I_P) - V_{IOUT}(I_P)}{Sens_{ideal}(I_P) \times I_P} \times 100\ (\%) \qquad (7)$$

The Total Output Error incorporates all sources of error and is a function of I_P. At relatively high currents, E_{TOT} will be mostly due to sensitivity error, and at relatively low currents, E_{TOT} will be mostly due to Offset Voltage (V_{OE}). In fact, at $I_P = 0$, E_{TOT} approaches infinity due to the offset. This is illustrated in Figure 12 and Figure 13. Figure 12 shows a distribution of output voltages versus I_P at 25°C and across temperature. Figure 13 shows the corresponding E_{TOT} versus I_P.

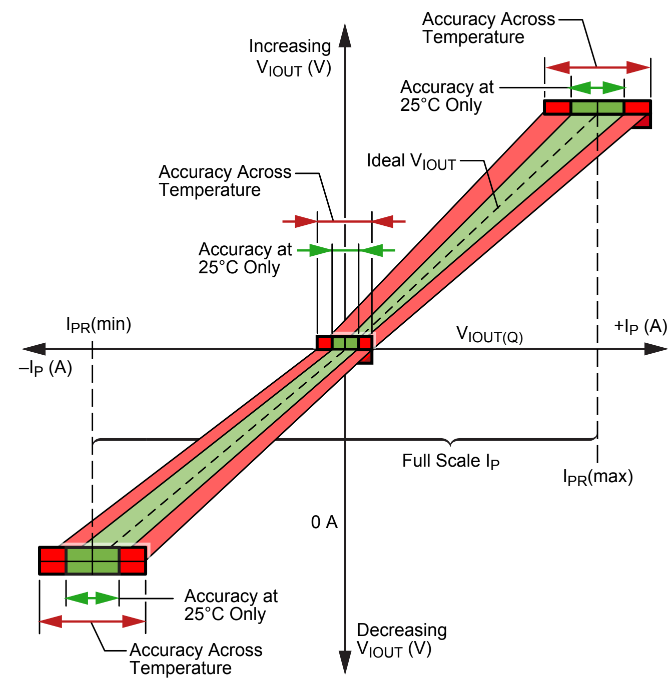

Figure 12: Output Voltage versus Sensed Current

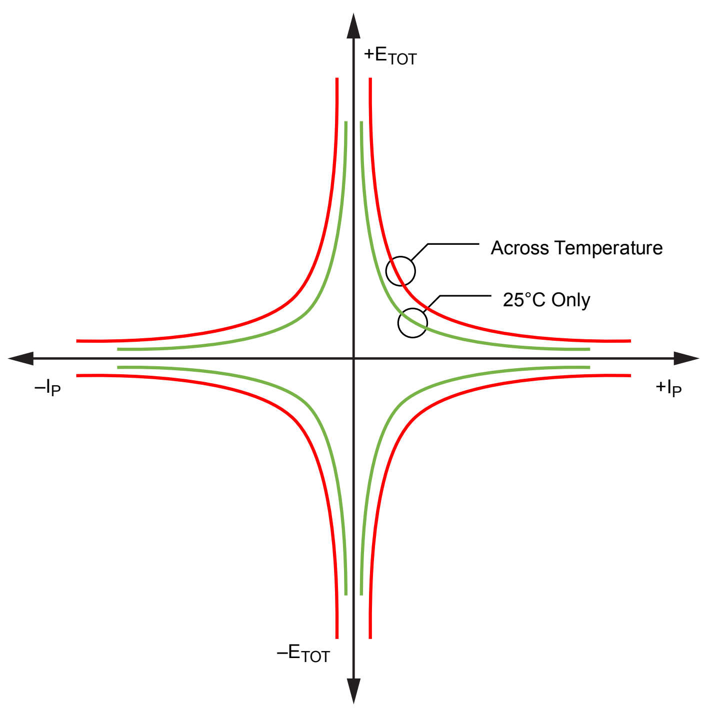

Figure 13: Total Output Error versus Sensed Current

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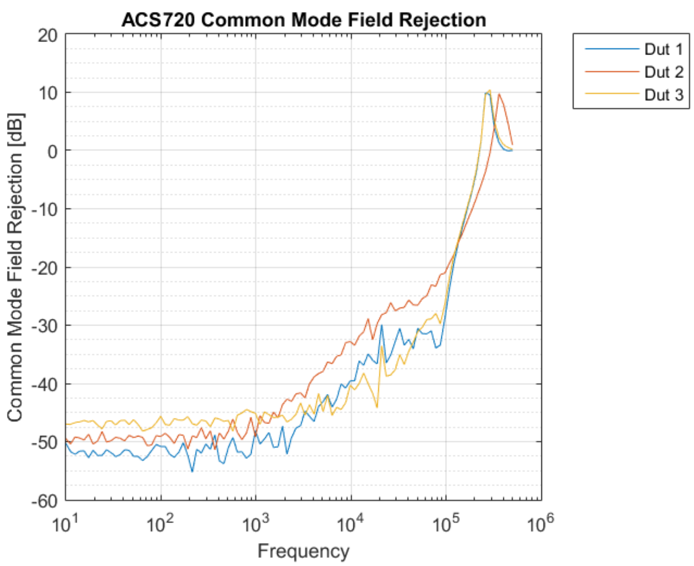
Common Mode Field Rejection

Common Mode Field Rejection (CMFR) measures the ability of the device to reject common-mode magnetic signals. It is defined as the ratio between the voltage swing due to a magnetic field divided by the magnetic field and the gain of the sensor in dB.

$$CMFR = 20\ \log_{10}\left|\frac{A_{CM}}{Sens/CF}\right|$$

where A_{CM} is the gain measured due to an external field in mV/G and CF is the coupling factor of the integrated current loop.

For a sensitivity (Sens) of 50 mV/A, a coupling factor or 12 G/A, a CMFR of –40 dB and a 1 G external field, the output will swing 6 mV.



Power Supply Rejection Ratio

Sensitivity Power Supply Rejection Ratio (PSRRS).

The ratio of the percent change in sensitivity from the sensitivity at nominal supply voltage (V_{CCN}) to the percent change in V_{CC} in dB.

$$PSRR_S = 20\ \log_{10}\left|\frac{[Sens_{Vccn} \times (V_{CC} - V_{CCN})]}{[(Sens_{Vcc} - Sens_{Vccn}) \times V_{CCN}}\right|$$

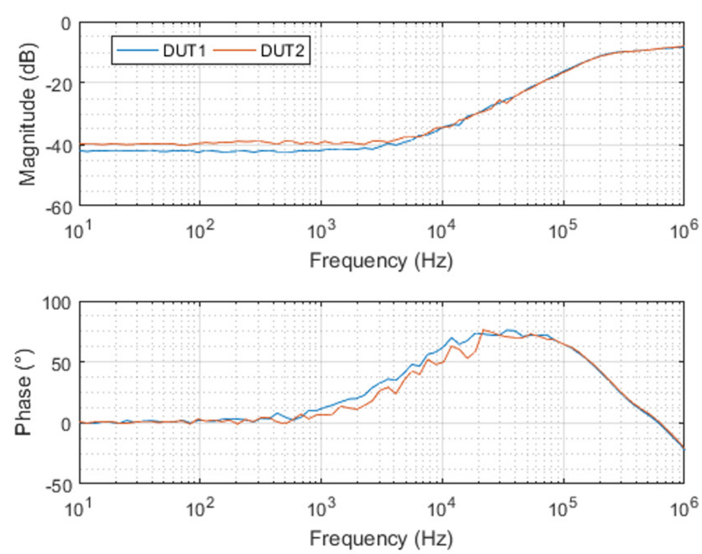
A $PSRR_S$ value of 40 dB means that a 5% change in V_{CC} (going from 5 to 5.25 V, for example) results in around a 0.05% change in sensitivity.

Quiescent Voltage Power Supply Rejection Ratio (PSRRQ).

The ratio of the change in quiescent voltage to the change in V_{CC} in dB.

$$PSRR_Q = 20\ \log_{10}\left|\frac{(\Delta V_{CC})}{(\Delta V_{IOUT(Q)})}\right|$$

A $PSRR_Q$ value of 40 dB means a 250 mV change in V_{CC} (going from 5 to 5.25 V, for example) results in a 2.5 mV change in quiescent voltage.



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DEFINITIONS OF DYNAMIC RESPONSE CHARACTERISTICS

Power-On Time (t_{PO}). When the supply is ramped to its operating voltage, the device requires a finite time to power its internal components before responding to an input magnetic field. Power-On Time, t_{PO} , is defined as the time it takes for the output voltage to settle within ±10% of its steady-state value under an applied magnetic field, after the power supply has reached its minimum specified operating voltage, V_{CC}(min), as shown in the chart at right.

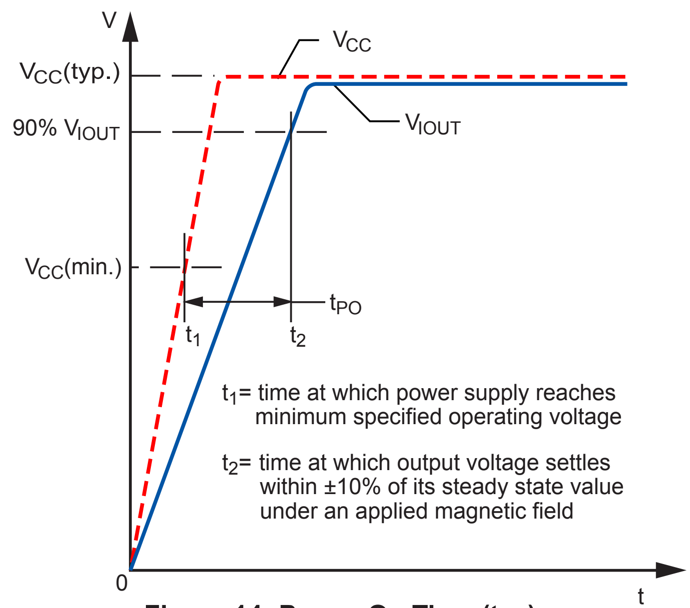

Figure 14: Power-On Time (t_{PO})

Rise Time (t_r). The time interval between a) when the sensor IC reaches 10% of its full-scale value, and b) when it reaches 90% of its full-scale value. The rise time to a step response is used to derive the bandwidth of the current sensor IC, in which $f(-3\text{ dB}) = 0.35/t_r$. Both t_r and $t_{RESPONSE}$ are detrimentally affected by eddy-current losses observed in the conductive IC ground plane.

Propagation Delay (t_{pd}). The propagation delay is measured as the time interval a) when the primary current signal reaches 20% of its final value, and b) when the device reaches 20% of its output corresponding to the applied current.

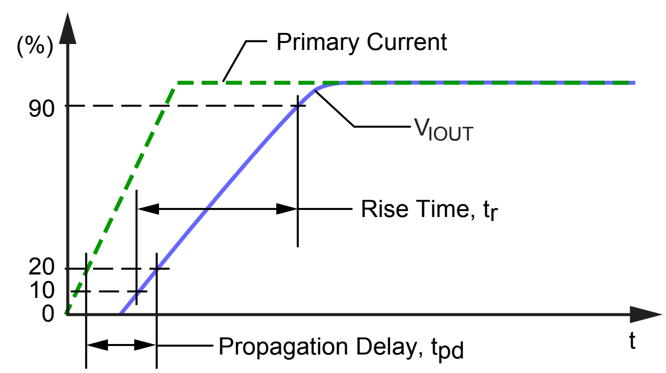

Figure 15: Rise Time (t_r) and Propagation Delay (t_{pd})

Response Time ($t_{RESPONSE}$). The time interval between a) when the primary current signal reaches 90% of its final value, and b) when the device reaches 90% of its output corresponding to the applied current.

Fault Response Time (t_{RFF}, t_{RSF}). The time interval between a) when the primary current signal reaches the fault threshold, and b) when the device fault pin reacts to the current event. A current of 20% above the fault trip level should be used to guarantee fault timing.

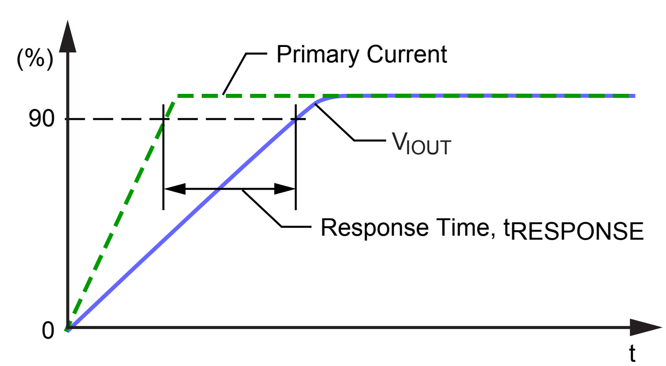

Figure 16: Response Time ($t_{RESPONSE}$)

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RECOMMENDED PCB LAYOUT

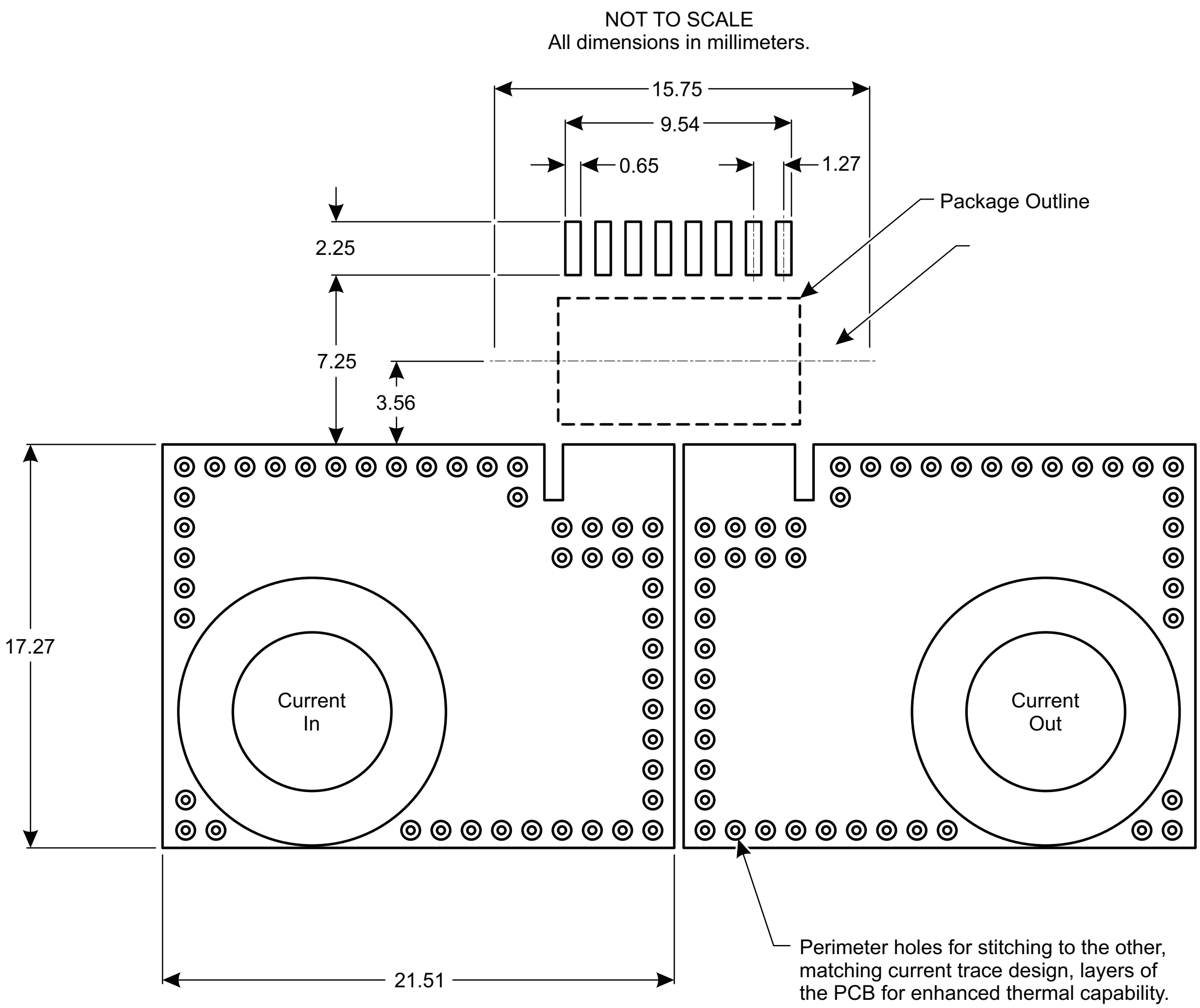

Figure 17: High Isolation PCB Layout

For additional information on layout, see:

https://www.allegromicro.com/en/insights-and-innovations/technical-documents/hall-effect-sensor-ic-publications/techniques-minimize-common-mode-field-interference

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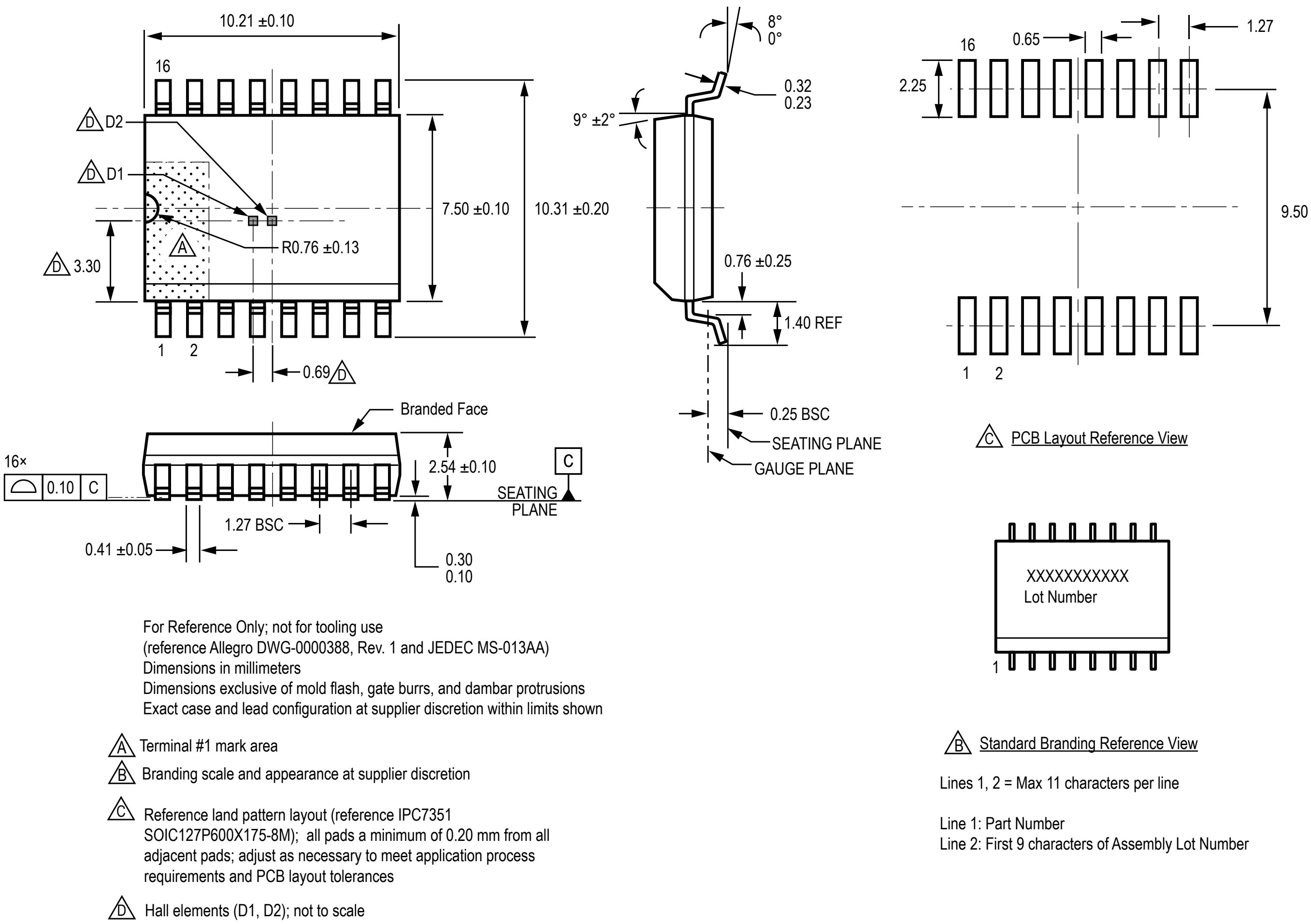

Figure 18: Package LA, 16-pin SOICW

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Revision History

Number	Date	Description
–	August 30, 2017	Initial release
1	November 13, 2017	Corrected typo in Dielectric Surge Strength Test Voltage notes of Isolation Characteristics table (p. 3)
2	December 12, 2017	Corrected branding information (p. 25)
3	April 2, 2018	Added ACS720KLATR-80AB-T part variant.
4	May 3, 2018	Moved Fault Timing Characteristics from Common Operating Characteristics table to Performance Characteristics tables (page 5-9); Updated Fault Application Information (pages 10-11)
5	June 20, 2018	Added Common Mode Field Rejection Ratio characteristic to Common Operating Characteristics table (page 4)
6	July 3, 2018	Added “Thermal Rise vs. Primary Current” and “ASEK720 Evaluation Board Layout” to the Applications Information section (page 25); minor editorial updates.
7	November 16, 2018	Added -4 part variants (pages 2, 8, 10, 12, 14).
8	December 7, 2018	Updated UL certificate number
9	August 23, 2019	Added Maximum Continuous Current to Absolute Maximum Ratings table (page 3), ESD ratings table (page 3), and updated thermal data section (page 29)
10	August 19, 2022	Updated package drawing (page 34)
11	August 26, 2024	Fixed broken link (page 33)

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